

PART NUMBERING GUIDE

Environmental/Mechanical Specifications on page 60

ODC 100 48 A - 14.000MHz

Package

- ODC = 3 Pin / 5.0Vdc / HCMOS-TTL
- ODC3 = 3 Pin / 3.3Vdc / HCMOS-TTL
- ODCSM = 3 Pin / 5.0Vdc / HCMOS-TTL / SMD
- ODC3SM = 3 Pin / 3.3Vdc / HCMOS-TTL / SMD

Inclusive Stability
100= +/-100ppm, 50= +/-50ppm, 25= +/-25ppm

Output Symmetry
Blank = 40/60%, A = 45/55%

Operating Temperature Range
Blank = 0°C to 70°C, 27 = -20°C to 70°C, 48 = -40°C to 85°C

ELECTRICAL SPECIFICATIONS

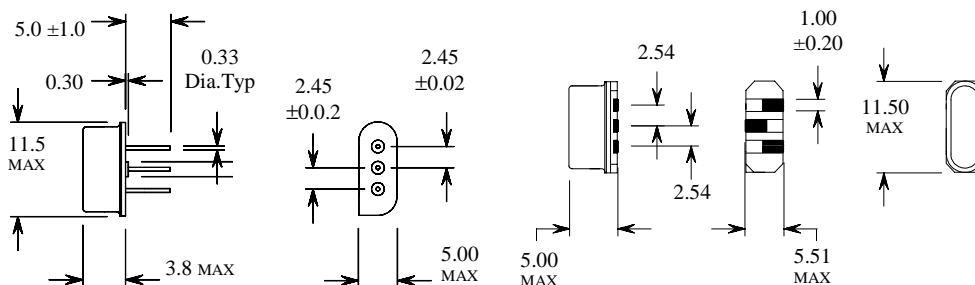
Frequency Range		250kHz to 80.000MHz
Operating Temperature Range		0°C to 70°C / -20°C to 70°C / -40°C to 85°C
Storage Temperature Range		-55°C to 125°C
Supply Voltage		5.0Vdc ±10%, 3.3Vdc ±10%
Input Current	250.000kHz to 24.000MHz 24.001MHz to 50.000MHz 50.001MHz to 66.667MHz 66.668MHz to 80.000MHz	30mA Maximum 45mA Maximum 60mA Maximum 80mA Maximum
Frequency Tolerance / Stability	Inclusive of Operating Temperature Range, Supply Voltage and Load	±100ppm, ±50ppm, ±25ppm
Output Voltage Logic High (Voh)	w/TTL Load w/HCMOS Load	2.4Vdc Minimum Vdd -0.5Vdc Minimum
Output Voltage Logic Low (Vol)	w/TTL Load w/HCMOS Load	0.4Vdc Maximum 0.5Vdc Maximum
Rise Time	0.4Vdc to 2.4Vdc w/TTL Load; 20% to 80% of Waveform w/HCMOS Load < /=66.667MHz.	15nSeconds Maximum
Fall Time	0.4Vdc to 2.4Vdc w/TTL Load; 20% to 80% of Waveform w/HCMOS Load >66.667MHz.	10nSeconds Maximum
Duty Cycle	@ 1.4Vdc w/TTL Load; @ 50% w/HCMOS Load @ 1.4Vdc w/TTL Load or w/HCMOS Load @ 50% of Waveform w/LSTTL or HCMOS Load >66.667MHz	50 ±10% (Standard) 50±5% (Optional) 50±5% (Optional)
Load Drive Capability	250.000kHz to 24.000MHz 24.001MHz to 66.667MHz 66.668MHz to 80.000MHz	10TTL or 15pF HCMOS Load 10TTL or 15pF HCMOS Load
Aging (@ 25°C)		±5ppm / year Maximum
Start Up Time		10mSeconds Maximum

MECHANICAL DIMENSIONS

Marking Guide on page 58-59

All Dimensions in mm.

Surface Mount Option



Pin	Connection
1	VDD
2	Ground/Case
3	Frequency Output