

DIGITAL DELAY LINE SERIES 0449 TTL PROGRAMMABLE LOGIC DELAY MODULE 3 BIT

TECHNICAL INFORMATION

TEST CONDITIONS

Driving Signal Schottky TTL Buffer
Pulse Width 1.5 x Maximum Delay
Pulse Period 5 x Pulse Width
Supply Current 50.0 Milliamps max.
Supply Voltage, Vcc 5.0 Volts
Ambient Temperature 25°C

PERFORMANCE CHARACTERISTICS

Programmable Delay Tolerance
± 2 Nsec or 5% whichever is greater

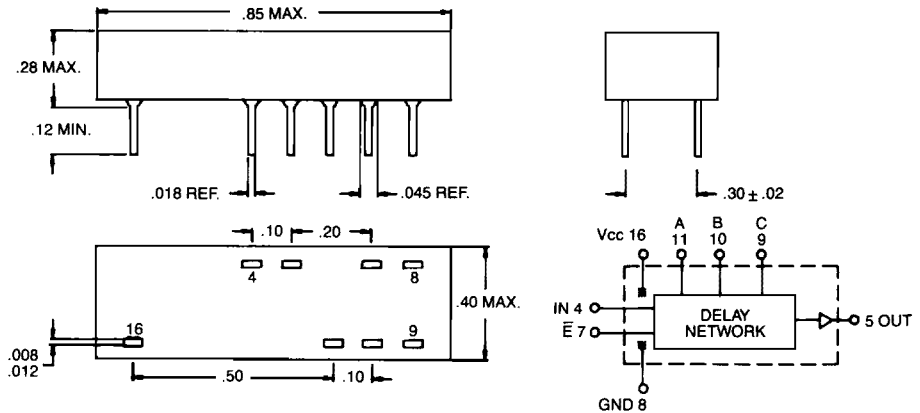
Performance Characteristics apply at above listed Test Conditions.

ELECTRICAL CHARACTERISTICS

Supply Voltage, Vcc 4.75 to 5.25 Volts
Logic 1 Input Voltage 2.5 Volts min. 5.5 Volts max.
Logic 0 Input Voltage .8 Volts max.
Logic 1 Output Voltage 2.7 Volts min.
Logic 0 Output Voltage 0.5 Volts max.
Operating Temperature Range 0°C To 70°C
Temperature Coefficient Of Total Delay 500PPM/°C Typical
Non Buffered Input

DRIVE CAPABILITIES

10 TTL Loads/Tap max.
—Compatible with TTL and DTL circuits.
—Other delays and tolerances upon request
—Buffered Input Available upon request



Part Number	* Min. Delay (NOM) 1, 2	** Max. Delay (NOM) 1, 2	ΔDelay / Step 1, 2
0449-0014-03	7NS	14NS	1 ± .5NS
0449-0021-03	7NS	21NS	2 ± .6NS
0449-0028-03	7NS	28NS	3 ± .7NS
0449-0035-03	7NS	35NS	4 ± .8NS
0449-0042-03	7NS	42NS	5 ± 1.0NS
0449-0049-03	7NS	49NS	6 ± 1.2NS
0449-0056-03	7NS	56NS	7 ± 1.4NS
0449-0063-03	7NS	63NS	8 ± 1.6NS
0449-0070-03	7NS	70NS	9 ± 1.8NS
0449-0077-03	7NS	77NS	10 ± 2.0NS

1 Delays measured at 1.5 Volt level on leading edge only.
2 Measured with no loads on output.

CONTROL SIGNAL TABLE

C	B	A
0	0	0 *
0	0	1
0	1	0
0	1	1
1	0	0
1	0	1
1	1	0
1	1	1 **

* Minimum Delay Code
** Maximum Delay Code