

MORNSUN®

PWA_(C)D-10W & PWB_(C)D-10W SERIES 10W, 4:1 WIDE INPUT, ISOLATED & REGULATED DUAL/SINGLE OUTPUT DC-DC CONVERTER

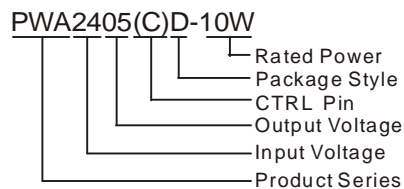


Patent Protection RoHS

FEATURES

- 4:1 wide input range
- DIP package
- Operating temperature: -40°C ~ +85°C
- 1500VDC Isolation
- Short circuit protection (automatic recovery)
- Metal shielding package
- Industry standard pinout
- MTBF>1,000,000 hours
- Good high temperature properties, can meet the industrial products technical requirements

PART NUMBER SYSTEM



APPLICATIONS

The PWA_(C)D-10W & PWB_(C)D-10W Series are designed for application where isolated output is required from a wide range input voltage distributed power system.

These products apply to where:

- 1) Input voltage range $\leq 4:1$;
- 2) 1.5KVDC input and output isolation;
- 3) Regulated and low ripple noise is required.

SELECTION GUIDE

Model Number	Input Voltage(VDC)		Output Voltage (VDC)	Output Current (mA)		Input Current (mA)(typ.)		Reflected Ripple Current (mA,typ.)	Max. Capacitive Load(μ F)	Efficiency (% , typ.) @ Max. Load
	Nominal (Range)	Max*		Max.	Min.	@Max. Load	@No Load			
PWA2405(C)D-10W	24 (9-36)	40	± 5	± 1000	± 100	521	35	35	680	80
PWA2412(C)D-10W			± 12	± 416	± 42	508			330	82
PWA2415(C)D-10W			± 15	± 333	± 33	502			220	83
▲PWB2403D-10W			3.3	2400	240	548		100	2200	76
PWB2405(C)D-10W			5	2000	200	527		35	1000	79
PWB2412(C)D-10W			12	833	83	514			470	81
PWB2415(C)D-10W			15	666	67	508			330	82
PWB2424(C)D-10W			24	416	42	502			220	83
▲PWA4805(C)D-10W	48 (18-72)	80	± 5	± 1000	± 100	267	20	100	680	78
PWA4812(C)D-10W			± 12	± 416	± 42	254		35	330	82
PWA4815(C)D-10W			± 15	± 333	± 33	251		220	83	
▲PWB4803D-10W			3.3	2400	240	271		100	2200	77
▲PWB4805(C)D-10W			5	2000	200	267		1000	78	
PWB4812(C)D-10W			12	833	83	254		35	470	82
PWB4815(C)D-10W			15	666	67	251		330	83	

Note: 1. *Input voltage can't exceed this value, or will cause the permanent damage.
2. ▲:PWM mode, others PFM mode.
3. "C" means the product with CTRL pin.

INPUT SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit
Input Surge Voltage (1000 ms)	24VDC Input Models	-0.7	--	50	VDC
	48VDC Input Models	-0.7	--	100	
Start-up Voltage	24VDC Input Models	--	7.5	9	
	48VDC Input Models	--	16	18	
Short Circuit Input Power		--	1.5	2	W
Input Filter	PWM	Capacitance Filter			
	PFM	π Filter			

OUTPUT SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit	
Output Power		1	--	10	W	
Positive voltage accuracy	Refer to recommended circuit	--	±1	±3	%	
Negative voltage accuracy		--	±3	±5		
Output Voltage Balance	Dual Output, Balanced Loads	--	±0.5	±1		
Line Regulation	Full load, Input voltage from low to high	--	±0.2	±0.5		
Load Regulation*	10% to 100% load	--	±0.5	±1		
Transient Recovery Time	25% load step change	PWM	--	0.3	0.5	ms
		PFM	--	8	15	
Transient Response Deviation	25% load step change	--	--	±5	%	
Temperature Drift	100% load	--	--	±0.03	%/°C	
Ripple**	20MHz Bandwidth	--	20	50	mVp-p	
Noise**		--	75	150		
Short Circuit Protection		Continuous, automatic recovery				

Note:* Dual output models unbalanced load: ±5%.

**Test ripple and noise by "parallel cable" method. See detailed operation instructions at Testing of Power Converter section, application notes.

COMMON SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit
Isolation Voltage	Tested for 1 minute and leakage current less than 1 mA	1500	--	--	VDC
Isolation Resistance	Test at 500VDC	1000	--	--	MΩ
Isolation Capacitance	Input/Output, 100KHz/1V	--	1000	--	pF
Switching Frequency	Full load, nominal input	--	300	--	KHz
MTBF	MIL-HDBK-217F@25°C	100	--	--	K hours
Case Material		Aluminum			
Weight		--	23.5	--	g

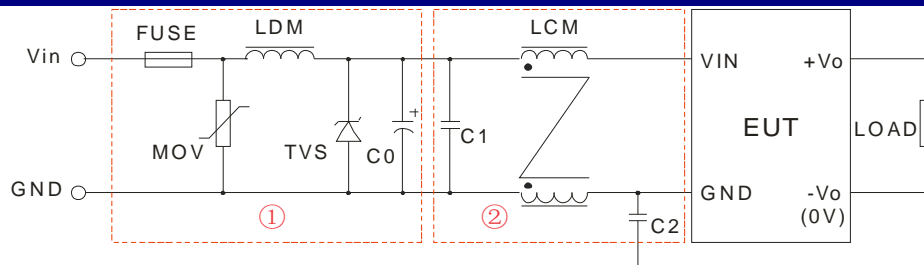
ENVIRONMENTAL SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit
Storage Humidity	Non condensing	--	--	95	%
Operating Temperature	Power derating (above 71°C)	-40	--	85	°C
Storage Temperature		-55	--	125	
Temp. rise allowed at full load	Ta=25°C	--	30	--	
Lead Temperature	1.5mm from case for 10 seconds	--	--	300	
Cooling		Free air convection			

EMC SPECIFICATIONS

EMI	CE	CISPR22/EN55022 CLASS A (External Circuit Refer to Figure1)
EMS	ESD	IEC/EN61000-4-2 Contact ±4KV perf. Criteria B
	EFT	IEC/EN61000-4-4 ±2KV perf. Criteria B (External Circuit Refer to Figure1)
	Surge	IEC/EN61000-4-5 ±2KV perf. Criteria B (External Circuit Refer to Figure1)

EMC RECOMMENDED CIRCUIT

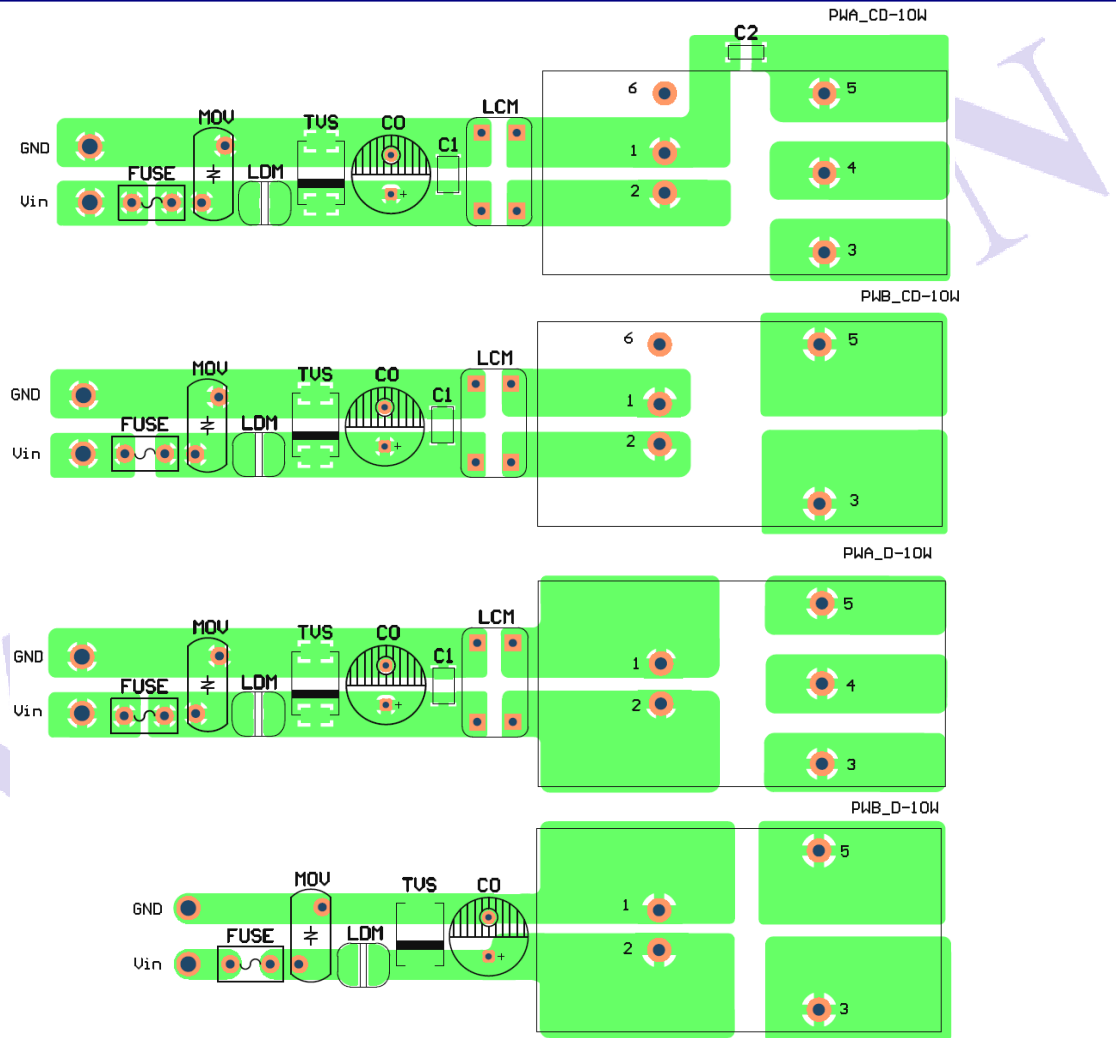


(Figure1)

Recommended external circuit parameters	PWA24_D-10W	PWB24_D-10W	PWA48_D-10W	PWA48_CD-10W	PWB48_D-10W	PWB48_CD-10W
FUSE	Choose according to load					
MOV	10D560K		10D101K			
LDM	56 μ H					
TVS	SMCJ48A			SMCJ100A		
C0	120 μ F/50V			120 μ F/100V		
C1	--	--	4.7 μ F/100V		--	4.7 μ F/100V
LCM	--	--	3.3mH core:A10		--	3.3mH core:A10
C2	--	--	--	47pF/2KV	--	--

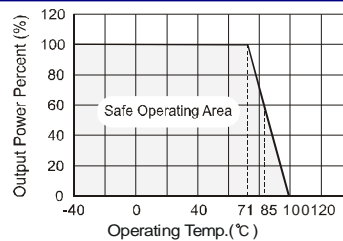
Note: 1. In Figure 1, part① is EMS Recommended external circuit, part② is EMI recommended external circuit. Choose according to requirements.
 2. If there is no recommended parameters, the model no require the external component.

EMC RECOMMENDED CIRCUIT PCB LAYOUT

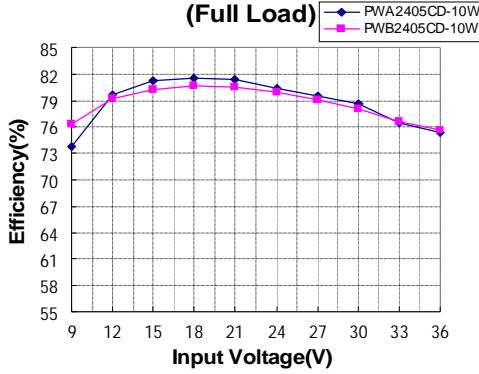


(Figure 2)

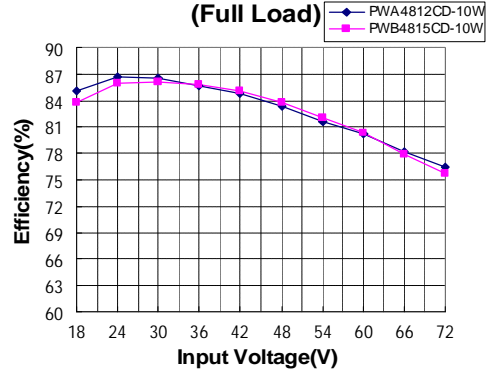
PRODUCT TYPICAL CURVE



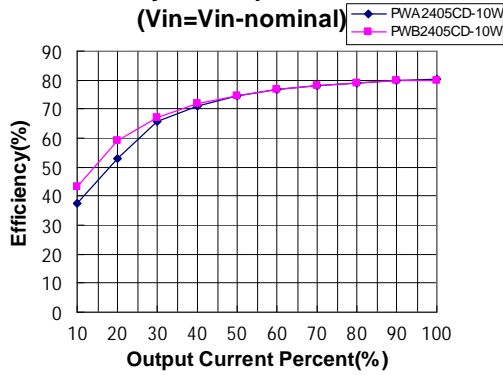
Efficiency VS Input Voltage curve (Full Load)



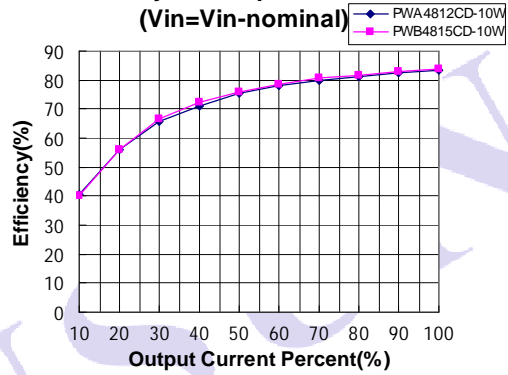
Efficiency VS Input Voltage curve (Full Load)



Efficiency VS Output Load curve (Vin=Vin-nominal)



Efficiency VS Output Load curve (Vin=Vin-nominal)



OUTLINE DIMENSIONS, RECOMMENDED FOOTPRINT & PACKAGING

MECHANICAL DIMENSIONS

FOOTPRINT DETAILS

Pin	Single	Dual
1	GND	GND
2	Vin	Vin
3	+Vo	+Vo
4	No Pin	0V
5	0V	-Vo
6*	CTRL	CTRL

*PWA/B_D-10W series without CTRL pin.

Note:
Unit:mm[inch]
Pin diameter tolerances:±0.10mm[±0.004inch]
General tolerances:±0.25mm[±0.010inch]

RECOMMENDED FOOTPRINT

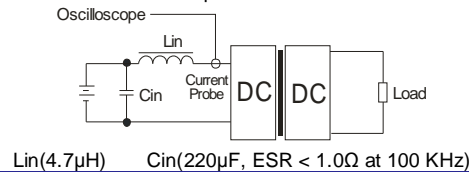
TUBE OUTLINE DIMENSIONS

Note:
Unit :mm[inch]
General tolerances: ±0.50mm[±0.020inch]
L=220mm[8.661inch] pcs/tube: 7
Inner packaging dimensions: L*W*H=255*170*80mm
Outer packaging dimensions(with six inner package boxes):
L*W*H=375*280*270mm

TEST CONFIGURATIONS

Input reflected-ripple current test setup

Input reflected-ripple current is measured with an inductor L_{in} and Capacitor C_{in} to simulate source impedance.



DESIGN & APPLY CONSIDERATIONS

1) Requirement on output load

To ensure this module can operate efficiently and reliably, During operation, the minimum output load **could not be less than 10% of the full load**. If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load, or use our company's products with a lower rated output power.

2) Overload Protection

Under normal operating conditions, the output circuit of these products has no protection against overload. The simplest method is add a circuit breaker to the circuit.

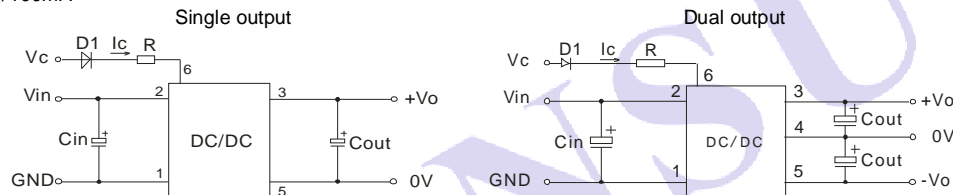
3) Recommended circuit

All the PWA_(C)D-10W & PWB_(C)D-10W series have been tested according to the following recommended testing circuit before leaving factory. (see Figure 3).

If you want to further decrease the input/output ripple, you can increase capacitance properly or choose capacitors with low ESR. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor must less than the Max. Capacitive Load.

General: C_{in} : 10 μ F~47 μ F

C_{out} : 10 μ F/100mA



(Figure 3)

4) CTRL Terminal

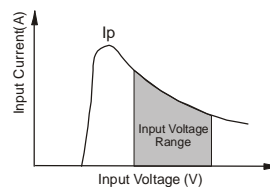
When open or high impedance, the converter work well; When this pin is 'high level'; the converter shutdown; It should be note that the input current should between 5-10mA,exceeding the maximum 20mA will cause permanence damage to the converter. The value of R can be derived as follows:

$$R = \frac{V_c - V_D - 1.0}{I_c}$$

5) Input current

Nominal input voltage range. The input current of the power supply must be sufficient to the startup current (I_p) of the DC/DC module (Figure 4).

General: $I_p \leq 1.6 \cdot I_{in-max}$



(Figure 4)

6) No parallel connection or plug and play

Note:

1. The load shouldn't be less than 10%, otherwise ripple will increase dramatically. Operation under minimum load will not damage the converter; However, they may not meet all specification listed.
2. Max. Capacitive Load tested at input voltage range and full load.
3. All specifications measured at $T_a=25^\circ\text{C}$, humidity<75%, nominal input voltage and rated output load unless otherwise specified.
4. In this datasheet, all the test methods of indications are based on corporate standards.
5. Only typical models listed, other models may be different, please contact our technical person for more details.
6. Our company offer custom products.
7. Specifications subject to change without notice.

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