

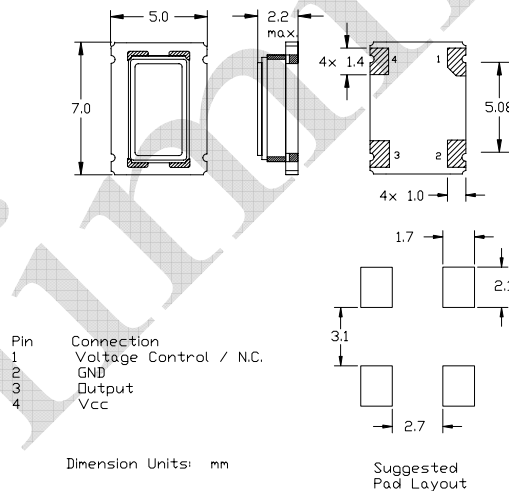


Surface Mount Oscillator, TCXO, TCVCXO  
Ceramic Package, 5 mm x 7 mm

I540 / I541/ I740 / I741 Series \*

	I540 / I740	I541 / I741
Frequency	10.000 MHz to 30.000 MHz	10.000 MHz to 30.000 MHz
Output Level	HC-MOS / TTL	Clipped Sine
Level	'0' = 0.1 Vcc Max., '1' = 0.9 Vcc Min.	0.8 V p-p Min.
Output Load	15 pF / 10 LSTTL	20K Ohms / 10 pF
Duty Cycle	50% ± 10%	
Rise / Fall Time	10 nS Max.	
Current	5 mA Max.	2 mA Max.
Supply Voltage	See Supply Voltage Table, tolerance ± 5 %	
Frequency Stability	See Frequency Stability Table	
Frequency Stability at +25° C	± 1 ppm (I540/I541)	
Aging	± 1 ppm / Year Max.	
Control Voltage (I733)	1.5 VDC ± 1.0 VDC, ± 5 ppm Min.	
Slope	Positive	
Temperature		
Operating	See Operating Temperature Table	
Storage	-40° C to +85° C	
Environmental	See Appendix B for information	
Package Information	MSL = 2a., Termination = e4	

\* I540 = HC-MOS TCXO, I541 = Clipped Sine TCXO, I740 = HC-MOS TCVCXO, I741 = Clipped Sine TCVCXO



Part Number Guide	Sample Part Number: I541 - 1Q3 - 20.000			
Package and Output	Operating Temperature	Frequency Stability vs. Temperature	Supply Voltage	Frequency
I540 -	7 = 0° C to +50° C	**N = ±1.0 ppm	3 = 3.3 VDC	- 20.000 MHz
I541 -	1 = 0° C to +70° C	**O = ±1.5 ppm	7 = 3.0 VDC	
I740 -	3 = -20° C to +70° C	**P = ±2.0 ppm	2 = 2.7 VDC	
I741 -	2 = -40° C to +85° C	Q = ±2.5 ppm		
		R = ±3.0 ppm		
		J = ±5.0 ppm		

NOTE: A 0.01 µF bypass capacitor is recommended between Vcc (pin 4) and Gnd (pin 2) to minimize power supply noise.  
\*\* Not available at all temperature ranges.