STRATUM 3E High Stability Oven Stabilized Oscillator OH100 - Series

OCXO – OCVCXO



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Description

Connor-Winfield's high stability OH100 series are exceptionally precise frequency standards, excellent for use in cellular base stations, test equipment, Synchronous Ethernet, VSAT and Stratum 3E applications

These unique OCXOs and OCVCXOs provide temperature stabilities in the range of ± 10 ppb to ± 50 ppb, over the commercial, extended commercial or the industrial temperature range. Power requirements are 3W over the commercial temperature range and 4.5W over the industrial temperature range. Additionally, excellent aging is achieved through the use of overtone SC cut crystals.

The OH100 - series is available with CMOS logic or Sinewave output along with Electronic Frequency Tuning. These oscillators provide outstanding phase noise that varies depending on frequency requirements. Allan Variance specifications are rated for primary reference standards. Warm up times are on the order of 5 minutes to 0.10 ppm of final frequency.



Features

- Frequency Range: 5 to 40 MHz
- OCXO Fixed Frequency
- OCVCXO Voltage Controlled Frequency
- 3.3 Vdc or 5.0 Vdc Operation
- Available Frequency Stabilities:
 ±10.0 ppb, ±20 ppb or ±50 ppb
- Available Temperature Ranges:
 0 to 70°C, -20 to 70°C, -20 to 75°C, -40 to 70°C or -40 to 85°C
- Low Phase Noise / Phase Jitter
- Output: CMOS Logic or Sinewave
- Package: 1.0" x 1.0" x 0.500"
- RoHS Compliant / Lead Free

RoHS

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Ordering Information

OH100-	6	10	03	C	F	- 010.0M
Package OH100- = 1.0"x1.0"x0.5" Through Hole Package	Temperature Range 5 = 0 to 70°C 6 = -40 to 85°C 7 = -20 to 70°C 8 = -40 to 70°C 9 = -20 to 75°C	Frequency Stability 10 = ±10.0 ppb 20 = ±20.0 ppb 50 = ±50.0 ppb	Supply Voltage 03 = 3.3 Vdc 05 = 5.0 Vdc	C = CMOS S = Sinewave	OCXO Type F = Fixed	Output Frequency Frequency Format -xxx.xM Min -xxx.xxxxxM Max *Amount of numbers after the decimal point. M = MHz

Example Part Number:

 $OH100-61003CF-010.0M = 1.0" \times 1.0" \times 0.445"$ package, -40 to 85°C temperature range, ± 10.0 ppb frequency stability, 3.3 Vdc supply voltage, CMOS square wave output, fixed output frequency OCXO, 10.0 MHz output frequency.

OH100-52005CV-010.0M = 1.0" x 1.0" x 0.445" package, 0 to 70°C temperature range, ±20.0 ppb frequency stability, 5.0 Vdc supply voltage, CMOS square wave output, voltage controlled output frequency, OCVCXO, 10.0 MHz output frequency.

OH100-71005SV-010.0M = $1.0^{\circ} \times 1.0^{\circ} \times 0.445^{\circ}$ package, -20 to 70° C temperature range, ± 10.0 ppb frequency stability, 5.0 Vdc supply voltage, Sinewave output, voltage controlled output frequency, OCVCXO, 10.0 MHz output frequency.



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Absolute Maximum Ratings

Parameter	Minimum	Nominal	Maximum	Units	Notes
Storage Temperature	-55	-	125	°C	
Supply Voltage - 3.3 Vdc (Vcc)	-0.5	-	4.5	Vdc	
Supply Voltage - 5.0 Vdc (Vcc)	-0.5	-	7.0	Vdc	
Control Voltage (Vc)	-0.5	-	7.0	Vdc	

Operating Specifications

5	_	40		
		40	MHz	
0	-	70	°C	
	-		°C	
	-			
	-			
	-			
	-	0.1	ppm	@ 25°C
	-	10.0	ppb	1
-20.0	-	20.0	ppb	1
-50.0	_	50.0		1
-2.0	-	2.0		±5%
-2.0	-	2.0	ppb	±5%
			'	
-1.0	_	1.0	ppb/dav	2
	-			2
			11	
-50	_	50	daa	
	_			
			1-1	
-300	_	300	daa	3
	_			3
	t be > 5 ms to 90			
			Vdc	
			Vdc	
_	_	3.00	W	4
_	_		W	4
_	_		W	4
_	_			4
		0.00		
_	_	1.10	W	4
_	_			4
_	_			4
_	_			4
		1.00	V V	
_	_	1.0	ns RMS	
_	_			
_	1 0F-11	<u> </u>		
	1.0L-11			
				5
	-2.0 -2.0 -1.0 -2.0 -50 -100 -300 -500 -on ramp time mus 3.13 4.75	-2040200.110.020.050.02.02.01.02.01.02.01.02.01.01.02.01	-20 - 70 -40 - 70 -20 - 75 -0.1 - 0.1 -10.0 - 10.0 -20.0 - 20.0 -50.0 - 50.0 -2.0 - 2.0 -1.0 - 1.0 -2.0 - 2.0 -1.0 - 1.0 -2.0 - 2.0 -1.0 - 100 -300 - 500 -100 - 100 -300 - 500 -100 - 100 -300 - 300 -500 - 500 -on ramp time must be > 5 ms to 90% of Vcc.) 3.13 3.30 3.47 4.75 5.00 5.25 3.20 - 3.80 1.10 - 1.15 1.15 1.20 - 1.50 - 1.0E-11 - 1.0	-20 - 70 °C - 40 °C - 40 °C - 70 °C - 20 °C - 75 °C °C - 20 °C - 75 °C °C - 20

Notes:

- 1. Referenced to the frequency measured @ 25°C.
- 2.At time of shipment after 48 hours of operation.
- 3. Inclusive of calibration, operating temperature, supply voltage change, load change and 20 years aging.
- 4. Measured with Vcc = Nominal, in calm air.
- 5. Measured @ 25° C, within ± 100 ppb, referenced one hour after turn-on.

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Phase Noise Characteristics

	Typical Phase	Noise for	10 MHz	OXCO's \	with CMOS	Output
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Parameter		Minimum	Nominal	Maximum	Units	Notes
SSB Phase Noise	5 - 12 MHz	>12- 20 MHz	>20 - 30 MHz	>30 - 40 MHz	Units	
@ 1Hz offset	-85	-80	-70	-65	dBc/Hz	
@ 10Hz offset	-115	-110	-100	-95	dBc/Hz	
@ 100Hz offset	-140	-135	-125	-120	dBc/Hz	
@ 1KHz offset	-145	-140	-140	-140	dBc/Hz	
@ 10KHz offset	-150	-150	-150	-150	dBc/Hz	
@ 100KHz offset	-150	-150	-150	-150	dBc/Hz	

OCVCXO Input Characteristics (OCXO Type Code V)

Parameter	Minimum	Nominal	Maximum	Units	Notes
Control Voltage Range:					
3.3 Vdc Models	0.30	1.65	3.00	V	Vcc = 3.3 Vdc
5.0 Vdc Models	0.50	2.50	4.50	V	Vcc = 5.0 Vdc
Frequency Pullability:	±0.4	-	-	ppm	
Input Impedance	100K	-	-	Ohms	

CMOS Output Characteristics (Output Code C)

Parameter	Minimum	Nominal	Maximum	Units	Notes
Load	-	15	-	рF	
Output Voltage:					
3.3 Vdc Models High (Voh)	3.0	-	-	V	
Low (Vol)	-	-	0.4		
5.0 Vdc Models High (Voh)	4.7	-	-	V	
Low (Vol)	-	-	0.4		
Duty Cycle at 50% of Vcc	45	50	55	%	
Rise / Fall Time: 10% to 90%	-	-	6	ns	
Spurious Output	-	-	-80	dBc	

Sinewave Output Characteristics (Output Code S)

Parameter	Minimum	Nominal	Maximum	Units	Notes
Load	-	50	-	Ohms	
Output Power	5.0	-	-	dBm:	
Harmonics	-	-	-30	dBc	
Spurious	-	-	-80	dBc	

Package Characteristics

OH100 Package Hermetically sealed, resistive welded package with grounded case.

Environmental Characteristics

500 G's 1ms, Halfsine, 3 shocks per direction, per MIL-STD 202G, Method 213B Test Condition D.
0.06" D.A. or 10G's Peak, 10 to 500 Hz, per MIL-STD-202G, Method 204D, Test Condition A.
5.35 G's rms. 20 to 2000 Hz per MIL-STD-202G, Method 214, Test Condition 1A, 15 minutes each axis.
10 cycles, 95% RH, Per MIL-STD-202G, Method 112.
Per MIL-STD-202G, Method 215J.
Through Hole Mounted
Per MIL-STD-202G, Method 210, Condition E.
RoHS compliant, lead free. See solder profile.

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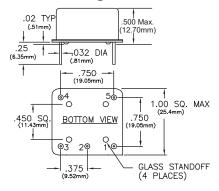
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Re-Stabilization Time

Off Time	Re stabilization Time	
<1 Hour	<2 Hours *	
<6 Hour	<12 Hours *	
<24 Hour	<48 Hours *	
1 to 16 Days	48 Hours + 1/4 Off Time *	
>16 Days	<6 Days *	

^{*} For a given off time, the time required to meet daily aging, short term stability requirements

Package Outline



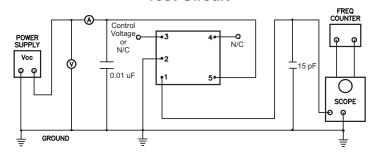
Dimensional Tolerance: ±.005 (.127mm)

Package Marking

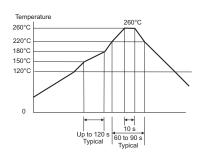


Keep Out Area: Under the OCXO is a keep out area, do not place any parts in this area.

Test Circuit



Solder Profile



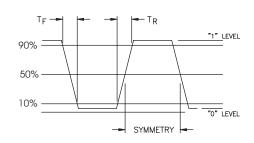
Meets IPC/JEDEC J-STD-020C

Pin Connections

Pin Connection

- 1: Output
- 2: Ground (Case)
- 3: Control Voltage or N/C
- 4: N/C
- 5: Supply Voltage (Vcc)

CMOS Output Waveform



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