Clock OSC SG-210STF

SEIKO EPSON CORPORATION

Product name SG-210STF 4.000000 MHz Y
Product Number / Ordering code X1G0041710299xx
Please refer to the 8.Packing information about xx (last 2 digits)

Output waveform CMOS
Pb free / Complies with EU RoHS directive
Reference weight Typ. 12 mg

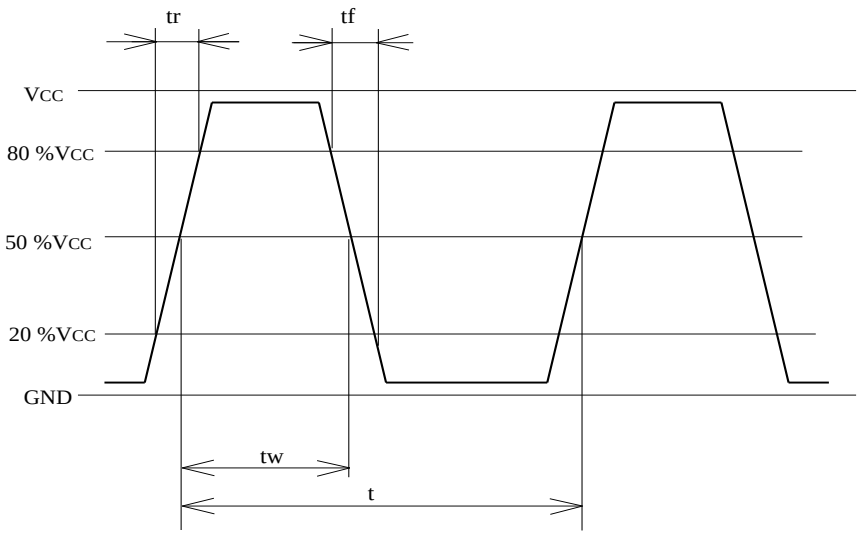
1.Absolute maximum ratings

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions / Remarks
Maximum supply voltage	Vcc-GND	-0.3	-	+4	V	-
Storage temperature	T_stg	-40	-	+125	ºC	Storage as single product
Input voltage	Vin	-0.3	-	Vcc+0.3	V	ST terminal

2.Specifications(characteristics)

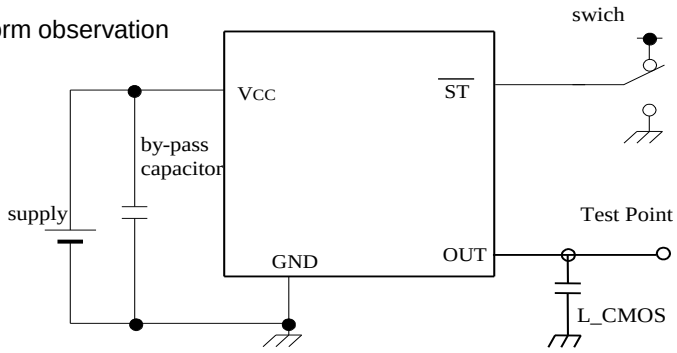
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions / Remarks
Output frequency	f0		4.000000		MHz	
Supply voltage	Vcc	1.6	-	3.6	V	-
Operating temperature	T_use	-40	-	+105	ºC	-
Frequency tolerance	f_tol	-50	-	50	$x10^{-6}$	T_use
Current consumption	Icc	-	-	1.8	mA	No load condition Vcc = 3.6V
Stand-by current	I_std	-	-	2.7	µA	Vcc = 3.6V , ST = GND
Symmetry	SYM	45	-	55	%	50% Vcc Level L_CMOS=<15pF
Output voltage	V_{OH}	Vcc-0.4	-	-		-
	V_{OL}	-	-	0.4		-
Output load condition	L_CMOS	-	-	15	pF	CMOS Load
Input voltage	V_{IH}	0.8Vcc	-	-		ST terminal
	V_{IL}	-	-	0.2Vcc		ST terminal
Rise time	t_r	-	-	3.5	ns	Vcc1.8V±10% : 0.2Vcc to 0.8Vcc Level, L_CMOS=15pF
Fall time	tf	-	-	3.5	ns	Vcc1.8V±10% : 0.2Vcc to 0.8Vcc Level, L_CMOS=15pF
Start-up time	t_str	-	-	3	ms	t = 0 at 0.9Vcc
Jitter	t_{DJ}	-	0	-	ps	Deterministic Jitter Vcc=3.3V
	t_{RJ}	-	2.4	-	ps	Random Jitter Vcc=3.3V
	t_{RMS}	-	2.3	-	ps	δ(RMS of total distribution) Vcc=3.3V
	t_{p-p}	-	20	-	ps	Peak to Peak Vcc=3.3V
	t_{acc}	-	2.5	-	ps	Accumulated Jitter(δ) n=2 to 50000 cycles
Phase jitter	t_{PJ}	-	-	-	ps	-
Phase noise	L(f)	-	-	-	dBc/Hz	-
		-	-	-	dBc/Hz	-
		-	-	-	dBc/Hz	-
		-	-	-	dBc/Hz	-
		-	-	-	dBc/Hz	-
		-	-	-	dBc/Hz	-
		-	-	-	dBc/Hz	-
Frequency aging	f_age	-3	-	3	$x10^{-6}$	@+25ºC first year
		-	-	-		-

3.Timing chart

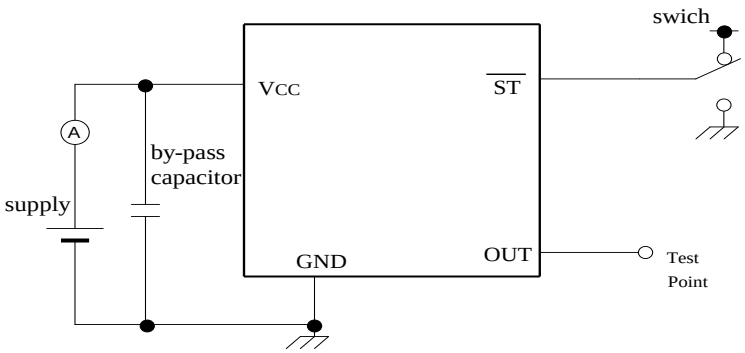


4.Test circuit

1) Waveform observation



2) Current consumption



*Current consumption under the disable function should be = GND.

3) Condition

(1) Oscilloscope

· Band width should be minimum 5 times higher (wider) than measurement frequency.

· Probe earth should be placed closely from test point and lead length should be as short as possible

* Recommendable to use miniature socket. (Don't use earth lead.)

(2) L_CMOS also includes probe capacitance.

(3) By-pass capacitor (0.01 µF to 0.1 µF) is placed closely between VCC and GND.

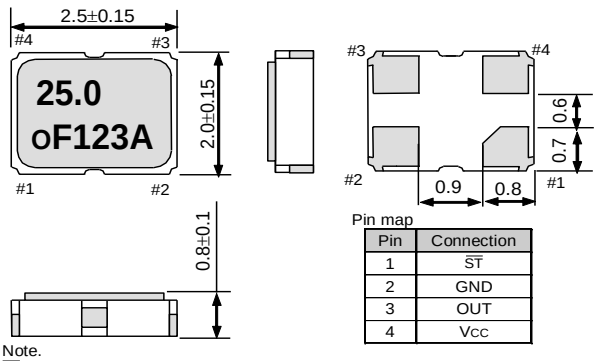
(4) Use the current meter whose internal impedance value is small.

(5) Power supply

· Start up time (0 %VCC to 90 %VCC) of power source should be more than 150 µs.

· Impedance of power supply should be as lowest as possible.

5.External dimensions (Unit: mm)

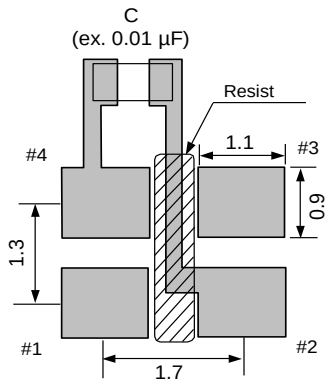


Pin map

Pin	Connection
1	$\overline{ST}$
2	GND
3	OUT
4	Vcc

Note.
$\overline{ST}$ pin = HIGH or "open" : Specified frequency output.
$\overline{ST}$ pin = LOW : Output is high impedance, oscillation stops.

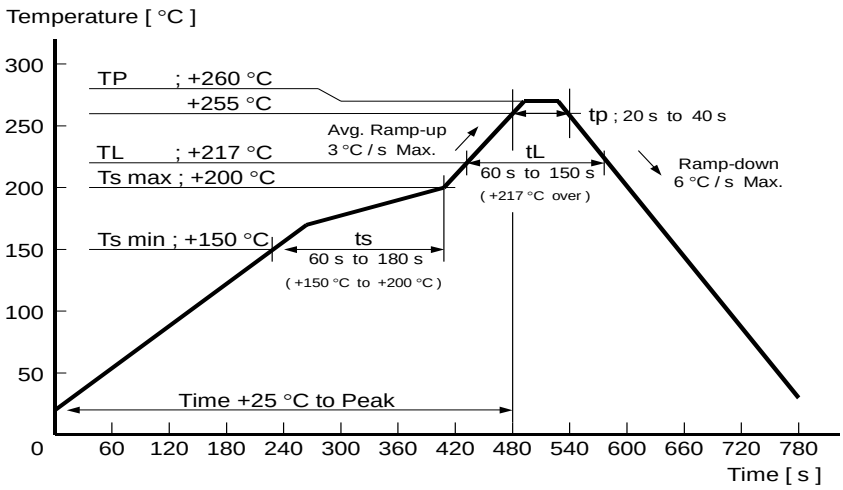
6.Footprint(Recommended) (Unit: mm)



To maintain stable operation, provide a 0.01uF to 0.1uF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc - GND).

7.Reflow profile

Reflow condition (Follow of JEDEC STD-020D.01)



8.Packing information

[1]Product number last 2 digits code(xx) description The recommended code is "00"

X1G0041710299xx

Code	Condition	Code	Condition
01	Any Q'ty vinyl bag(Tape cut)	14	1000pcs / Reel
11	Any Q'ty / Reel	15	2000pcs / Reel
12	250pcs / Reel	00	3000pcs / Reel

[2] Taping specification

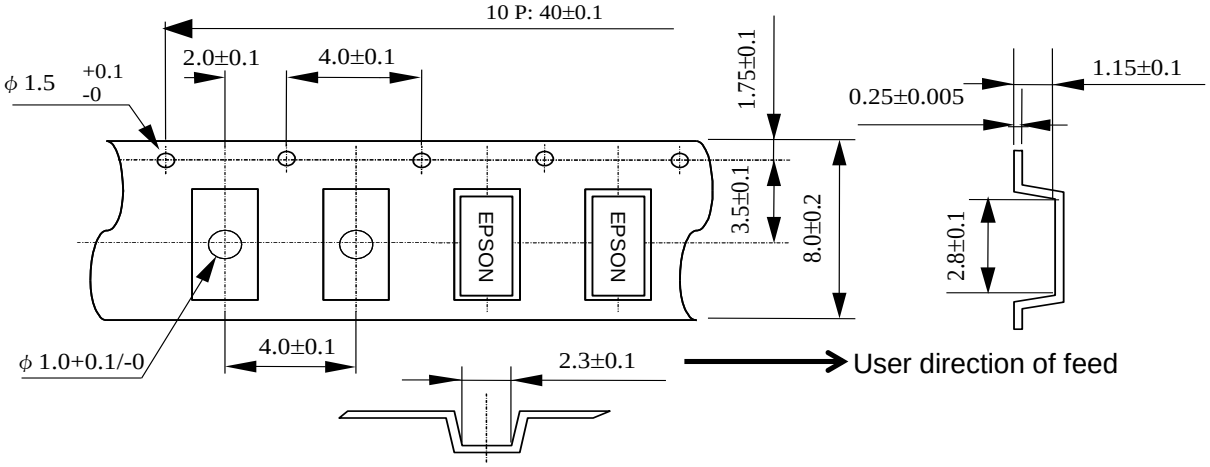
Subject to EIA-481 & IEC-60286

(1) Tape dimensions

Material of the Carrier Tape : PS

Material of the Top Tape : PET+PE

Unit: mm

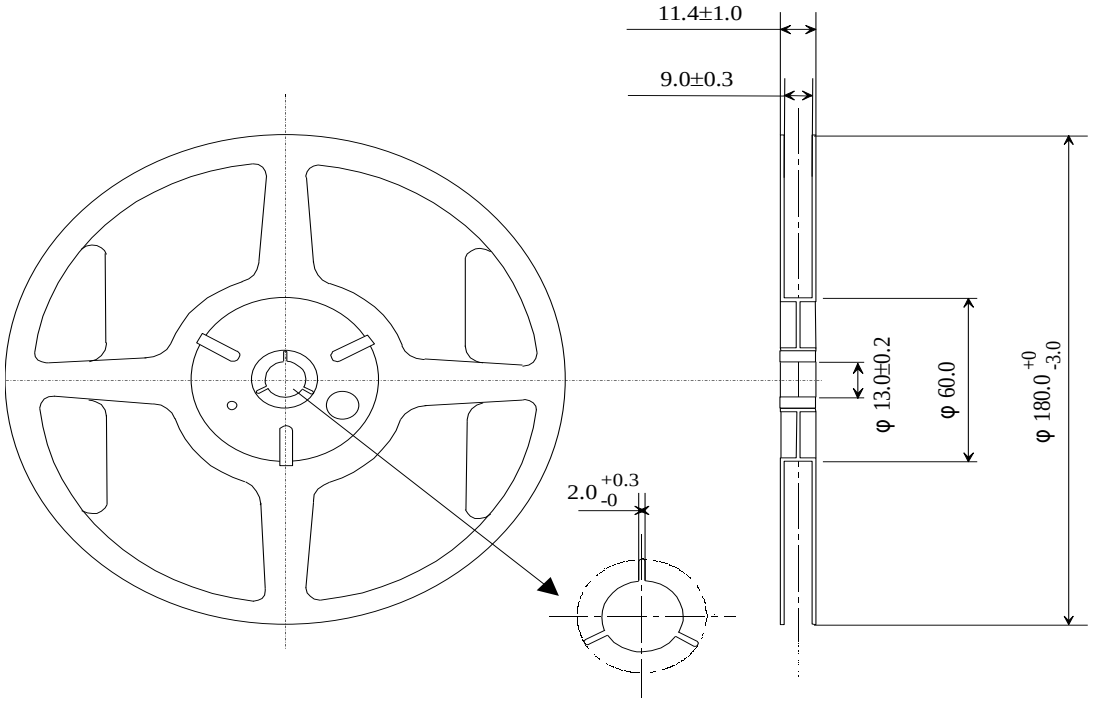


(2) Reel dimensions

Center material : PS

Material of the Reel : PS

Unit: mm



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