National Semiconductor

29F524 • 29F525
Dual 7- and 8-Deep Pipeline Registers

General Description

The 29F524/525 are 8-bit wide, 14- and 16-word deep pipeline registers with TRI-STATE® outputs. The registers are organized as two 7- or 8-byte shift registers. A single clock is provided and operation of the shift registers is under microprogram control.
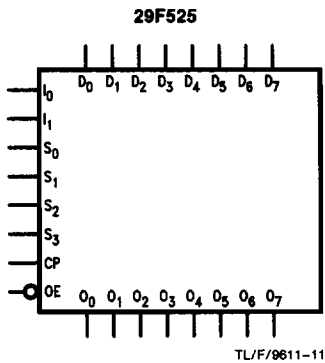
In the 29F524, the shift registers are 7 deep. All fourteen registers are available at the output. The input data is fed directly to the output or an all-zero byte.

The shift registers are 8 deep in the 29F525. Any of the sixteen registers may be cascaded to from a single 14- or 16-byte-long pipeline register.
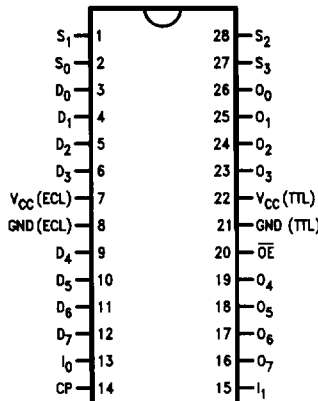
Features

- 29F524—Dual 7-deep or single 14-deep (with feedthrough and 0) registers
- 29F525—Dual 8-deep or single 16-deep registers
- Allows saving addresses within its registers for use at a later time
- Hold, or shift and load instructions
- Number of delay cycles can be changed by the user without interrupting the data flow
- All registers available at TRI-STATE output
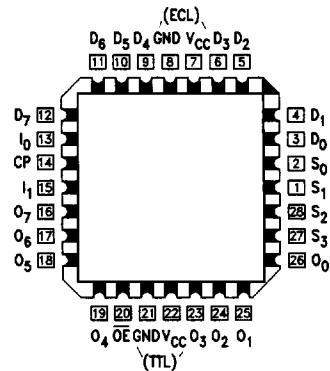- Functionally and pin compatible to AMD Am29524/Am29525

Logic Symbol

29F525

TL/F/9611–11

Connection Diagrams

Pin Assignment for DIP/SOIC 29F525

TL/F/9611–1

Pin Assignment for LCC and PCC 29F525

TL/F/9611–2