2SB0950 (2SB950), 2SB0950A (2SB950A)

Silicon PNP epitaxial planar type darlington

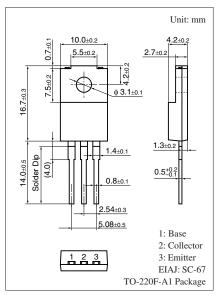
For power amplification and switching Complementary to 2SD1276 and 2SD1276A

■ Features

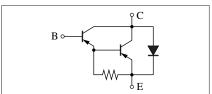
- High forward current transfer ratio h_{FE}
- High-speed switching
- Full-pack package which can be installed to the heat sink with one screw

■ Absolute Maximum Ratings $T_C = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage	2SB0950	V_{CBO}	-60	V
(Emitter open)	2SB0950A		-80	
Collector-emitter voltage	2SB0950	V _{CEO}	-60	V
(Base open)	2SB0950A		-80	
Emitter-base voltage (Coll	V _{EBO}	-5	V	
Collector current	I_{C}	-4	A	
Peak collector current	I_{CP}	-8	A	
Collector power	P _C	40	W	
dissipation	$T_a = 25$ °C		2	
Junction temperature	T_{j}	150	°C	
Storage temperature	T_{stg}	-55 to +150	°C	



Internal Connection



■ Electrical Characteristics $T_C = 25$ °C ± 3 °C

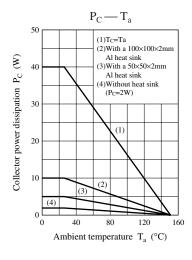
Parameter		Symbol	Conditions	Min	Тур	Max	Unit
Collector-emitter voltage	2SB0950	V _{CEO}	$I_{\rm C} = -30 \text{ mA}, I_{\rm B} = 0$	-60			V
(Base open)	2SB0950A			-80			
Base-emitter voltage		V_{BE}	$V_{CE} = -3 \text{ V}, I_{C} = -3 \text{ A}$			-2.5	V
Collector-base cutoff	2SB0950	I_{CBO}	$V_{CB} = -60 \text{ V}, I_E = 0$			-200	μΑ
current (Emitter open)	2SB0950A		$V_{CB} = -80 \text{ V}, I_E = 0$			-200	
Collector-emitter cutoff	2SB0950	I_{CEO}	$V_{CE} = -30 \text{ V}, I_B = 0$			-500	μΑ
current (Base open)	2SB0950A		$V_{CE} = -40 \text{ V}, I_{B} = 0$			-500	
Emitter-base cutoff current (Collector open)		I_{EBO}	$V_{EB} = -5 \text{ V}, I_C = 0$			-2	mA
Forward current transfer ratio		h _{FE1}	$V_{CE} = -3 \text{ V}, I_{C} = -0.5 \text{ A}$	1 000			_
		h _{FE2} *	$V_{CE} = -3 \text{ V}, I_{C} = -3 \text{ A}$	1000		10 000	
Collector-emitter saturation voltage		V _{CE(sat)1}	$I_C = -3 \text{ A}, I_B = -12 \text{ mA}$			-2	V
		V _{CE(sat)2}	$I_C = -5 \text{ A}, I_B = -20 \text{ mA}$			-4	V
Transition frequency		f_T	$V_{CE} = -10 \text{ V}, I_{C} = -0.5 \text{ A}, f = 1 \text{ MHz}$		20		MHz
Turn-on time		t _{on}	$I_C = -3 \text{ A}, I_{B1} = -12 \text{ mA}, I_{B2} = 12 \text{ mA}$		0.3		μs
Storage time		t _{stg}	$V_{CC} = -50 \text{ V}$		2		μs
Fall time		t _f			0.5		μs

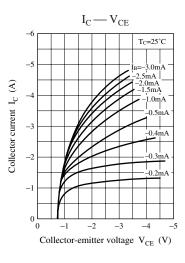
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

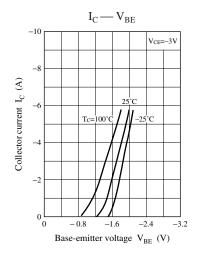
2. *: Rank classification

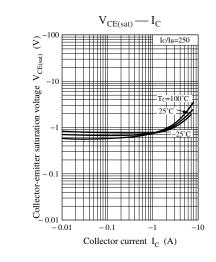
Rank	R	Q	P	
h _{FE2}	1000 to 2500	2000 to 5000	4000 to 10000	

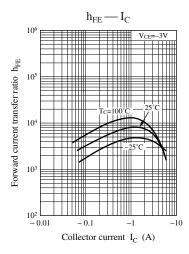
Note) The part numbers in the parenthesis show conventional part number.

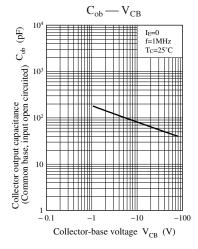


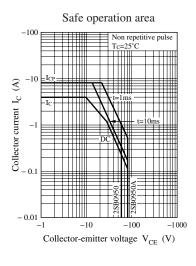


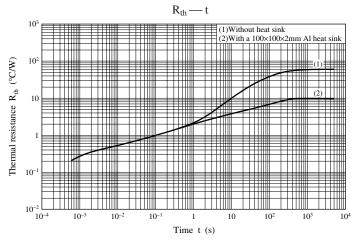












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