SynQor

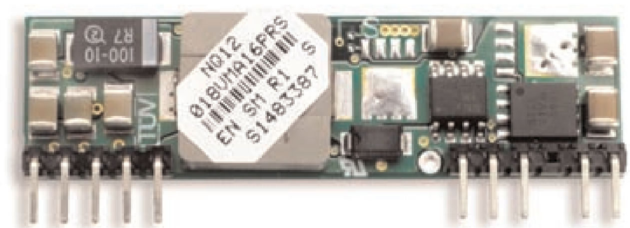
Technical Specification*

Non-Isolated SIP Converter **9.6 - 14.4V$_{in}$ 16A**

16A Non-Isolated DC/DC Converter in SIP configuration

The NiQor™ SIP DC/DC converter is a non-isolated buck regulator, which employs synchronous rectification to achieve extremely high conversion efficiency. The NiQor family of converters are used predominately in DPA systems using a front end DC/DC high power brick (48Vin to low voltage bus). The non-isolated NiQor converters are then used at the point of load to create the low voltage outputs required by the design. Typical applications include telecom/datacom, industrial, medical, transportation, data processing/storage and test equipment.

NiQor Non-Isolated



NiQor vertical mount SIP module

Operational Features

- Ultra-high efficiency, up to 95% at full and half load
- Delivers 16 amps of output current with minimal derating - no heatsink required
- Input voltage range: 9.6 - 14.4V
- Fast transient response time
- On-board input and output filter capacitor
- No minimum load requirement means no preload resistors required

Protection Features

- Input under-voltage lockout disables converter at low input voltage conditions
- Temperature compensated over-current shutdown protects converter from excessive load current or short circuits
- Output over-voltage protection protects load from damaging voltages
- Thermal shutdown

Mechanical Features

- Industry standard SIP pin-out configuration
- Industry standard size: 2.0″ x 0.55″ x 0.29 (50.8 x 14 x 7.3mm)
- Total weight: 0.30 oz. (9.4 g) , lower mass greatly reduces vibration and shock problems
- Open frame construction maximizes air flow cooling
- Available in both vertical and horizontal mounting

Control Features

- On/Off control
- Output voltage trim (industry standard) permits custom voltages and voltage margining
- Optional features include remote sense and wide output voltage trim (0.85V - 5.0V) - see wide trim NQ12T50 datasheet

Safety Features

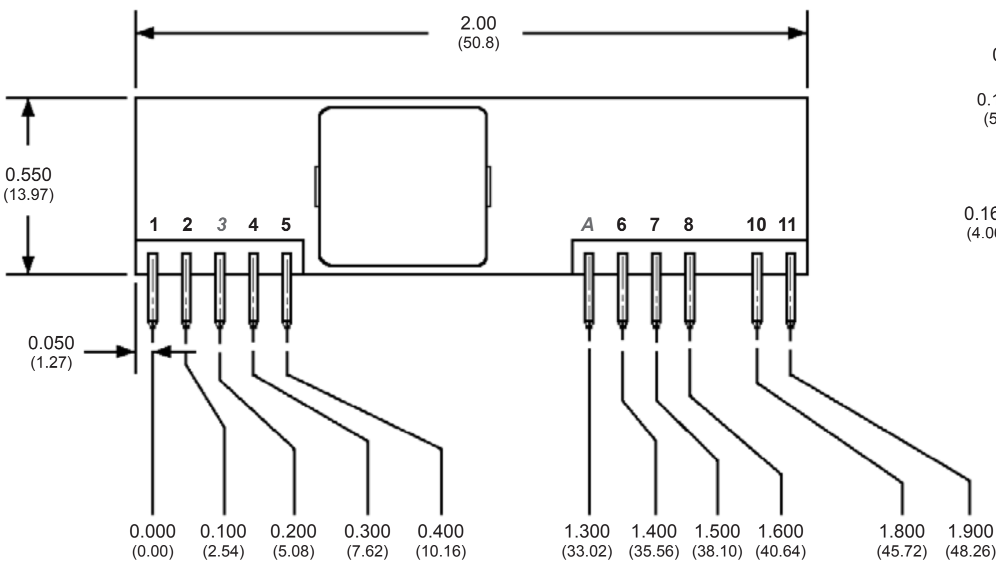
- UL 1950 recognized (US & Canada)
- TUV certified to EN60950
- Meets 72/23/EEC and 93/68/EEC directives which facilitates CE Marking in user's end product
- Board and plastic components meet UL94V-0 flammability requirements

* Final datasheet pending ECO review and signature.

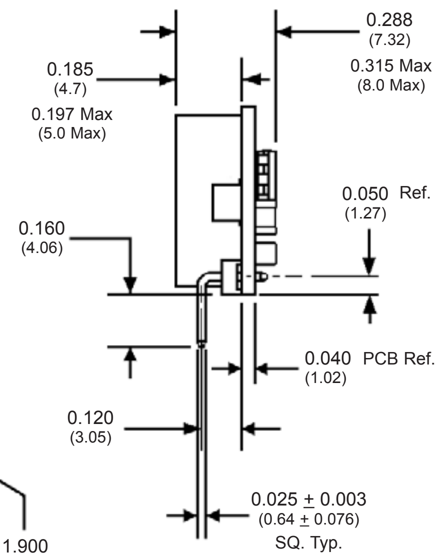
MECHANICAL DIAGRAM

Vertical Mount

Front View



Side View



NOTES

1) All pins are 0.025" (0.64mm) +/- 0.003 (0.076mm) square.
2) All Pins: Material - Copper Alloy
   Finish - Tin over Nickel plate
3) Vertical, horizontal, vertical with reverse pins and surface mount options (future) available.
4) Undimensioned components are shown for visual reference only.
6) All dimensions in inches (mm)
   Tolerances: x.xx +/-0.02 in. (x.x +/-0.5mm)
   x.xxx +/-0.010 in. (x.xx +/-0.25mm)
7) Weight: 0.30 oz. (9.4 g) typical
8) Workmanship: Meets or exceeds IPC-A-610C Class II

Pin Connection Notes:

1. Pin 10 - for fixed resistors, connect between Trim and Vout(+) to trim down or between Trim and Common (Ground) to trim up.
2. Pin 11 - see section on Remote ON/OFF pin for description of enable logic options.

PIN DESIGNATIONS

Pin No.	Name	Function
1	Vout(+)	Positive output voltage
2	Vout(+)	Positive output voltage
3	*SENSE(+)*	*Positive remote sense*
4	Vout(+)	Positive output voltage
5	Common	
A	*I share*	*Current share**
6	Common	
7	Vin(+)	Positive input voltage
8	Vin(+)	Positive input voltage[1]
10	TRIM	Output voltage trim[2]
11	ON/OFF	LOGIC input to turn the converter on and off.

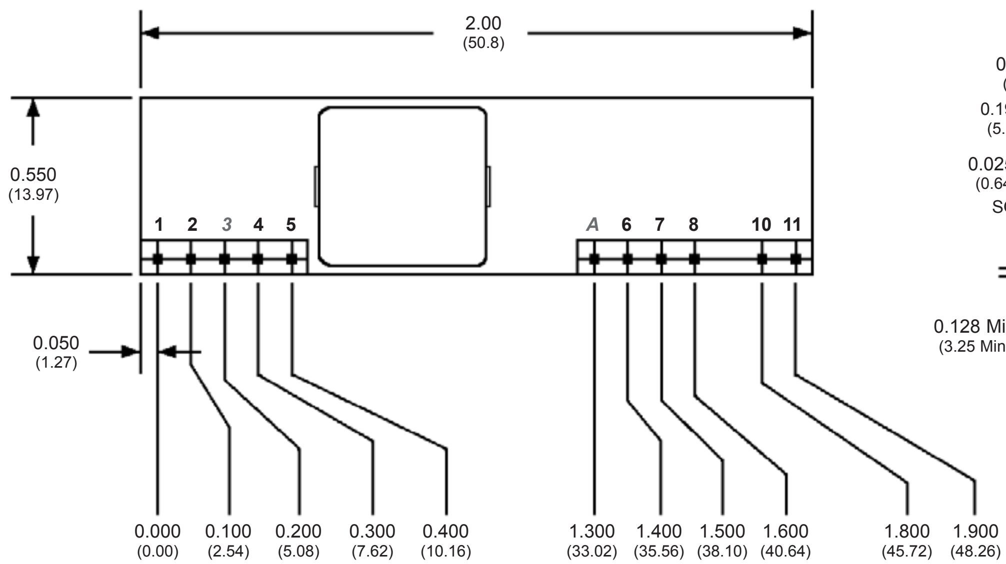
Pins in Italics Shaded text are Optional

* Contact factory for availability of current share modules.

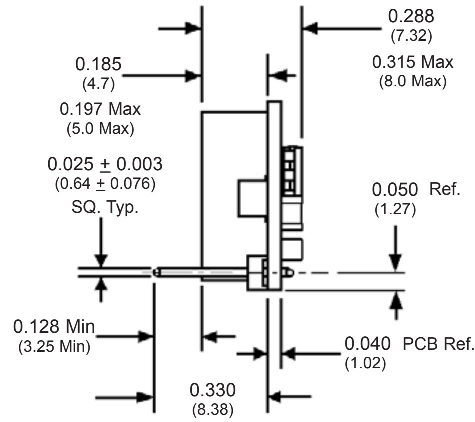
MECHANICAL DIAGRAM

Horizontal Mount

Front View



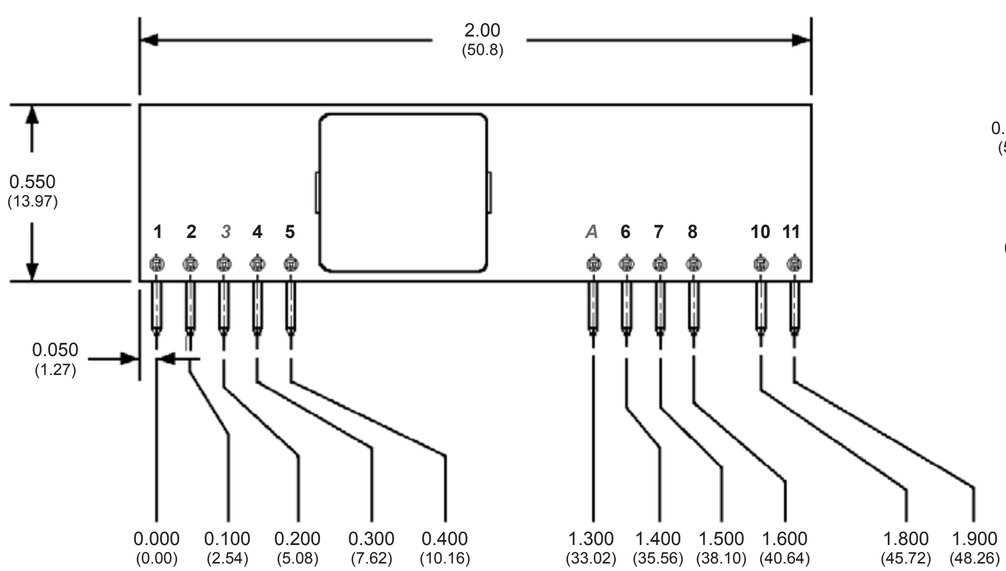
Side View



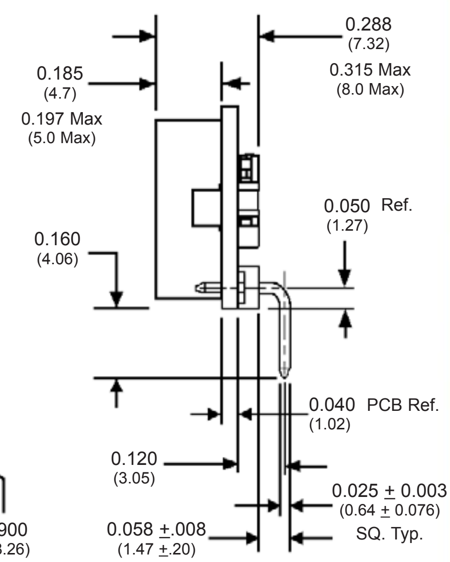
See note on Thermal Considerations in Applications section.

Vertical Mount Reversed Pins

Front View



Side View



ELECTRICAL CHARACTERISTICS - NQ12xxxVMA16 Series

T_A=25°C, airflow rate=300 LFM, V_{in}=12.0Vdc unless otherwise noted; full operating temperature range is -40°C to +105°C ambient temperature with appropriate power derating. Specifications subject to change without notice.

Parameter	Module	Min.	Typ.	Max.	Units	Notes & Conditions
ABSOLUTE MAXIMUM RATINGS						
Input Voltage						
Non-Operating	All	0		16	V	continuous
Operating	All			14.4	V	continuous
Operating Temperature	All	-40		105	°C	
Storage Temperature	All	-55		125	°C	
Voltage at ON/OFF input pin	All	-3		15	V	
INPUT CHARACTERISTICS						
Operating Input Voltage Range	All	9.6	12	14.4	V	
Input Under-Voltage Lockout						
Turn-On Voltage Threshold	All	8.25	8.75	9.2	V	
Turn-Off Voltage Threshold	All	7.25	7.75	8.1	V	
Lockout Hysteresis	All		1.0		V	
Maximum Input Current	0.9V			2.25	A	9.6Vin, 100% Load, 10% trim up (all)
	1.0V			2.5	A	
	1.2V			3.0	A	
	1.5V			3.5	A	
	1.8V			4.0	A	
	2.0V			4.4	A	
	2.5V			5.5	A	
	3.3V			7.0	A	
	5.0V			10.5	A	
No-Load Input Current	0.9V		0.026	0.031	A	
	1.0V		0.028	0.033	A	
	1.2V		0.031	0.037	A	
	1.5V		0.037	0.045	A	
	1.8V		0.043	0.052	A	
	2.0V		0.048	0.059	A	
	2.5V		0.062	0.075	A	
	3.3V		0.084	0.101	A	
	5.0V		0.116	0.140	A	
Disabled Input Current	All		4	10	mA	
Inrush Current Transient Rating	All			0.1	A^2s	with minimum output capacitance
Response to Input Transient	0.9-2.5V		5		mV/V	80V/ms input transient (all)
	3.3V		7.5		mV/V	
	5.0V		12.5		mV/V	
Input Reflected-Ripple Current	0.9V		25	49	mA	pk-pk thru 1μH inductor; Fig 15-16 (all)
	1.0V		28	54	mA	
	1.2V		35	63	mA	
	1.5V		45	76	mA	
	1.8V		50	89	mA	
	2.0V		54	96	mA	
	2.5V		65	112	mA	
	3.3V		80	131	mA	
	5.0V		100	147	mA	
Recommended Input Fuse	All			15	A	fast blow external fuse recommended
Input Filter Capacitor Value	All		45		μF	internal ceramic

ELECTRICAL CHARACTERISTICS (continued) - NQ12xxxVMA16 Series

Parameter	Module	Min.	Typ.	Max.	Units	Notes & Conditions
INPUT CHARACTERISTICS (cont.)						
Input Ripple Voltage	0.9V		34		mV	RMS, full load, Figures 15, 17 (all)
	1.0V		37		mV	
	1.2V		44		mV	
	1.5V		53		mV	
	1.8V		61		mV	
	2.0V		66		mV	
	2.5V		77		mV	
	3.3V		91		mV	
	5.0V		107		mV	
OUTPUT CHARACTERISTICS						
Output Voltage Set Point	0.9V	0.888	0.900	0.912	V	12Vin; 50% load (all)
	1.0V	0.987	1.000	1.013	V	
	1.2V	1.184	1.200	1.216	V	
	1.5V	1.481	1.500	1.520	V	
	1.8V	1.777	1.800	1.823	V	
	2.0V	1.974	2.000	2.026	V	
	2.5V	2.468	2.500	2.533	V	
	3.3V	3.257	3.300	3.343	V	
	5.0V	4.935	5.000	5.065	V	
Output Voltage Regulation						
Over Line	All		TBD	3	mV	
Over Load	All		TBD	7	mV	with sense pin
Over Temperature	All		±0.50	±1.50	%	
Total Output Voltage Range	0.9V	0.882		0.918	V	with sense pin, over sample, line, load, temperature & life (all)
	1.0V	0.980		1.020	V	
	1.2V	1.176		1.224	V	
	1.5V	1.470		1.530	V	
	1.8V	1.764		1.836	V	
	2.0V	1.960		2.040	V	
	2.5V	2.450		2.550	V	
	3.3V	3.234		3.366	V	
	5.0V	4.900		5.100	V	
Output Voltage Ripple and Noise (pk-pk\RMS)	0.9-2.5V		TBD\10	40\TBD	mV	Full load;20MHz bandwidth; Figs 15, 18
	3.3V		TBD\10	50\TBD	mV	
	5.0V		TBD\10	60\TBD	mV	
Operating Output Current Range	All	0		16	A	Subject to thermal derating; Figs 5-10
Output DC Over-Current Shutdown	All	17	22	27	A	Figure 23
Output Capacitance Range	All	0		5,000	µF	>2.5 mΩ ESR
DYNAMIC CHARACTERISTICS						
Input Voltage Ripple Rejection	0.9V		82		dB	120 Hz; Figure 20
	5.0V		72		dB	
Output Voltage during Load Current Transient						
For a Step Change in Output Current (0.1A/µs)	All		40	75	mV	50%-75%-50% Iout max, 10µF, Fig 13
For a Step Change in Output Current (3A/µs)	All		100		mV	50%-75%-50% Iout max, 470µF, Fig 14
Settling Time	All		50		µs	to within 1.5% Vout nom., Figs 13-14
Turn-On Transient						Figures 11-12
Turn-On Time	0.9-1.5V	0.75	1.5	2.5	ms	Full resistive load, Vout=100% nom. (all)
	1.8-5.0V	1	2	3	ms	
Output Voltage Overshoot	All		0	1	%	resistive load

ELECTRICAL CHARACTERISTICS (continued) - NQ12xxxVMA16 Series

Parameter	Module	Min.	Typ.	Max.	Units	Notes & Conditions
EFFICIENCY						
100% Load	0.9V		84		%	Figures 1-4
	1.0V		85		%	
	1.2V		86		%	
	1.5V		88		%	
	1.8V		89		%	
	2.0V		90		%	
	2.5V		92		%	
	3.3V		93		%	
	5.0V		95		%	
50% Load	0.9V		87		%	Figures 1-4
	1.0V		88		%	
	1.2V		89		%	
	1.5V		91		%	
	1.8V		91		%	
	2.0V		92		%	
	2.5V		93		%	
	3.3V		94		%	
	5.0V		95		%	
TEMP. LIMITS FOR POWER DERATING						
Semiconductor Junction Temperature	All			125	°C	Package rated to 150°C; Figs 5-10
Board Temperature	All			125	°C	UL rated max operating temp 130°C
FEATURE CHARACTERISTICS						
Switching Frequency	All	300	325	350	kHz	may drop by 10% at light load
ON/OFF Control						See Applications Information
Off-State Voltage	All			2.3	V	
On-State Voltage	All	2.65			V	
Pull-Up Voltage	All		Vin/2		V	
Pull-Up Resistance	All		10		kΩ	
Output Voltage Trim Range	All	-10		+10	%	Measured Vout+ to common pins; Table 1
Output Voltage Remote Sense Range	All			+10	%	Measured Vout+ to common pins
Output Over-Voltage Protection	0.9,1.0V	140	145	150	%	Over full temp range; % of nominal Vout
	1.2-5.0V	118	127	140	%	
Over-Temperature Shutdown	All		133		°C	Average PCB Temperature
Over-Temperature Shutdown Restart Hysteresis	All		12		°C	
RELIABILITY CHARACTERISTICS						
Calculated MTBF (Telcordia)	All		TBD		10^6 Hrs.	TR-NWT-000332; 100% load, 200LFM, 40°C T_a
Calculated MTBF (MIL-217)	All		TBD		10^6 Hrs.	MIL-HDBK-217F; 100% load, 200LFM, 40°C T_a
Field Demonstrated MTBF	All				10^6 Hrs.	See website for latest values

STANDARDS COMPLIANCE

Parameter	Notes
STANDARDS COMPLIANCE	
UL/cUL 60950	File # E194341
EN60950	Certified by TUV
72/23/EEC	
93/68/EEC	
Needle Flame Test (IEC 695-2-2)	test on entire assembly; board & plastic components UL94V-0 compliant
IEC 61000-4-2	ESD test, 8kV - NP, 15kV air - NP (Normal Performance)
GR-1089-CORE	Section 7 - electrical safety, Section 9 - bonding/grounding
Telcordia (Bellcore) GR-513	

- An external input fuse must always be used to meet these safety requirements. Contact SynQor for official safety certificates on new releases or download from the SynQor website.

QUALIFICATION TESTING

Parameter	# Units	Test Conditions
QUALIFICATION TESTING		
Life Test	32	95% rated V_{in} and load, units at derating point, 1000 hours
Vibration	5	10-55Hz sweep, 0.060" total excursion,1 min./sweep, 120 sweeps for 3 axis
Mechanical Shock	5	100g minimum, 2 drops in x and y axis, 1 drop in z axis
Temperature Cycling	10	-40°C to 100°C, unit temp. ramp 15°C/min., 500 cycles
Power/Thermal Cycling	5	$T_{operating}$ = min to max, V_{in} = min to max, full load, 100 cycles
Design Marginality	5	T_{min}-10°C to T_{max}+10°C, 5°C steps, V_{in} = min to max, 0-105% load
Humidity	5	85°C, 85% RH, 1000 hours, continuous Vin applied except 5min./day
Solderability	15 pins	MIL-STD-883, method 2003

- Extensive characterization testing of all SynQor products and manufacturing processes is performed to ensure that we supply robust, reliable product. Contact factory for official product family qualification document.

OPTIONS

SynQor provides various options for Packaging, Enable Logic, Pin Style and Feature Set for this family of DC/DC converters. Please consult the last page of this specification sheet for information on available options.

PATENTS

SynQor is protected under various patents, including but not limited to U.S. Patent numbers: 5,999,417; 6,222,742 B1; 6,594,159 B2; 6,545,890 B2.

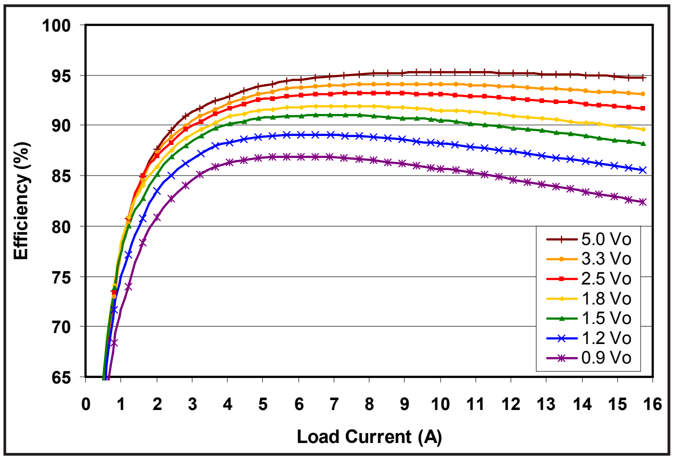


Figure 1: *Efficiency at nominal output voltage vs. load current for all modules at 25°C and nominal input voltage.*

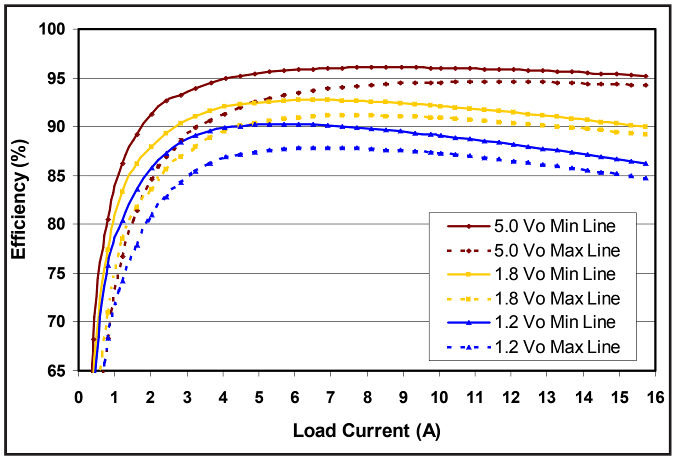


Figure 2: *Efficiency at min and max line vs. load current for 1.2Vo, 1.8Vo and 5.0Vo units at 25°C.*

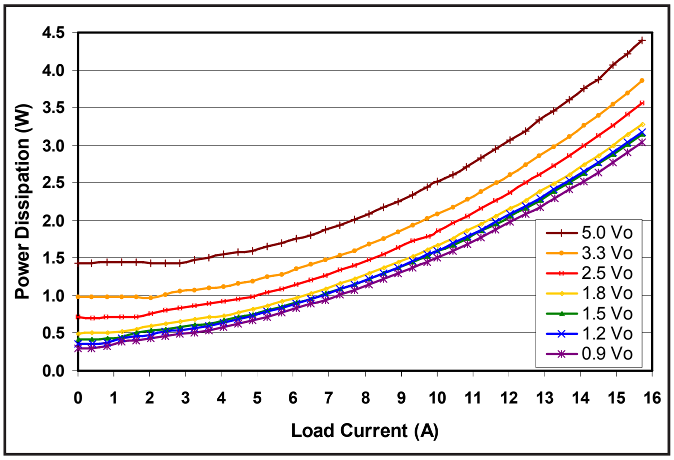


Figure 3: *Power dissipation at nominal output voltage vs. load current for all modules at 25°C and nominal input voltage.*

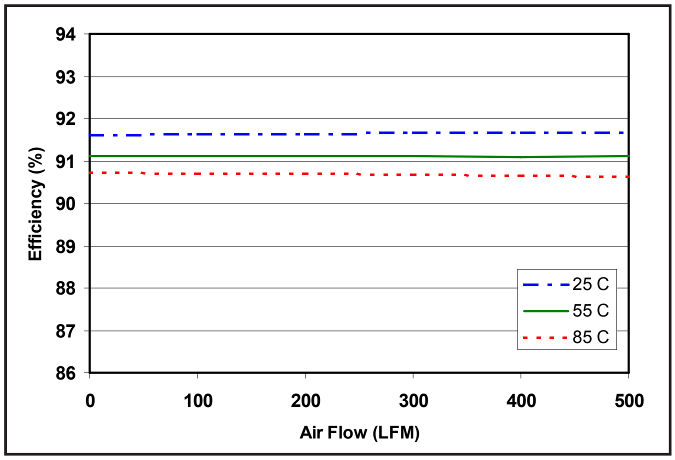


Figure 4: *Efficiency at 1.8Vout and 60% rated power vs. airflow rate for ambient air temperatures of 25°C, 55°C, and 85°C (nominal input voltage).*

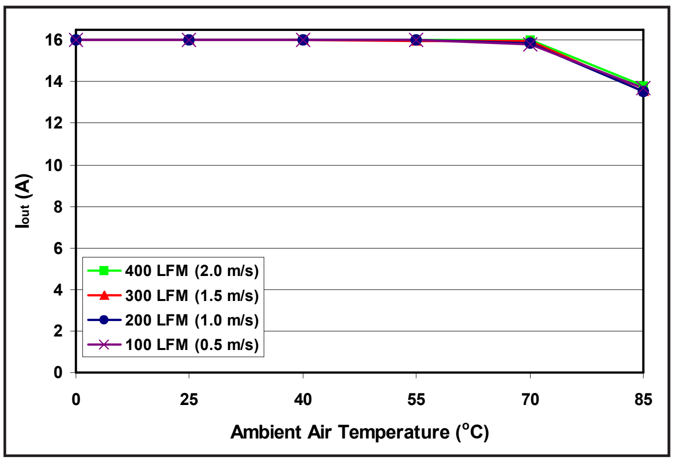


Figure 5: *Maximum output power derating curves vs. ambient air temp for **0.9Vo, 1.2Vo, 1.5Vo** units. Airflow rates of 100 - 400 LFM with air flowing across the converter from pin 11 to pin 1 (Vin nom, vert mount).*

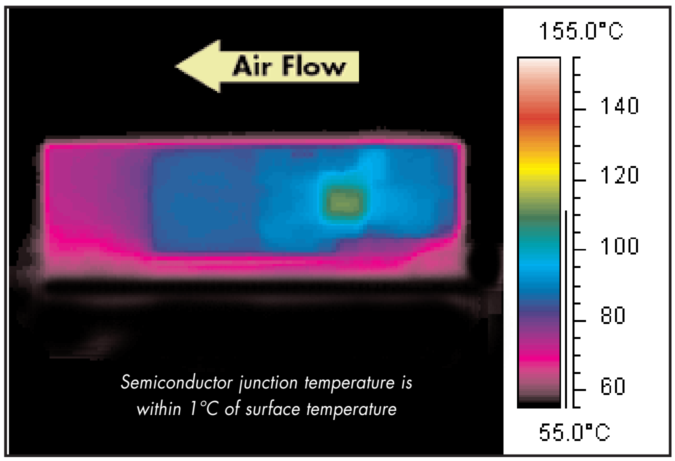


Figure 6: *Thermal plot of **0.9Vo, 1.2Vo, 1.5Vo** converters at 16 amp load current with 55°C air flowing at the rate of 200 LFM. Air is flowing across converter from pin 11 to pin 1 (Vin nom, vert mount).*

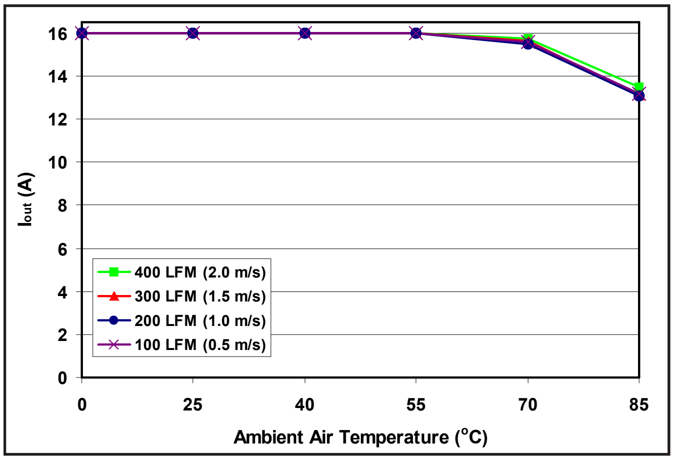


Figure 7: *Maximum output power derating curves vs. ambient air temp for* ***1.8Vo, 2.5Vo*** *units. Airflow rates of 100 - 400 LFM with air flowing across the converter from pin 11 to pin 1 (Vin nom, vert mount).*

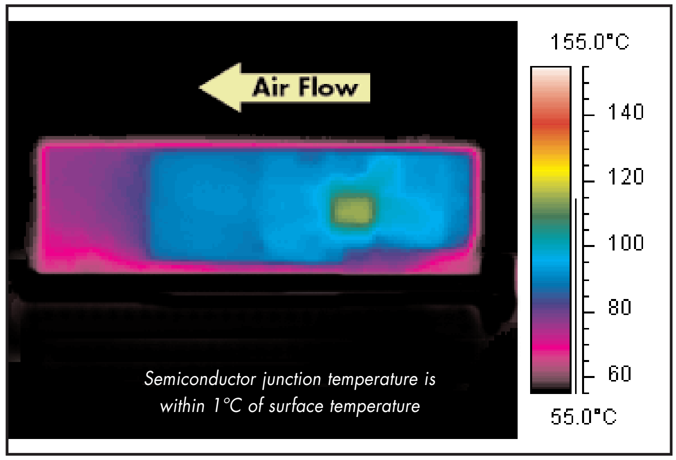


Figure 8: *Thermal plot of* ***1.8Vo, 2.5Vo*** *converters at 16 amp load current with 55°C air flowing at the rate of 200 LFM. Air is flowing across converter from pin 11 to pin 1 (Vin nom, vert mount).*

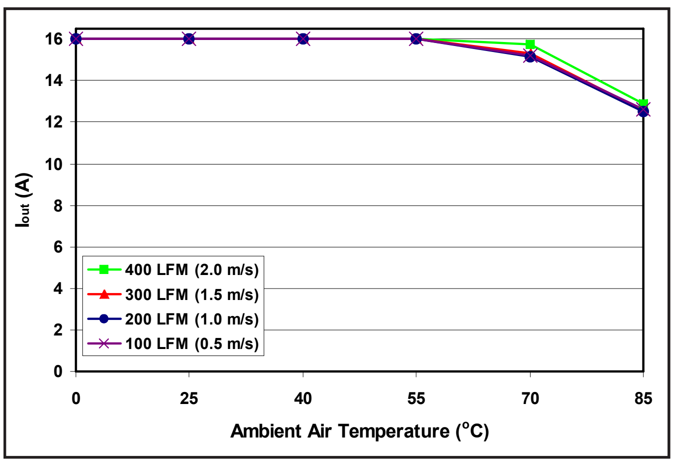


Figure 9: *Maximum output power derating curves vs. ambient air temp for* ***3.3Vo, 5.0Vo*** *units. Airflow rates of 100 - 400 LFM with air flowing across the converter from pin 11 to pin 1 (Vin nom, vert mount).*

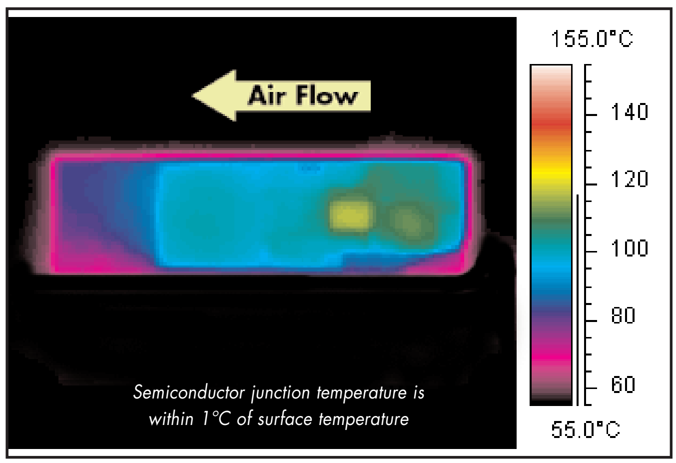


Figure 10: *Thermal plot of* ***3.3Vo, 5.0Vo*** *converters at 16 amp load current with 55°C air flowing at the rate of 200 LFM. Air is flowing across converter from pin 11 to pin 1 (Vin nom, vert mount).*

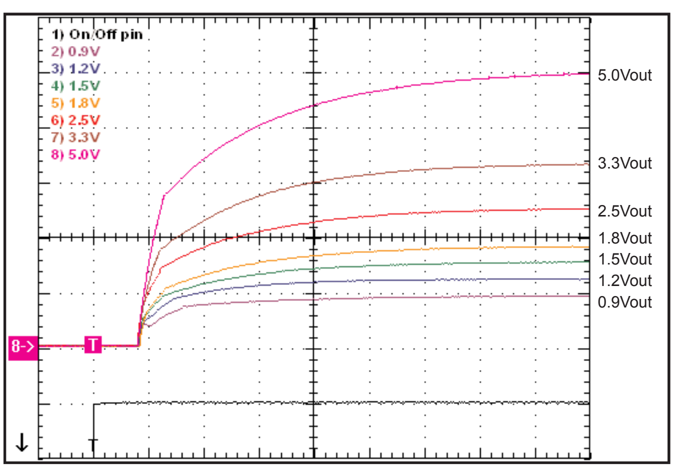


Figure 11: *Turn-on transient at full load (resistive load) (400 μs/div).*
Ch 1: ON/OFF input (5V/div)
Ch 2-8: Vout (1V/div)

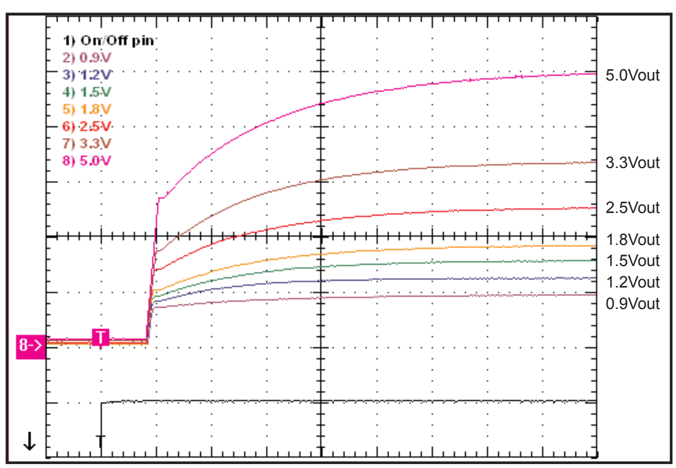


Figure 12: *Turn-on transient at zero load (400 μs/div).*
Ch 1: ON/OFF input (5V/div)
Ch 2-8: Vout (1V/div)

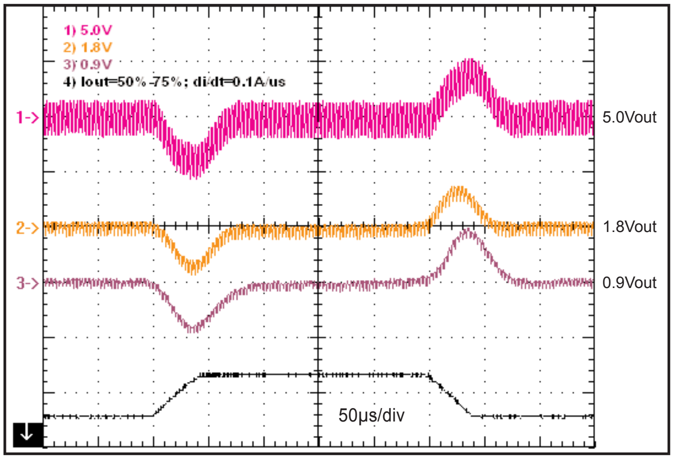


Figure 13: Output voltage response for **0.9V, 1.8V, 5.0V units** to step-change in load current (50-75-50% of Iout max; di/dt=0.1A/µs). Load cap: 15µF, 100mΩ ESR tant, 10µF cer. Ch 1: Iout (5A/div), Ch 2-4: Vout (50mV/div).

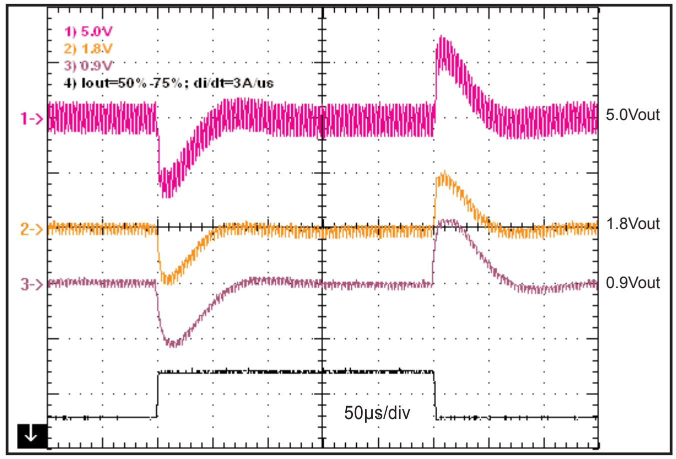


Figure 14: Output voltage response for **0.9V, 1.8V, 5.0V units** to step-change in load current (50-75-50% of Iout max; di/dt=3A/µs). Load cap: 470µF, 25mΩ ESR tant, 10µF cer. Ch 1: Iout (5A/div), Ch 2-4: Vout (50mV/div).

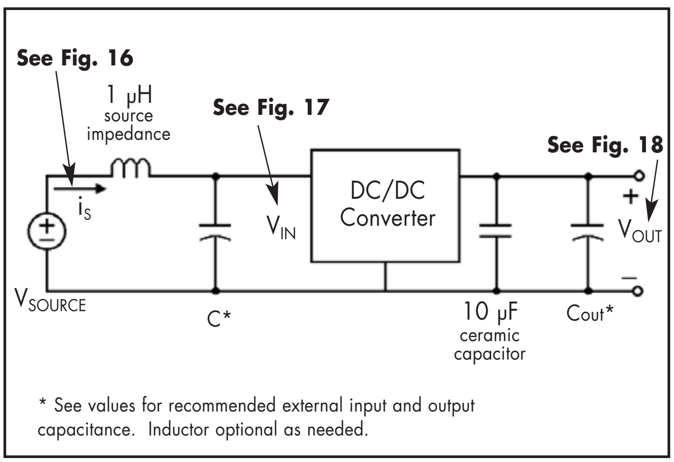


Figure 15: Test set-up diagram showing measurement points for Input Reflected Ripple Current (Figure 16), Input Terminal Ripple Voltage (Figure 17), and Output Voltage Ripple (Figure 18).

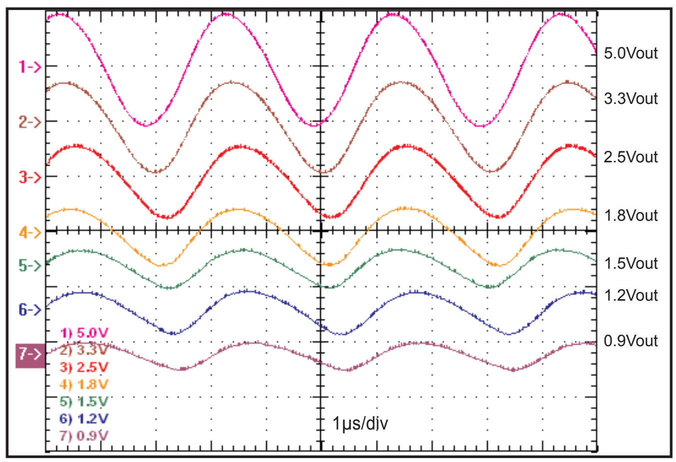


Figure 16: Input Reflected Ripple Current, i_S, through a 1 µH source inductor at nominal input voltage and rated load current (50 mA/div). See Figure 15.

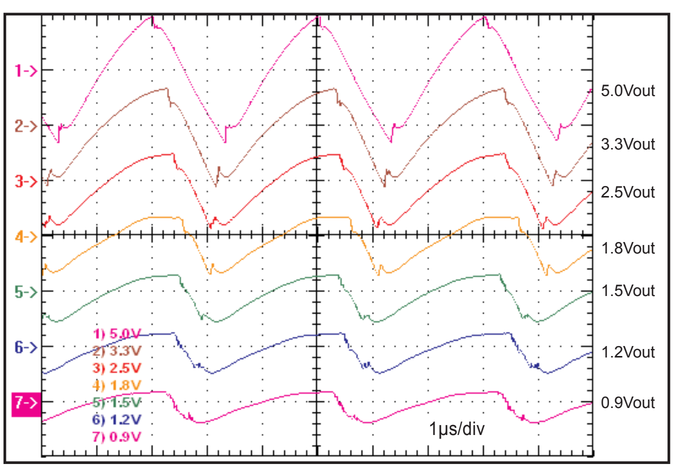


Figure 17: Input Terminal Ripple Voltage at nominal input voltage and rated load current (200 mV/div). Load capacitance: 10µF ceramic cap. Bandwidth: 20 MHz. See Figure 15.

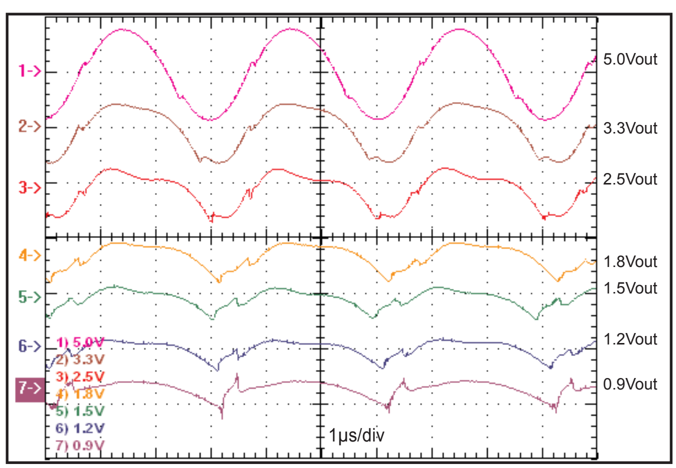


Figure 18: Output Voltage Ripple at nominal input voltage and rated load current (20 mV/div). Load capacitance: 10µF ceramic cap. Bandwidth: 20 MHz. See Figure 15.

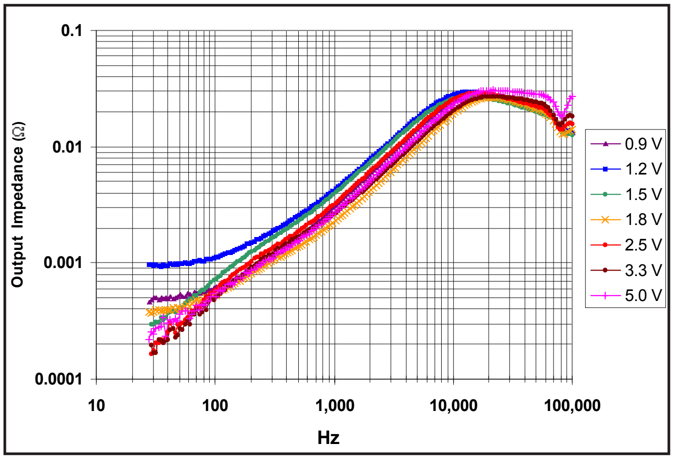


Figure 19: *Magnitude of incremental output impedance ($Z_{out} = v_{out}/i_{out}$) for nominal input voltage at full rated power.*

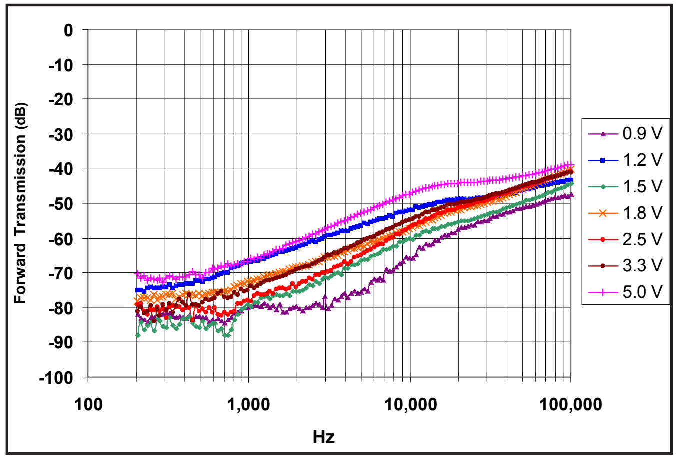


Figure 20: *Magnitude of incremental forward transmission ($FT = v_{out}/v_{in}$) for nominal input voltage at full rated power.*

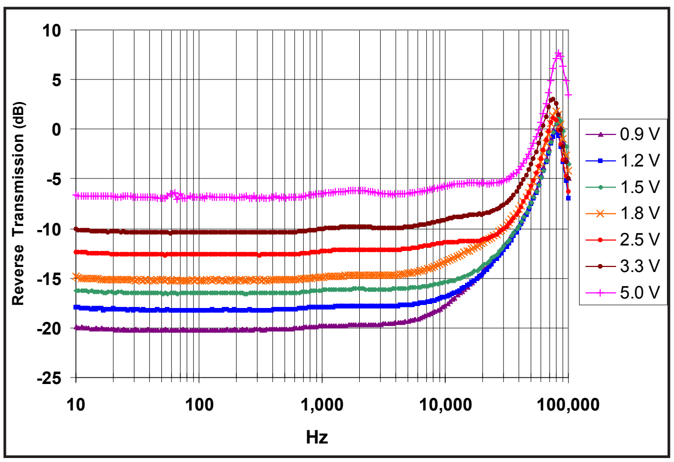


Figure 21: *Magnitude of incremental reverse transmission ($RT = i_{in}/i_{out}$) for nominal input voltage at full rated power.*

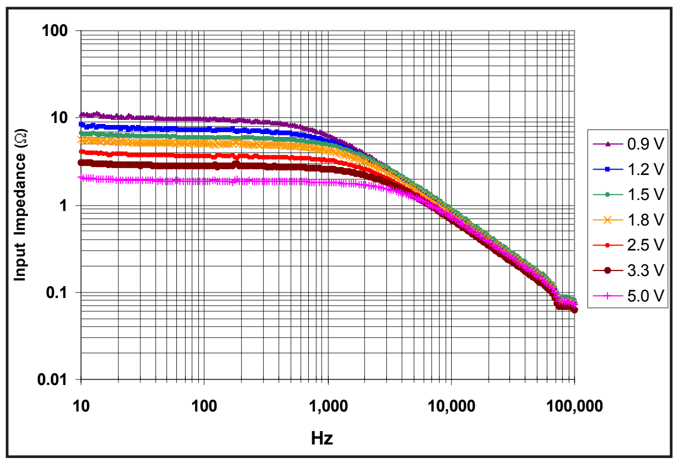


Figure 22: *Magnitude of incremental input impedance ($Z_{in} = v_{in}/i_{in}$) for nominal input voltage at full rated power.*

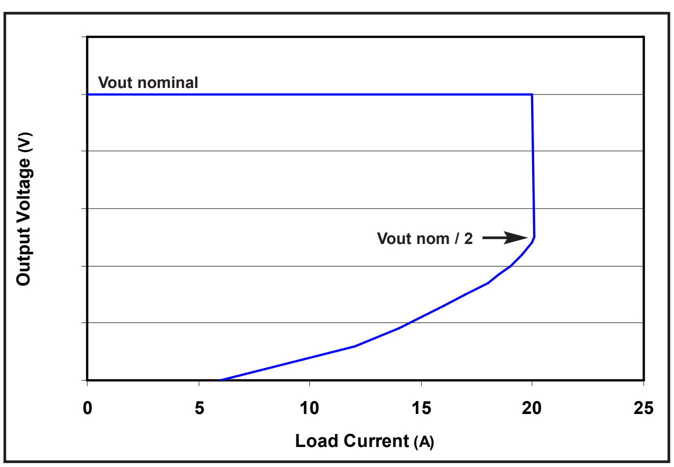


Figure 23: *Output voltage vs. load current showing current limit inception point and fold-back current limit behavior.*

BASIC OPERATION AND FEATURES

The *Ni*Qor series non-isolated converter uses a buck-converter that keeps the output voltage constant over variations in line, load, and temperature. The *Ni*Qor modules employ synchronous rectification for very high efficiency.

Dissipation throughout the converter is so low that it does not require a heatsink or metal baseplate for operation. The *Ni*Qor converter can thus be built more simply and reliably using high yield surface mount techniques on a single PCB substrate.

The *Ni*Qor series of SIPs and SMT converters uses the established industry standard footprint and pin-out configurations.

CONTROL FEATURES

REMOTE ON/OFF: The ON/OFF input permits the user to control when the converter is on or off. There is currently a single option available for the ON/OFF input described in the table below. Others may become available if demand exsists.

Option	Description	Pin-Open Float Voltage	Pin-Open Converter State	Pin Action
P Logic	Positive/Open	Vin / 2	On	Pull Low = Off

OUTPUT VOLTAGE TRIM: The TRIM input permits the user to adjust the output voltage up or down according to the trim range specifications by using an external resistor. If the TRIM feature is not being used, leave the TRIM pin disconnected.

TRIM-DOWN: To decrease the output voltage using an external resistor, connect the resistor $R_{trim\text{-}down}$ between the TRIM and the Vout or Sense+ pins accoring to Figure A.

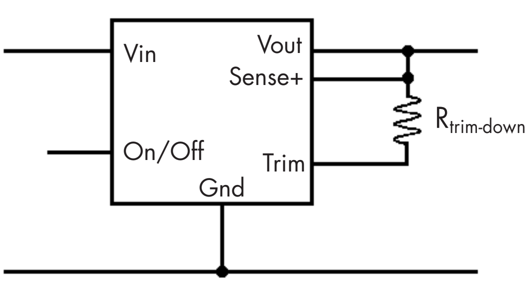


***Figure A:** Trim Down connection*

For a desired decrease of the nominal output voltage, the value of the resistor should be:

$$R_{trim\text{-}down} = 15000 * \frac{V_{DES} - 0.7}{V_{NOM} - V_{DES}} - 1000 \quad (\Omega)$$

or

$$V_{OUT} = \frac{V_{NOM} * (R_{trim\text{-}down} + 1000) + 10500}{R_{trim\text{-}down} + 16000} \quad (\Omega)$$

where V_{NOM} = Nominal Output Voltage

V_{DES} = Desired Output Voltage

TRIM-UP: To increase the output voltage using an external resistor, connect the resistor $R_{trim\text{-}up}$ between the TRIM and the Ground pin accoring to Figure B.

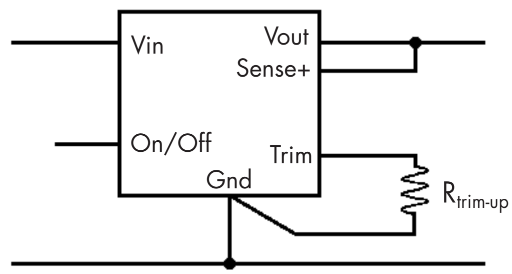


***Figure B:** Trim Up connection*

For a desired increase of the nominal output voltage, the value of the resistor should be:

$$R_{trim\text{-}up} = \frac{10500}{V_{DES} - V_{NOM}} - 1000 \quad (\Omega)$$

or

$$V_{OUT} = V_{NOM} + \frac{10500}{R_{trim\text{-}up} + 1000} \quad (\Omega)$$

where V_{NOM} = Nominal Output Voltage

V_{DES} = Desired Output Voltage

To maintain the accuracy of the output voltage over load current, it is vital that any trim-up resistor be terminated directly to the converter's ground foot, not at the connection to the load. A separate Kelvin connection to the PCB pad for the ground foot is optimal. Trim-down resistors should be terminated at the converter's Sense+ pin.

We do <u>not</u> recommend bypassing the trim pin directly to ground with a capacitor. The voltage gain from the trim pin to output is rather large, 15:1. Ground bounce through a bypass capacitor could introduce significant noise into the converter's control circuit.

PROTECTION FEATURES

Input Under-Voltage Lockout: The converter is designed to turn off when the input voltage is too low, helping avoid an input system instability problem, described in more detail in the application note titled "Input System Instability". The lockout circuitry is a comparator with DC hysteresis. When the input voltage is rising, it must exceed the typical Turn-On Voltage Threshold value (listed on the specification page) before the converter will turn on. Once the converter is on, the input voltage must fall below the typical Turn-Off Voltage Threshold value before the converter will turn off.

Output Current Limiting: The NQ12 family of converters employs foldback current limiting. A typical output voltage-current curve is shown in Figure 23 in the Performance Curves section. Current limit is reached at about 135% of rated current. Loads in excess of that limit will cause the output to droop. If the load is sufficient to pull the output down to roughly 1/2 of its nominal setpoint, foldback will ensue. From there, as the load is further increased, the output current will decrease linearly to about 1/3 of rated current at zero Vout. Thus, operating into a dead short, the unit will deliver 1/3 rated current indefinitely. This reduces stress on the converter and ensures that prolonged short-circuits will not overheat the converter.

Since there is no "hiccup mode" to the current-limit operation, there is also no concern with operation or startup into large capacitive loads. The voltage may rise slowly while charging the output capacitance, but it will rise.

There are also no problems starting into a load that has a resistive V-I curve. As long as the load draws less than the current limit value at 1/2 of the unit's setpoint voltage, proper startup is ensured.

Internal Over-Voltage Protection: To fully protect from excessive output voltage, the NQ12 series contains two levels of Output Over-Voltage Shutdown circuitry.

The first type monitors the output at the load via the Sense+ pin (or the output if Sense+ is left open). If the sensed voltage exceeds the (optionally trimmed) setpoint by ~10%, this protective circuit asserts the converter's low-side switch until the output returns to normal. This circuit tracks the trimmed setpoint; the +10% threshold is maintained over the wide trim range of the T50 model. This circuit can also be benignly activated during the response to a large, fast drop in load current. In this instance the converter's normal transient response is momentarily overridden by this OVP. The result is a slight asymmetry in the converter's observed transient response.

It should be noted that there is no limit on this OVP; if a powerful external source attempts to raise the output of an NQ12 converter beyond 110% of its setpoint, the converter will sacrifice itself trying to draw down that external source and protect its load from the overvoltage.

The second Output Over-Voltage Shutdown circuit independently compares the voltage at the converter's output pin with that of a redundant reference. If the output ever exceeds ~125% of nominal setpoint, both converter switches are disabled. After the output voltage returns to normal, a softstart cycle is initiated.

This OVP is independent of the trimmed setpoint. As such, the converter's load is protected from faults in the external trim circuitry (such as a trim pin shorted to ground). Since the setpoint of this OVP does not track trim, it is set at 125% of 5.0V, or 6.2V, in the wide-trim T50 model.

Over-Temperature Shutdown: A temperature sensor on the converter senses the average temperature of the module. The thermal shutdown circuit is designed to turn the converter off when the temperature at the sensed location reaches the Over-Temperature Shutdown value. It will allow the converter to turn on again when the temperature of the sensed location falls by the amount of the Over-Temperature Shutdown Restart Hysteresis value.

APPLICATION CONSIDERATIONS

Input Filtering/Capacitance/Damping: The filter circuit of Figure C is often added to the converter's input to prevent switching noise from reaching the input voltage bus.

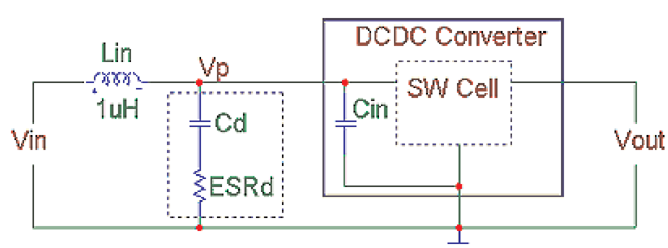


Figure C: *NQ12 converter with Input Filter*

In the VMA16 (SIP) converters Cin = 45µF of high quality ceramic capacitors. With Lin of 1µH, Cd should be 100-200µF and Rd should be 0.1-0.2Ω, in most applications. For more information on designing the input filter and choosing proper values, contact SynQor technical support.

With the values listed above, the ripple current in L1 will be below 60mA RMS for all units. The full-load worst-case filter operation is summarized in Table 1.

Vout Model	SW Cell Current (A RMS)	VMA16 Vp Ripple (V RMS)	VMA16 Ripple Current in L1 (mA RMS)
0.9	5.2	0.04	21
1.0	5.5	0.04	22
1.2	6.0	0.05	27
1.5	6.7	0.06	31
1.8	7.4	0.07	36
2.0	7.8	0.08	39
2.5	8.8	0.09	46
3.3	10.4	0.10	54
5.0	12.3	0.11	57

Table 1: *Full Load Input Filter Performance, VMA16*

Adding significant external pure ceramic capacitance directly across the converter's input pins is not recommended. Parasitic inductance associated with the input pin geometry and PCB traces can create a high-Q CLC circuit with any external capacitors. Just a few nano-Henries of parasitic inductance can create a resonance (or an overtone) near the converter's switching frequency. Cin has a reactance of 10-20mΩ at the 330kHz switching frequency. To avoid this high-frequency resonance, any external input filter should exhibit a net source impedance of at least 20mΩ resistive through this frequency range. This requirement is easily met with the damping elements discussed above. Adding a small amount (a few μF) of high-frequency external ceramic will not violate it.

If using converters at higher powers, do consider the ripple current rating of Cd. Contact SynQor technical support for more information.

Output Capacitance: The VMA16 series does not require any external output capacitance. In many applications, however, additional external output capacitance is required to reduce the response to load transients to an allowable level.

The output impedance of these converters can be quite accurately modeled from DC to about 100kHz as shown in Figure D. A further simplified version of it, valid below 40Hz and above 1kHz, is shown in Figure E. In diagram, Ct represents the minimum recommended output capacitance, and its resistance Rt.

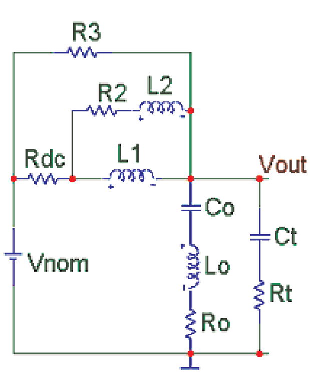


Figure D: *NQ12 Passive Output Model*

However, in the VMA16 family, that capacitor is again included in the converter.

If the dynamic characteristics of the load are known, any standard simulator can use these models to predict the in-circuit transient response.

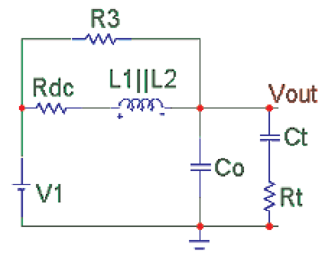


Figure E: *Simplified NQ12 Output Model*

Vnom	0.9	1.0	1.2	1.5	1.8	2.5	3.3	5.0
Rdc	360μΩ		490μΩ	450μΩ	365μΩ	300μΩ	330μΩ	510μΩ
L1	650nH		920nH	1.08μH	590nH	775nH	675nH	650nH
R2	4mΩ		4mΩ	4mΩ	2.0mΩ	3mΩ	2mΩ	1.5mΩ
L2	1.56μH		1.4μH	1.4μH	850nH	1.03μH	706nH	840nH
R3	29mΩ		31mΩ	32mΩ	35mΩ	30mΩ	29mΩ	35mΩ
Co	60μF		50μF	60μF	50μF	30μF	30μF	20μF
Lo	30nH		30nH	30nH	50nH	75nH	75nH	145nH
Ro	15mΩ		20mΩ	15mΩ	20mΩ	23mΩ	23mΩ	33mΩ

Table 2: *Component Values for Passive Output Models*

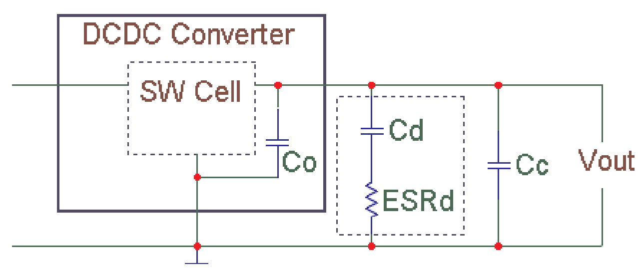


Figure F: *Converter with Additional Output Capacitance*

If the transients are current steps then Table 3 provides values for ESRd and Cd for different allowable responses. The allowable step response is normalized to a 1A step, and the maximum allowable value of ESRd can be read from the table.

For minimal overshoot upon recovery, Cd should be related to the minimum in-circuit net ESR.

$$Cd > \frac{\text{parallel (L1, L2)}}{ESRd_{min}^{2}}$$

The third column in Table 3 gives Cdmin for a 40% reduction in ESR and the highest L1||L2 value in Table 2. Cc represents external ceramic capacitance that may be added. It has no appreciable ESR and provides no damping to the output. For more detailed derivations of these values, contact SynQor technical support.

Load Current Step Response	External Capacitor	
Pk mV/Amp	ESRd max	Cd min (uF)
23	0.1*	100*
19	0.05	400
14	0.025	1,600
9	0.0125	6,400
5	0.00625	25,600

* Included in VMA, min recommended for SMA

Table 3: *External Capacitor Values for Different Step Responses*

Thermal Considerations: For vertical mount applications at elevated temperatures that call for forced air cooling (see thermal derating curves), the preferred airflow direction is from pin 11 to pin 1, as indicated in the thermal images provided. If airflow is in the opposite direction (pin 1 to pin 11) the power devices will run hotter by about 5 °C (corresponding to an additional 1 ampere of load derating at conditions where derating occurs).

For horizontal mount applications (NQ12xxxHMA parts), where the inductor and power devices are facing down, the preferred airflow direction is into the leading edge opposite the pin header edge, such that air flowing under the *Ni*Qor PCB flows out between the pins and the inductor. With this airflow direction, and with the inductor firmly contacting the application board, the user can apply the thermal derating curves provided herein for vertical mount with airflow from pin 11 to pin 1. Airflows in other directions across the horizontally mounted *Ni*Qor will result in temperatures that are higher by about 5 °C with pin 11 to pin 1 airflow and about 10 °C with pin 1 to pin 11 airflow. Also, temperature increases of up to 10 °C (2 Amp lower derating) can be expected if the inductor thermal interface does not make good contact to the customer's circuit board.

Layout Suggestion: When using a fixed output *Ni*Qor converter, the designer may chose to use the trim function and would thus be required to reserve board space for a trim resistor. It is suggested that even if the designer does not plan to use the trim function, additional space should be reserved on the board for a trim resistor. This will allow the flexibility to use the wide output voltage trim range *Ni*Qor module at a later date. This module is especially useful if output voltages need trimming or are subject to change over the life of the design.

OPTIONAL FEATURES

REMOTE SENSE(+) (Pin 3 - Optional): The optional SENSE(+) input corrects for voltage drops along the conductors that connect the converter's output pins to the load.

Pin 3 should be connected to Vout(+) at the point on the board where regulation is desired. A remote connection at the load can adjust for a voltage drop only as large as that specified in this datasheet, that is

$$\text{Vout(+)} - \text{Vsense(+)} \leq \text{Sense Range \% x Vout}$$

Pin 3 must be connected for proper regulation of the output voltage. If these connections are not made, the converter will deliver an output voltage that is slightly higher than its specified value.

Note: the output over-voltage protection circuit senses the voltage across the output (pins 1, 2 and 4) to determine when it should trigger, not the voltage across the converter's sense lead (pin 3).

CURRENT SHARE (Pin A - Optional): Additional information on the current share feature will be provided in a future revision of this technical specification. Please contact SynQor engineering support for further details.

PART NUMBERING SYSTEM

The part numbering system for SynQor's *Ni*Qor DC/DC converters follows the format shown in the example below.

NQ 12 025 V M A 16 P R S

- NQ – Product Family
- 12 – Input Voltage
- 025 – Output Voltage
- V – Packaging (see Order Info)
- M – Performance Level
- A – Thermal Design
- 16 – Output Current
- P R S – Options (see Ordering Information)

The first 12 characters comprise the base part number and the last 3 characters indicate available options. Although there are no default values for packaging, enable logic, pin length and feature set, the most common options are vertical mount SIP (V), Positive/Open logic (P), 0.160" pins (R) and Sense feature set (S). These part numbers are more likely to be readily available in stock for evaluation and prototype quantities.

Application Notes

A variety of application notes and technical white papers can be downloaded in pdf format at www.synqor.com.

ORDERING INFORMATION

The tables below show the valid model numbers and ordering options for converters in this product family. When ordering SynQor converters, please ensure that you use the complete 15 character part number consisting of the 12 character base part number and the additional 3 characters for options.

Model Number	Input Voltage	Output Voltage	Max Output Current
NQ12009*p*MA16*xyz*	9.6 - 14.4 V	0.9 V	16 A
NQ12010*p*MA16*xyz*	9.6 - 14.4 V	1.0 V	16 A
NQ12012*p*MA16*xyz*	9.6 - 14.4 V	1.2 V	16 A
NQ12015*p*MA16*xyz*	9.6 - 14.4 V	1.5 V	16 A
NQ12018*p*MA16*xyz*	9.6 - 14.4 V	1.8 V	16 A
NQ12020*p*MA16*xyz*	9.6 - 14.4 V	2.0 V	16 A
NQ12025*p*MA16*xyz*	9.6 - 14.4 V	2.5 V	16 A
NQ12033*p*MA16*xyz*	9.6 - 14.4 V	3.3 V	16 A
NQ12050*p*MA16*xyz*	9.6 - 14.4 V	5.0 V	16 A
NQ12T50*p*MA16*xyz* *	9.6 - 14.4 V	0.9-5.0 V	16 A

* Represents the wide trim unit. Details for this module are located in a separate datasheet located on the SynQor website.

The following option choices must be included in place of the *p x y z* spaces in the model numbers listed above.

Packaging: *p*	Options Description: *x y z*		
Packaging	**Enable Logic**	**Pin Style**	**Feature Set**
V - Vert. Mount SIP H - Horz. Mount SIP	P - Positive/Open	R - 0.160" (Standard) V - 0.160" (Vert Reversed)	S - Sense (Std.) N - None

Contact SynQor for further information:

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Toll Free: 888-567-9596
Fax: 978-849-0602
E-mail: sales@synqor.com
Web: www.synqor.com
Address: 155 Swanson Road
Boxborough, MA 01719

Warranty

SynQor offers a three (3) year limited warranty. Complete warranty information is listed on our web site or is available upon request from SynQor.