

**Features:**

- This Design Tested to 100 krad (Si) Total Dose
- Hermetically Sealed in Surface Mount Package
- Low On-resistance
- 2A Continuous Output Current
- Operation over Full Military Temperature Range -55°C to +125°C
- Optically Coupled
- Input/Output Isolation Tested to 1000 VDC
- Shock and Vibration Resistance

**Applications:**

- Satellite/Space Systems
- Military/High Reliability Systems
- Power Distribution/Switching
- Solenoid Driver
- Stepper Motor Driver
- Switching Heaters

**DESCRIPTION**

The 53246 is a SPST, radiation tolerant, DC solid-state relay (SSR) designed for military and space applications. This light-weight device is resistant to damage from severe shock and vibration, and is immune to contact related problems inherent in electro-mechanical relays. The SSR is enclosed in a hermetic metal package to ensure reliability in harsh environments. Effective isolation of 1000 VDC between control and load circuits is achieved through the use of optical coupling.

Functionally, the device operates as a single-pole single-throw, normally open (1 Form A) DC solid-state relay. The SSR is actuated by an input current of 5 to 15 mA, which can be supplied from standard logic types such as a standard TTL device. Output is provided by a power MOSFET exhibiting very low  $R_{DS(ON)}$  and capable of carrying a continuous current of 2 amperes. This design has demonstrated it will function with minimal degradation after exposure to 100 krad (Si) total dose. The 53246 is available in a variety of quality levels from COTS to class K including any custom screening requirements. The basic data sheet part is environmentally screened to H level in accordance with Table C-IX of MIL-PRF-38534.

**ABSOLUTE MAXIMUM RATINGS**

Output Voltage .....	50 VDC
Continuous Output current .....	2 A
Peak Output Current <sup>(1)</sup> .....	19 A
Storage Temperature Range .....	-65°C to +150°C
Operating Junction Temperature .....	-55°C to +150°C
Lead Solder Temperature, for 10 seconds .....	300°C
Continuous Input Current .....	20 mA
Peak Input Current <sup>(2)</sup> .....	100 mA
Reverse Input Voltage .....	6 VDC
Power Dissipation <sup>(3)</sup> .....	20 W
Linear Derating Factor .....	0.15 W/°C

**WEIGHT:** 3.7 grams (typical)

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**RECOMMENDED OPERATING CONDITIONS:**

Parameter	Symbol	Min.	Max.	Units
Output Voltage	$V_{O(OFF)}$		40	VDC
Continuous Output Current	$I_{O(ON)}$		2	A
Input Current (on)	$I_{F(ON)}$	5	15	mA
Input Voltage (off)	$V_{F(OFF)}$	0	1	VDC
Operating Case Temperature	$T_C$	-55	125	°C

**ELECTRICAL SPECIFICATIONS (Pre-Irradiation)**

$T_C = -55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$  unless otherwise specified

Parameter	Symbol	Min.	Typ.*	Max.	Units	Test Conditions	Notes
Output On-Resistance	$R_{(ON)}$	—	.032	.060	$\Omega$	$I_F = 15\text{ mA}$ $I_O = 1\text{ A}$	
Output Leakage Current	$I_{O(OFF)}$	—	<1	250	$\mu\text{A}$	$V_F = 1\text{ VDC}$ $V_O = 50\text{ VDC}$	
Input Forward Voltage	$V_F$	3.10	3.75	4.20	VDC	$I_F = 15\text{ mA}$	
Input Reverse Breakdown Voltage	$V_R$	6	40	—	VDC	$I_R = 10\ \mu\text{A}$	
Input-Output Leakage	$I_{I-O}$	—	—	1	$\mu\text{A}$	$RH \leq 45\%$ , $t = 5\text{ s}$ $V_{I-O} = 1000\text{ VDC}$ $T_C = 25^{\circ}\text{C}$	4, 5
Turn-On Time	$t_{ON}$	—	2.8	8.0	ms	$I_F = 15\text{ mA}$ $I_O = 10\text{ A}$ $V_{\text{load}} = 40\text{ VDC}$ Pulse width = 10 ms Duty cycle $\leq 1\%$	Figures 3, 4
Turn-Off time	$t_{OFF}$	—	61	80	ms		Figures 3, 5
Rise Time	$t_R$	—	2.1	6.0	ms		6, Figure 3
Fall Time	$t_F$	—	31	50	ms		
Thermal Resistance (junction-case)	$\theta_{JC}$	—	5.5	—	°C/W		
Output Off-Capacitance	$C_{O(OFF)}$	—	1.8	—	nF	$V_O = 28\text{ VDC}$ $f = 1\text{ MHz}$	
Input Capacitance	$C_{IN}$	—	27	—	pF	$V_F = 0\text{ V}$ $f = 1\text{ MHz}$	
Input-Output Capacitance	$C_{I-O}$	—	3.8	—	pF	$V_{I-O} = 0\text{ V}$ $f = 1\text{ MHz}$	4

\* All typical values are at  $T_C = 25^{\circ}\text{C}$

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**Notes:**

1. Non-repetitive, pulse width  $\leq 10$  ms,  $T_C = 25^\circ\text{C}$  (see Figure 6).
2. Non-repetitive, pulse width  $\leq 100$   $\mu\text{s}$ ,  $T_C = 25^\circ\text{C}$ .
3. Case Temperature  $T_C = 25^\circ\text{C}$  (see Figure 7).
4. Input pins shorted together and output pins shorted together.
5. Input-output potential applied momentarily, not an operating condition.
6. Rise time is measured from 10% to 90% of load current (90% to 10% of  $V_O$ ). Fall time is measured from 90% to 10% of load current (10% to 90% of  $V_O$ ).

**CAUTION:**

Care should be taken so as not to exceed the maximum power dissipation and maximum junction temperature when repetitively switching loads.

INPUT	OUTPUT
ON	ON
OFF	OFF

Figure 1. Truth Table

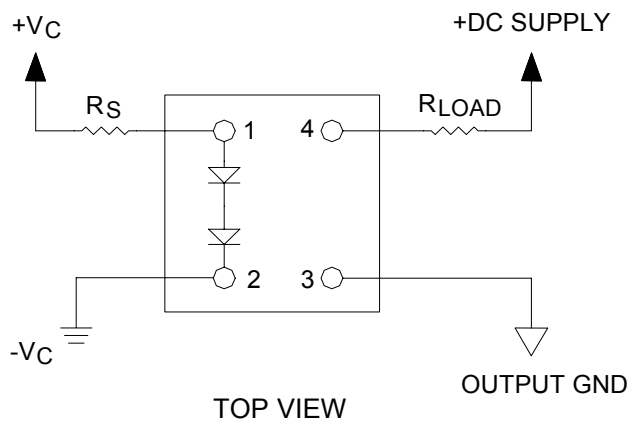


Figure 2. Terminal Connections

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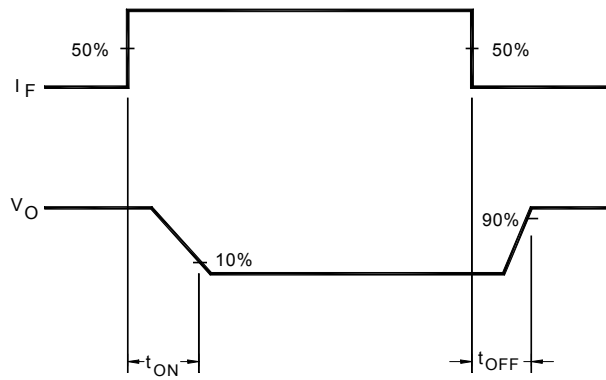
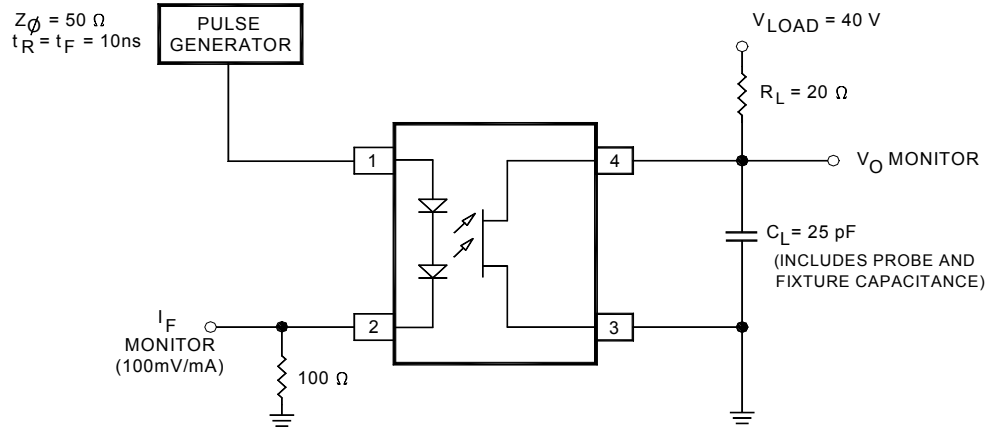


Figure 3. Switching Waveforms and Test Circuits

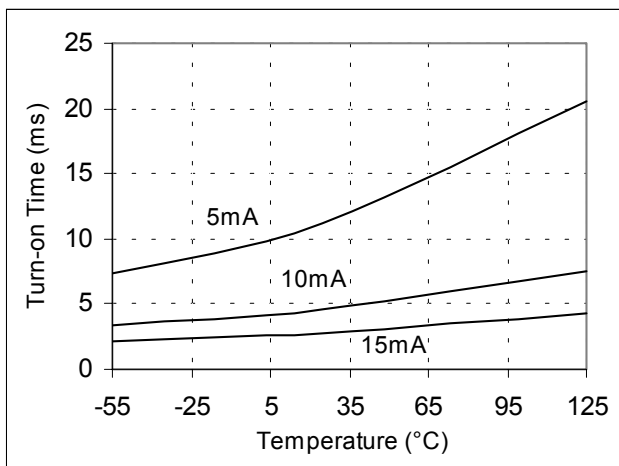


Figure 4. Turn-on Time vs. Case Temperature for Different Values of  $I_F$  (typical data).

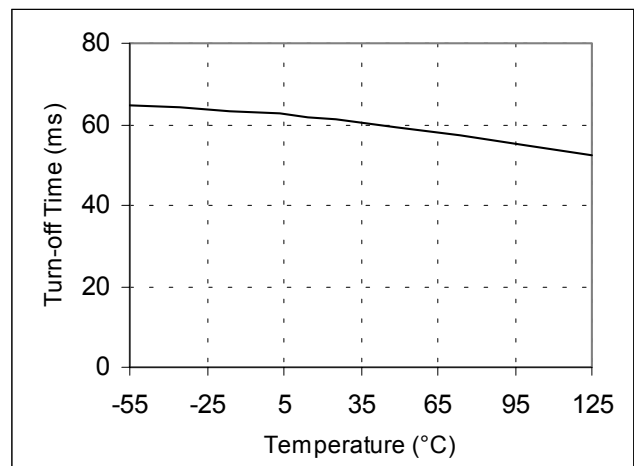


Figure 5. Turn-off Time vs. Case Temperature with  $I_F = 5$  to 15mA (typical data).

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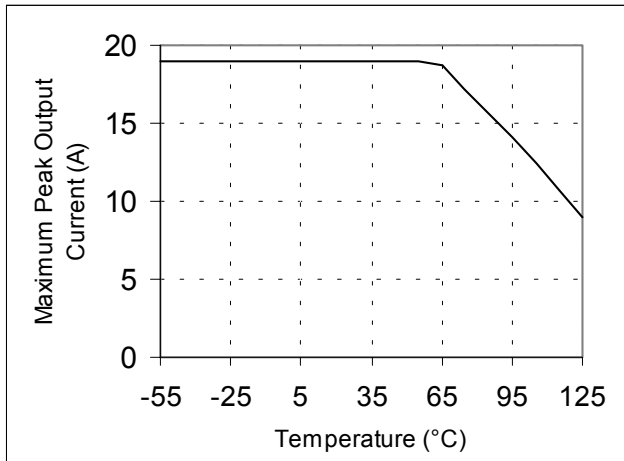


Figure 6. Maximum Non-repetitive Output Current vs. Case Temperature (pulse width  $\leq 10$  ms).

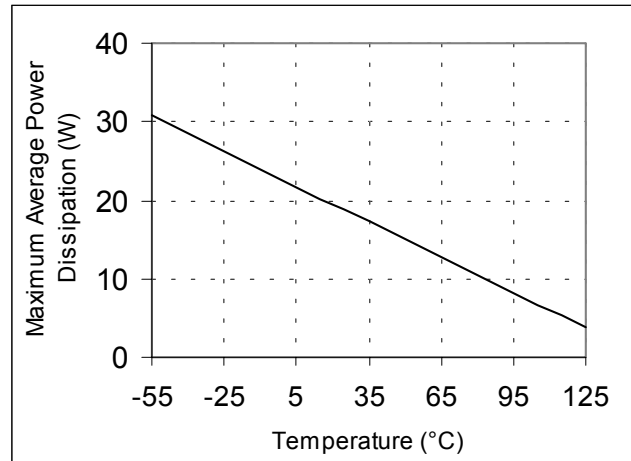


Figure 7. Maximum Average Output Power Dissipation vs. Case Temperature.

**TOTAL DOSE TEST RESULTS**

***Disclaimer:*** The data of 3 representative units irradiated in Cobalt-60 chamber is only typical of one lot of solid state relays. Micropac does not guarantee performance of its SSR to these radiation levels. Individual lots have to be screened to guarantee the performance.

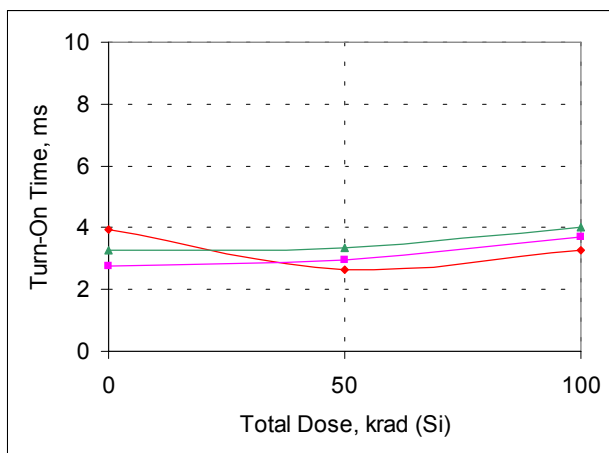


Figure 8. Turn-On Time vs Total Dose at  $V_{out} = 28$  V,  $R_{load} = 35 \Omega$ ,  $I_{in} = 15$  mA, 10% duty cycle.

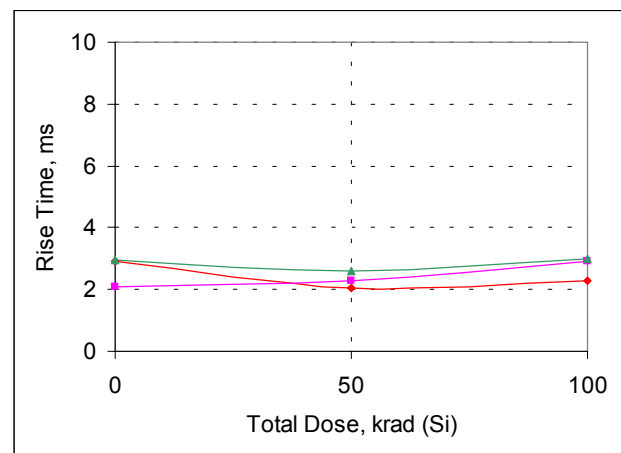


Figure 9. Rise Time vs Total Dose at  $V_{out} = 28$  V,  $R_{load} = 35 \Omega$ ,  $I_{in} = 15$  mA, 10% duty cycle.

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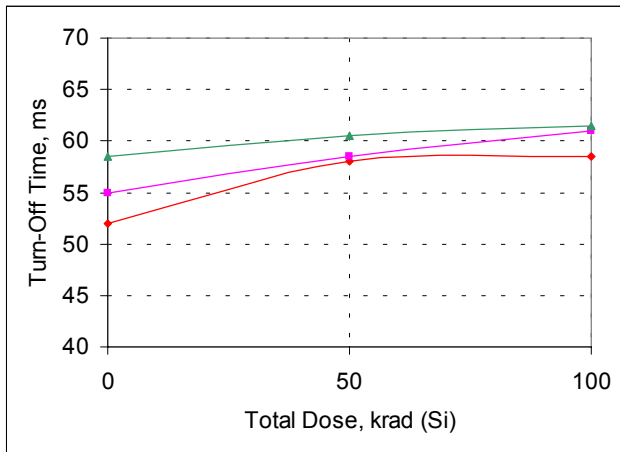


Figure 10. Turn-Off Time vs Total Dose at  $V_{out} = 28\text{ V}$ ,  $R_{load} = 35\ \Omega$ ,  $I_{in} = 15\text{ mA}$ , 10% duty cycle.

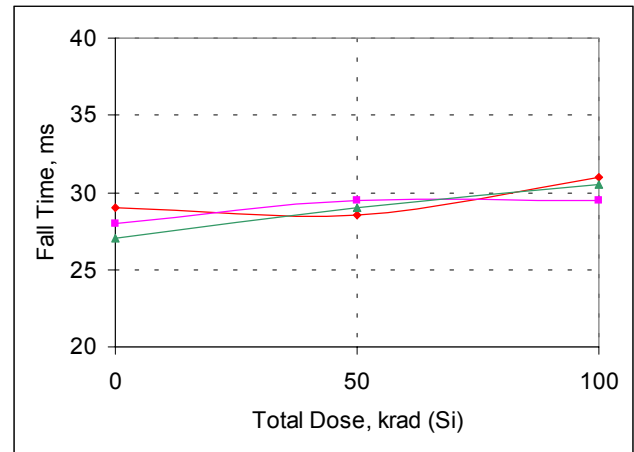


Figure 11. Fall Time vs Total Dose at  $V_{out} = 28\text{ V}$ ,  $R_{load} = 35\ \Omega$ ,  $I_{in} = 15\text{ mA}$ , 10% duty cycle.

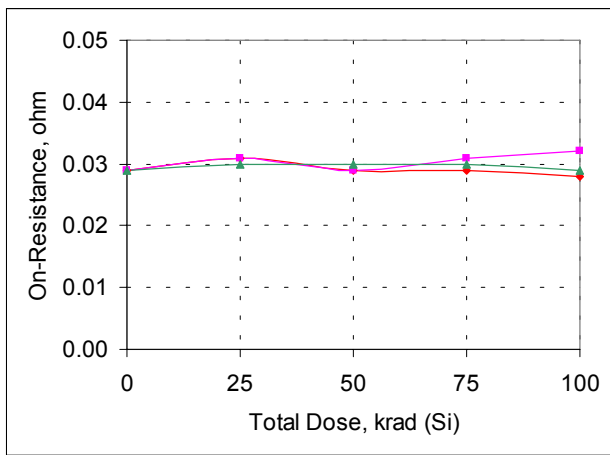


Figure 12. On-Resistance vs Total Dose at  $I_F = 10\text{ mA}$ ,  $I_{out} = 100\text{ mA}$  for 1second.

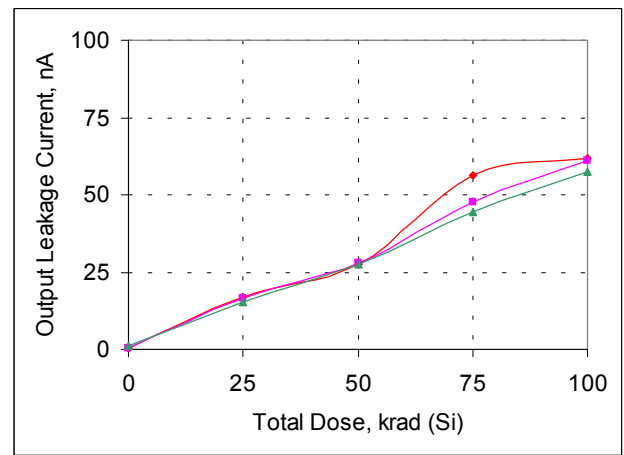


Figure 13. Output Leakage Current vs Total Dose at  $V_F = 1.0\text{ V}$ ,  $V_{out} = 50\text{ V}$ .

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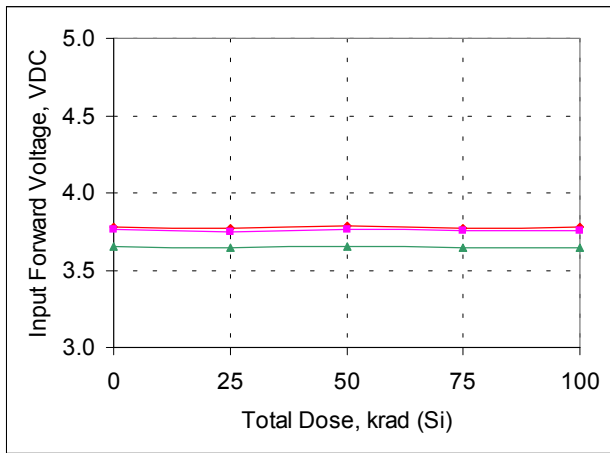


Figure 12. Input Forward Voltage vs Total Dose at  $I_F = 15$  mA.

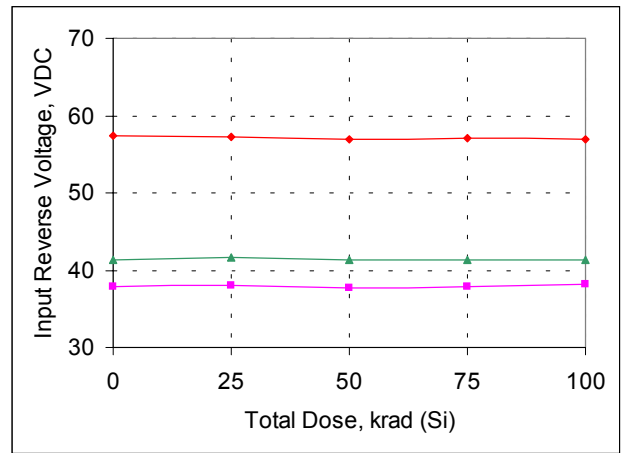
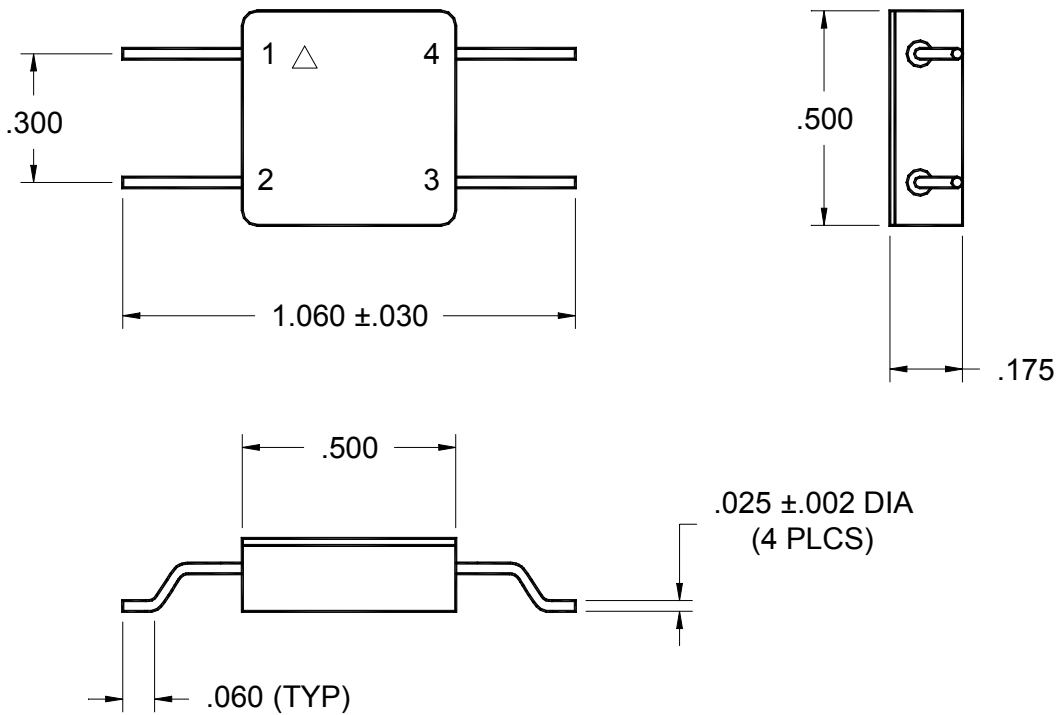


Figure 15. Input Reverse Voltage vs Total Dose at  $I_R = 10$   $\mu$ A.

**CASE OUTLINE**



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