MORNSUN®

PWA YMD-6W & PWB YMD-6W Series

6W, 4:1 WIDE INPUT, ISOLATED & REGULATED DUAL & SINGLE OUTPUT DC-DC CONVERTER





Patent Protection RoHS

FEATURES

- DIP package
- 1500VDC isolation
- Short circuit protection (automatic recovery)
- Operating temperature: -40°C to +85°C
- Internal SMD construction
- Metal shielding package
- Industry standard pinout
- No heat sink required
- MTBF>1,000,000 hours
- RoHS Compliance

APPLICATIONS

The PWA_YMD-6W & PWB_YMD-6W Series are specially designed for applications where a wide range input voltage power supplies are isolated from the input power supply in a distributed power supply system on a circuit board.

These products apply to:

 Where the voltage of the input power supply is wide range (voltage range≤ 4:1);

*Designing.

- Where isolation is necessary between input and output(isolation voltage≤1500VDC);
- 3) Where the regulation of the output voltage and the output ripple noise are demanded.

MODEL S	SELECTION	
PWA240	5YMD-6W	
		Rated Power
		Package Style
		Output Voltage
		Input Voltage
		Product Series

MORNSUN Science & Technology Co., Ltd.

Address: No. 5, Kehui St. 1, Kehui development center, Science Ave., Guangzhou Science City, Luogang district, Guangzhou, P.R. China.

Tel: 86-20-28203030 Fax:86-20-28203068

Http://www.mornsun-power.com

PRODUCT PROGRAM									
	Input		Output						
Part Number	Voltage (VDC)			Voltage	Current (mA)		Efficiency (%, Typ.)		
	Nominal	Range	Max**	(VDC)	Max.	Min.	(11)		
PWA2405YMD-6W				±5	±600	±60	80		
PWA2412YMD-6W				±12	±250	±25	82		
PWA2415YMD-6W		24 9.0-36		±15	±200	±20	84		
PWA2424YMD-6W	24		0.036	0.036	40	±24	±125	±13	82
PWB2405YMD-6W			40	5	1200	120	80		
PWB2412YMD-6W					A 1	12	500	50	82
PWB2415YMD-6W								15	400
PWB2424YMD-6W				24	250	25	82		
*PWA4805YMD-6W				±5	±600	±60	80		
PWA4812YMD-6W		48 18-72		k .	<i></i>	±12	±250	±25	82
PWA4815YMD-6W	40		80	±15	±200	±20	84		
PWB4805YMD-6W	40		10-72	00	5	1200	120	80	
PWB4812YMD-6W			12	500	50	82			
PWB4815YMD-6W				15	400	40	84		

COMMON SPECI	FICATIONS				
Item	Test Conditions	Min.	Тур.	Max.	Units
Storage humidity				95	%
Operating temperature		-40		85	
Storage temperature		-55		125	°C
Temp. rise at full load			40		
Lead temperature	1.5mm from case for 10 seconds			300	
Cooling		Free air convection			
Short circuit protection		Continuous, automatic recovery			
Case material		Aluminum			
MTBF		1000			K hours
Weight			15		g

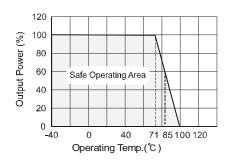
* *Input voltage can't exceed this value, or will cause the permanent damage.

ISOLATION SPECIFICATIONS					
Item	Test Conditions	Min.	Тур.	Max.	Units
Isolation voltage	Tested for 1 minute and 1 mA max	1500			VDC
Isolation resistance	Test at 500VDC	1000			МΩ
Isolation capacitance	Input/Output, 100KHz/1V		100		pF

OUTPUT SPECIFICATIONS					
Item	Test Conditions	Min.	Тур.	Max.	Units
Output power	See above products program	0.6		6	W
Line regulation (at full load)	Input voltage from low to high		±0.2	±0.5	
Load regulation	Form 10% to 100%load		±0.5	±2*	%
Positive output voltage accuracy	Refer to recommended circuit		±1	±3	76
Negative output voltage accuracy	Refer to recommended circuit		±3	±5	
Temperature Drift (Vout)	Refer to recommended circuit		±0.02		%/°C
Ripple**	20MHz Bandwidth		30	50	mVp-p
Noise**	20MHz Bandwidth	75 150		III v p-p	
Switching frequency	100% load, input voltage range		300		KHz

^{*} Dual output models unbalanced load: ±5%

TYPICAL TEMPERATURE CURVE



APPLICATION NOTE

1) Requirement on output load

In order to ensure the product operate efficiently and reliably, in addition to a max load (namely full load), a minimum load is specified for this kind of DC/DC converter. Make sure the specified range of input voltage is not exceeded, the minimum output load no less than 10% load. If the actual load is less than the specified minimum load, the output ripple may increase sharply while its efficiency and reliability will reduce greatly. If the actual output power is very small, please add an appropriate resistor as extra loading, or contact our company for other lower output power products.

2) Recommended Circuit

All the PWA_YMD-6W & PWB_YMD-6W series have been tested according to the following recommended testing circuit before leaving factory. This series should be tested under load (see Figure 1).

If you want to further decrease the input/output ripple, you can increase capacitance properly or choose capacitors with low ESR. However, the capacitance should not be too high, or may cause start-up problem. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor sees (Table 1). General:

Cin: 24V&48V 10μF~47μF

Cout: 10µF/100mA

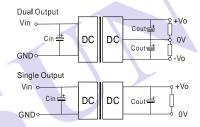
3) Input current

While using unstable power source, please ensure the output voltage and ripple voltage do not exceed indexes of the converter. The preceding power source must be able to provide for converter sufficient starting current Ip (Figure 2).

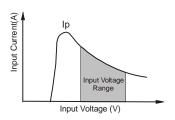
General: Ip ≤1.4*lin-max

4) No parallel connection or plug and play

RECOMMENDED CIRCUIT



(Figure 1)



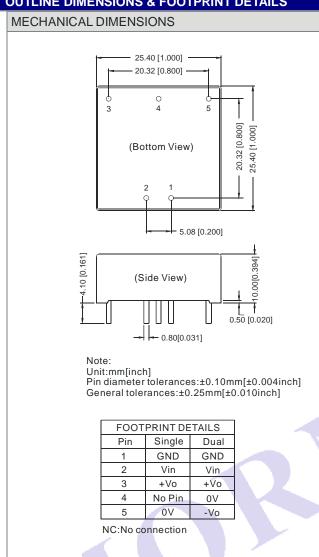
(Figure 2)

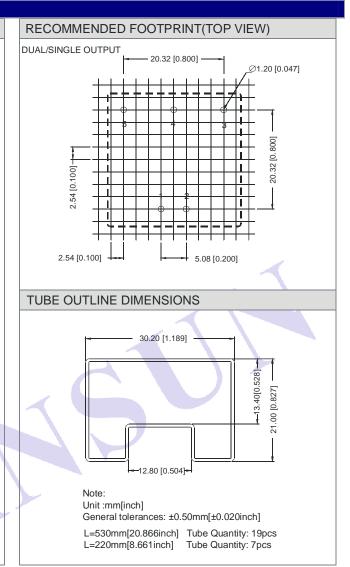
External Canacitor Table (Table 1)

External Capacitor Table (Table 1)					
Single Vout	Cout	Dual Vout	Cout		
(VDC)	(μF)	(VDC)	(µF)		
5	1000	±5	680		
12	470	±12	330		
15	330	±15	220		
24	220	±24	100		

^{**}Test ripple and noise by "parallel cable" method. See detailed operation instructions at Testing of Power Converter section, application notes.

OUTLINE DIMENSIONS & FOOTPRINT DETAILS





Note:

- 1. The load shouldn't be less than 10%, otherwise ripple will increase dramatically.
- 2. Operation under 10% load will not damage the converter; However, they may not meet all specification listed.
- 3. Capacitor MAX load tested at input voltage range and full load.
- 4. All specifications measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.
- 5. In this datasheet, all the test methods of indications are based on corporate standards.
- 6. Only typical models listed, other models may be different, please contact our technical person for more details.