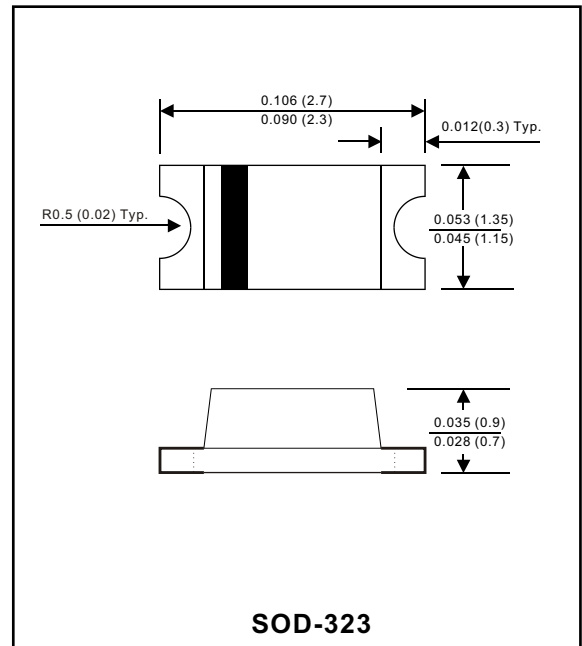


ASD501V-N

Surface mount small signal type

Features

- Extermely low VF
- Extermely thin package
- Low stored charge
- Majority carrier conduction



Mechanical data

Case : Molded plastic, JEDEC SOD-323
 Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
 Polarity: Indicated by cathode band
 Mounting Position : Any
 Weight : 0.000159 ounce, 0.0045 gram

MAXIMUM RATINGS (AT T_A=25°C unless otherwise noted)

PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Repetitive peak reverse voltage		V _{RM}			45	V
Continuous reverse voltage		V _R			40	V
Mean rectifying current		I _O			100	mA
Forward surge current	8.3ms single half sine-wave superimposed on rate load (JEDEC methode)	I _{FSM}		1000		mA
Capacitance between terminals	f=1MHz and applied 10VDC reverse voltage	C _T		20		pF
Storage temperature		T _J	-40		+125	°C
Operating temperature		T _{STG}	-40		+125	°C

ELECTRICAL CHARACTERISTICS (AT T_A=25°C unless otherwise noted)

PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 100 mA DC	V _F			0.55	V
Reverse current	V _R = 10 V DC	I _R			30	uA

RATING AND CHARACTERISTIC CURVES (ASD501V-N)

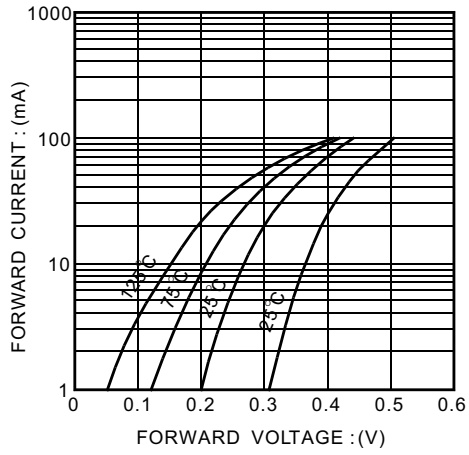


Fig. 1 Forward characteristics

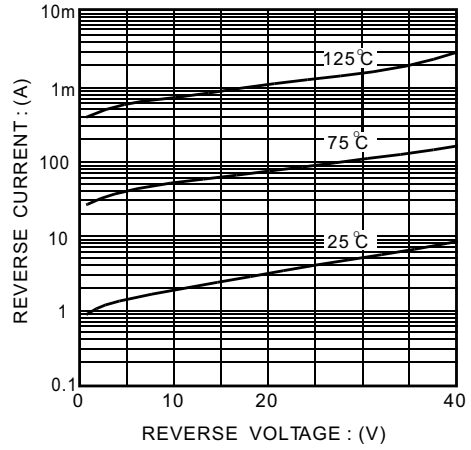


Fig. 2 Reverse characteristics

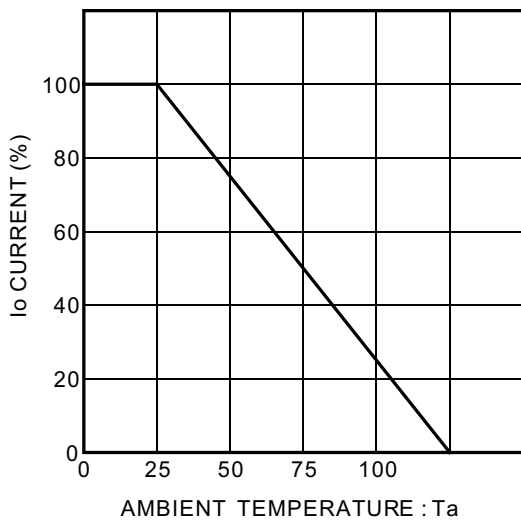


Fig. 4 Derating curve
(mounting on glass epoxy PCBs)

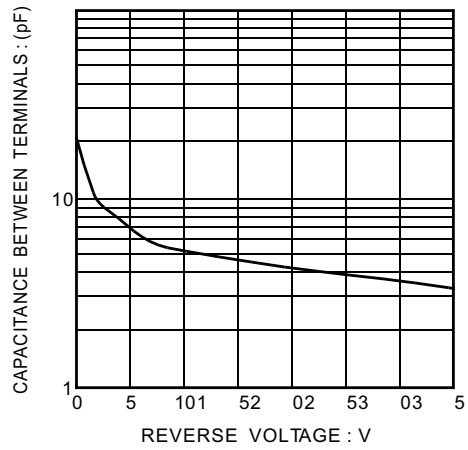


Fig. 3 Capacitance between terminals characteristics