

- Rugged construction for severe environments
- Tight stability, from $\pm 0.5\text{ppm}$ over -40° to $+85^\circ\text{C}$
- Squarewave (CMOS), Clipped Sine, LVPECL or LVDS outputs



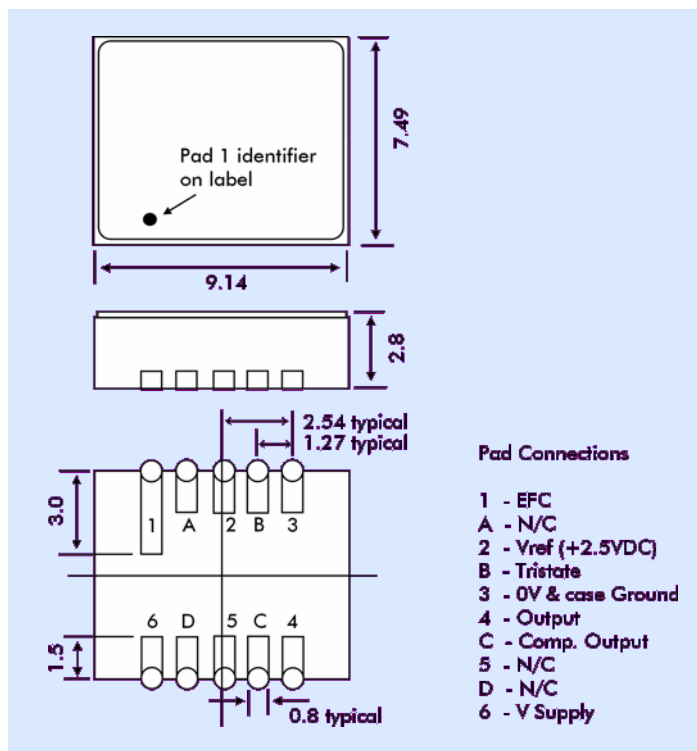
SPECIFICATIONS

Frequency Range:	750.0kHz to 800MHz
Output:	Option 'C': CMOS Square (750kHz to 150MHz) Option 'S': Clipped Sinewave (10MHz to 50MHz) Option 'PE': LVPECL (50MHz to 800MHz) Option 'DS': LVDS (50MHz to 800MHz)
Symmetry:	$50\% \pm 10\%$
Frequency Stability:	See table
Ageing:	$< 1\text{ppm/year}$, $< 10\text{ppm}$ for 20 years
Frequency Adjust:	$\pm 7\text{ppm}$ typical for 0 to V_{cc} EFC Positive slope
Supply Voltage:	+2.7 Volts to +5.0 Volts $\pm 5\%$
Supply Current:	$< 50\text{mA}$ (frequency dependent)
Acceleration Sensitivity:	$2.5 \times 10^{-9}/\text{g}$ standard (SD) $< 7 \times 10^{-10}/\text{g}$ available (Option LG)

ENVIRONMENTAL

Vibration:	per MIL-STD-202F, Meth. 204, Cond. A
Shock:	per MIL-STD-202F, Meth. 213, Cond. C
Storage Temperature:	-55° to $+95^\circ\text{C}$
Part may be screened to MIL-PRF-55310, Class B (option B) no screening is option 'X'.	
Fine Leak:	per MIL-STD-202, Meth. 112, Cond. C
Other vibration and shock levels may be available upon review by Euroquartz engineering.	

T1215 - OUTLINES AND DIMENSIONS



STABILITY OVER TEMPERATURE

Temp. Range	Stability	Option Code
$-40^\circ \sim +85^\circ\text{C}$	$\pm 0.5\text{ppm}$	T57
$-40^\circ \sim +85^\circ\text{C}$	$\pm 1.0\text{ppm}$	T16
$-55^\circ \sim +105^\circ\text{C}$	$\pm 2.0\text{ppm}$	T26
$-55^\circ \sim +105^\circ\text{C}$	$\pm 3.0\text{ppm}$	V36

PART NUMBERING PROCEDURE

Example: **T1215-T57-PE-2.7-100.0MHz**

