

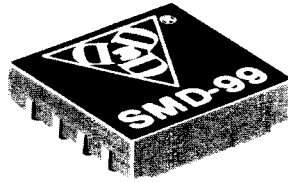
# Surface Mounted Delay Line

SERIES: **SMD-99**  
TTL Interfaced

**data  
delay  
devices, inc.** 

## FEATURES:

- Designed for surface mounting.
- Low profile .175 max. height.
- Completely interfaced for TTL.
- Up to 5 outputs.



## Specifications:

- Outputs: 5, equally spaced.
- Delay tolerance: see table.
- Rise-time: 4 NS typ.
- Minimum pulse width: 40% of total delay.
- Temperature range: 0 to 70°C. (-55°C to +125°C on request.)
- Temperature coefficient: 100 PPM/°C.
- Supply voltage: 4.75 to 5.25 Vdc.
- Input signal: TTL logic.
- Output fan-out: TTL Schottky load.
- Power dissipation: 375 mw max.

Part No.	Total Delay (NS)	Tap Delay (NS)
*SMD-99-5004	4 ± 1	1 ± 0.5
*SMD-99-5006	6 ± 1	1.5 ± 0.5
*SMD-99-5008	8 ± 2	2 ± 1
*SMD-99-5010	10 ± 2	2.5 ± 1
*SMD-99-5012	12 ± 2	3 ± 1
*SMD-99-5016	16 ± 2	4 ± 1.5
*SMD-99-5020	20 ± 3	5 ± 2
SMD-99-5030	30 ± 3	6 ± 2
SMD-99-5035	35 ± 3	7 ± 2
SMD-99-5040	40 ± 3	8 ± 2
SMD-99-5045	45 ± 3	9 ± 3
SMD-99-5050	50 ± 3	10 ± 3
SMD-99-5060	60 ± 3	12 ± 3
SMD-99-5075	75 ± 4	15 ± 3
SMD-99-5100	100 ± 5	20 ± 3
SMD-99-5125	125 ± 6.5	25 ± 3
SMD-99-5150	150 ± 7.5	30 ± 3
SMD-99-5175	175 ± 8	35 ± 4
SMD-99-5200	200 ± 10	40 ± 4
SMD-99-5250	250 ± 12.5	50 ± 5

\*Time delay measured with respect to 1st tap.  
6 NS ± 1 NS inherent delay.

