

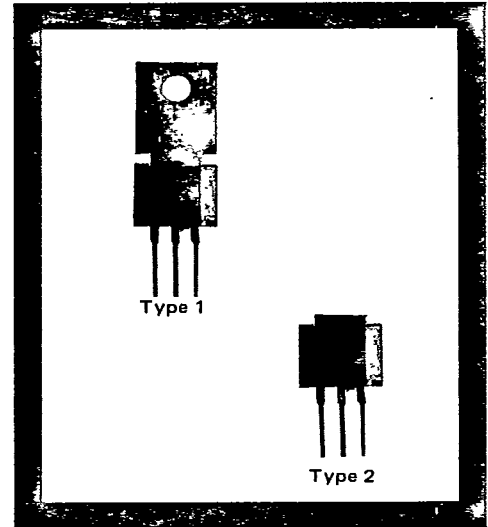
**HUTSON INDUSTRIES**

**TRIAC's**



**3 AMPERE  
SENSITIVE GATE TRIACS  
TO-202**

- 3mA DC Gate-Trigger Current
- 4mA DC Gate-Trigger Current
- 5mA DC Gate-Trigger Current
- 10mA DC Gate-Trigger Current
- 25mA DC Gate-Trigger Current
- ALL QUADRANT GATING**



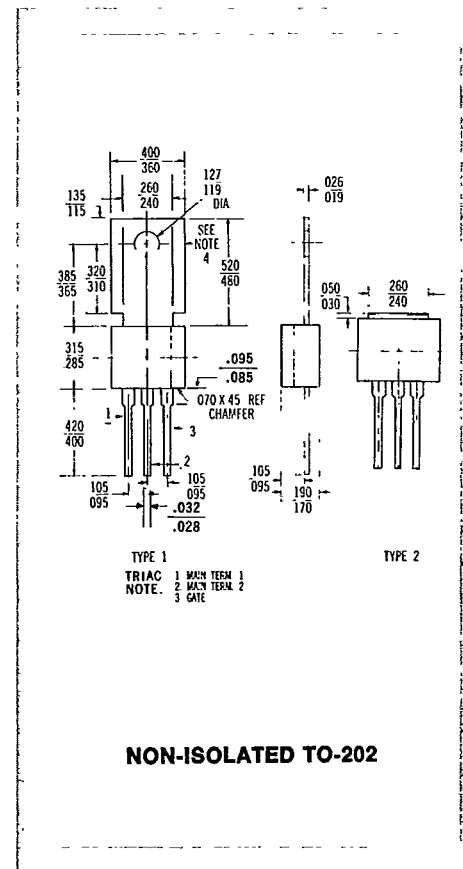
The Hutson line of sensitive gate triacs are designed to be driven directly with IC and MOS devices. These triacs feature proprietary, void-free glass passivated chips.

These 3 Ampere triacs are available in voltage ratings from 50 to 600 volts ( $V_{DROM}$ ) and in 3mA, 4mA, 5mA, 10mA and 25mA ( $I_{GT}$ ) ratings. All devices are tested at their upper operating limits before shipment.

The economical and highly reliable triacs are the result of Hutson's advanced engineering and manufacturing technology, state-of-the-art-passivation materials and techniques and experience in switching device applications.

Hutson triacs are bi-directional triode thyristors and may be switched from off-state to conduction for either polarity of applied voltage with positive or negative gate-trigger current. They are designed for control applications in lighting, heating, cooling and static switching relays.

In addition to standard package configurations, all Hutson triacs are also available in chip form. Please consult Hutson Industries for additional information.



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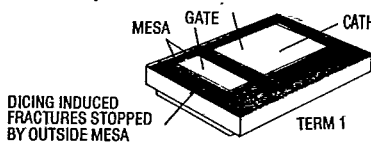


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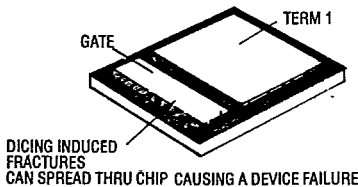
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	SYMBOL	V <sub>DROM</sub>	DEVICE NOS.	DEVICE NOS.	
<b>MAXIMUM RATINGS</b>	Repetitive Peak Off-State Voltage, <sup>(2)</sup> Gate Open, and T <sub>J</sub> = 100° C	V <sub>DROM</sub>	50 100 200 300 400 500 600	T106F*SS T106A*SS T106B*SS T106C*SS T106D*SS T106E*SS T106M*SS	T106F*SC T106A*SC T106B*SC T106C*SC T106D*SC T106E*SC T106M*SC
	RMS On-State Current at T <sub>c</sub> = 75° C and Conduction Angle of 360°	I <sub>t(RMS)</sub>		3	3
	Peak Surge (Non-Repetitive) On-State Current, One-Cycle, at 50Hz or 60Hz	I <sub>TSM</sub>		30	30
	Peak Gate-Trigger Current for 3μsec, Max.	I <sub>GT</sub>		1	1
	Peak Gate-Power Dissipation at I <sub>GT</sub> ≤ I <sub>GT</sub> for 3 μsec. Max.	P <sub>GM</sub>		20	20
	Average Gate-Power Dissipation	P <sub>G(AV)</sub>		0.2	0.2
	Storage Temperature Range	T <sub>stg</sub>		-40 to +150	-40 to +150
	Operating Temperature Range, T <sub>J</sub>	T <sub>oper</sub>		-40 to +90	-40 to +90
<b>ELECTRICAL CHARACTERISTICS</b> At Maximum Ratings and at Specified Case Temperatures	Peak Off-State Current, Gate Open, <sup>(2)</sup> T <sub>J</sub> = 100° C V <sub>DROM</sub> = Max. Rating	I <sub>DROM</sub>		0.75 Max.	0.75 Max.
	Maximum On-State Voltage at T <sub>c</sub> = 25° C and I <sub>T</sub> = 4 Amp (Peak)	V <sub>TM</sub>		1.30 Max.	1.30 Max.
	DC Holding Current, Gate Open and T <sub>c</sub> = 25° C	I <sub>HO</sub>		5 Max.	15 Max.
	Critical Rate-of-Rise of Off-State Voltage <sup>3)</sup> for V <sub>D</sub> = V <sub>DROM</sub> , Gate Open, T <sub>c</sub> = 100° C	Critical dv/dt		5 Typ.	6 Typ.
	DC Gate-Trigger Current for V <sub>D</sub> = 6VDC, R <sub>L</sub> = 100Ω and at T <sub>c</sub> = 25° C (T <sub>2</sub> +Gate+, T <sub>2</sub> -Gate-) Quads I and III (T <sub>2</sub> +Gate-, T <sub>2</sub> -Gate+) Quads II and IV (Note 1)	I <sub>GT</sub>		3 Max.	4 Max.
	DC Gate-Trigger Voltage for V <sub>D</sub> = 6VDC, R <sub>L</sub> = 100Ω and at T <sub>c</sub> = 25° C	V <sub>GT</sub>		2.2 Max.	2.2 Max.
	Gate-Controlled Turn-on Time for V <sub>D</sub> = V <sub>DROM</sub> , I <sub>GT</sub> = 80 mA, t <sub>r</sub> = 0.1 μsec., I <sub>T</sub> = 10A (Peak) and T <sub>c</sub> = 25° C	t <sub>gt</sub>		2.2 Typ.	2.2 Typ.
	Thermal Resistance, Junction-to-Case	θ <sub>J-C</sub>		6 Typ.	6 Typ.

HUTSON DI-MESA CONSTRUCTION OF THYRISTOR CHIP



CONVENTIONAL CONSTRUCTION OF THYRISTOR CHIP



HUTSON DI-MESA\* CONSTRUCTION PROTECTS PASSIVATION AGAINST FIELD FAILURE

This unique construction technique, patented by Hutson, provides the proven advantages of inorganic (glass) passivation while eliminating field breakdown of the passivated device. In the dicing operation, using Laser, scribe, saw or other methods, minute edge fractures, representing incipient failures, frequently occur in any glass passivation. These fractures usually escape detection during ordinary testing; however, during repeated operational cycling, these fractures spread

to the device junction and result in a device failure. This failure mode is eliminated by Hutson's Di-Mesa construction, which provides a physical barrier to the spread of any fracture which may occur. The Di-Mesa construction technique also allows, for the first time, unpackaged thyristor chips to be easily and safely handled by chip users for mounting on their substrates.

\*Di-Mesa is a Trademark of Hutson Industries.

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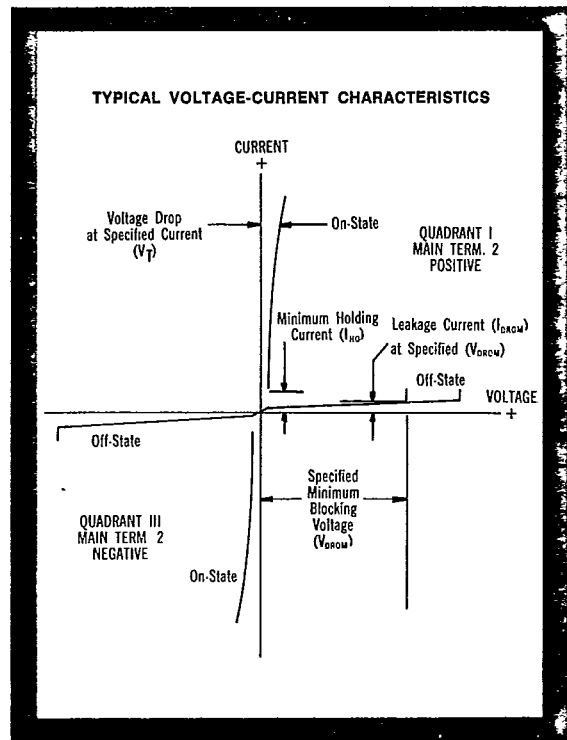
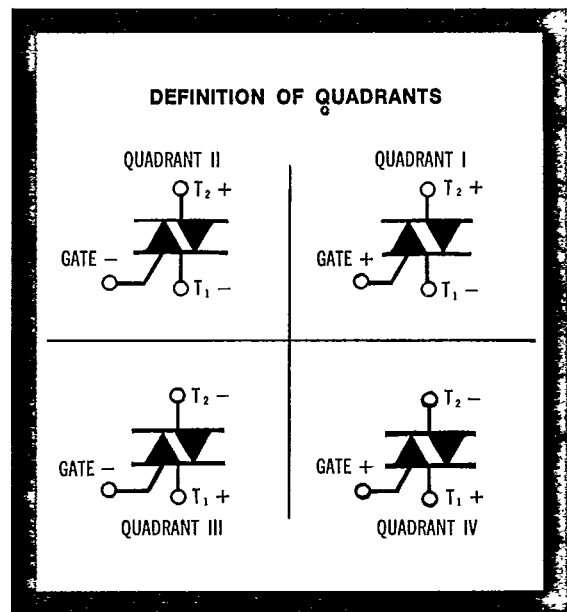
# 3 AMPERE SENSITIVE GATE TRIACS

DEVICE NOS.			UNITS
T106F*SD T106A*SD T106B*SD T106C*SD T106D*SD T106E*SD T106M*SD	T106F*SG T106A*SG T106B*SG T106C*SG T106D*SG T106E*SG T106M*SG	T106F*SH T106A*SH T106B*SH T106C*SH T106D*SH T106E*SH T106M*SH	VOLT
3	3	3	AMP
30	30	30	AMP
1	1	1	AMP
20	20	20	WATT
0.2	0.2	0.2	WATT
-40 to +150	-40 to +150	-40 to +150	°C
-40 to +100	-40 to +100	-40 to +100	°C
0.75 Max.	0.75 Max.	0.75 Max.	mA
1.90 Max.	1.90 Max.	1.90 Max.	VOLT
15 Max.	15 Max.	15 Max.	mA
10 Typ.	10 Typ.	10 Typ.	V/μsec
5 Max.	10 Max.	25 Max.	mA
2.2 Max.	2.2 Max.	2.2 Max.	VOLT
2.2 Typ.	2.2 Typ.	2.2 Typ.	μsec
6 Typ.	6 Typ.	6 Typ.	°C/WATT

\* Part Number requires a "1" for Type 1 or a "2" for Type 2

**NOTES:**

1. Quadrant II & IV gating may be deleted from any Hutson sensitive gate triac as many applications do not require this capability. To order those devices suffix part number with "A".
2. SS and SC devices are at  $T_j = 90^\circ\text{C}$
3. SS and SC devices are at  $T_c = 90^\circ\text{C}$
4. All values apply in either direction.

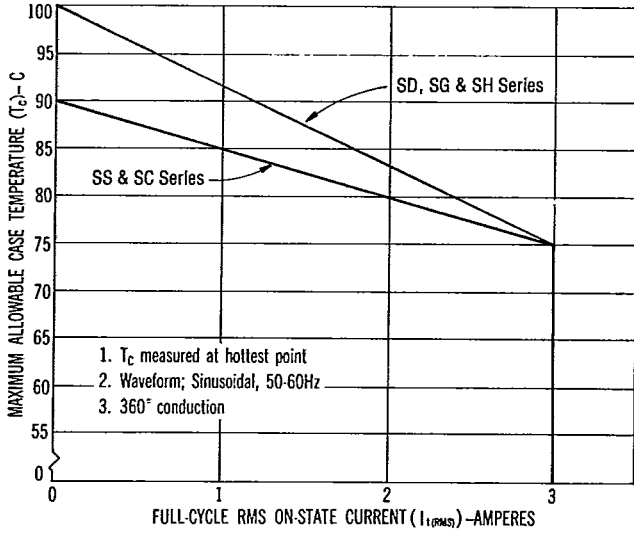


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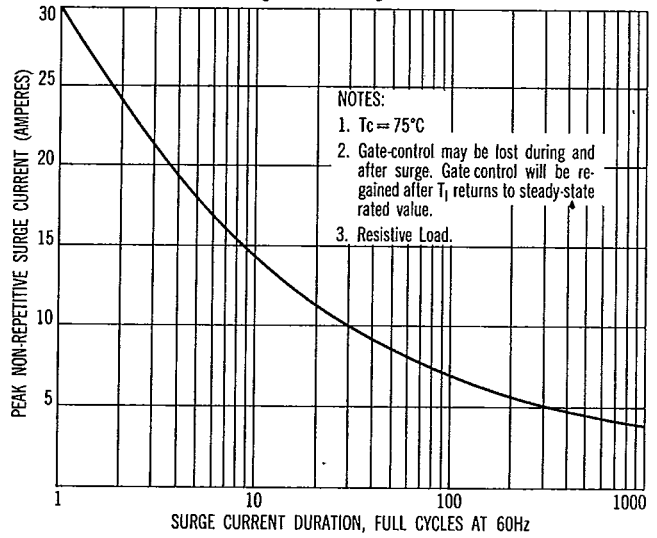
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# 3 AMPERE SENSITIVE GATE TRIACS

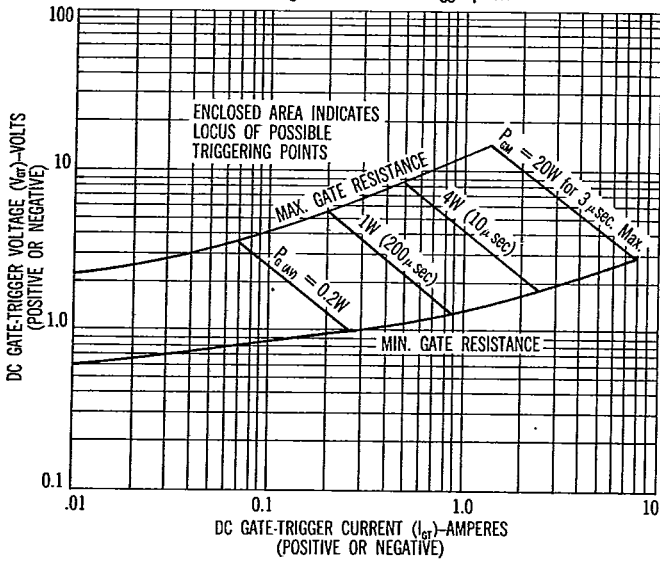
Maximum Allowable Case Temperature vs On-State Current.



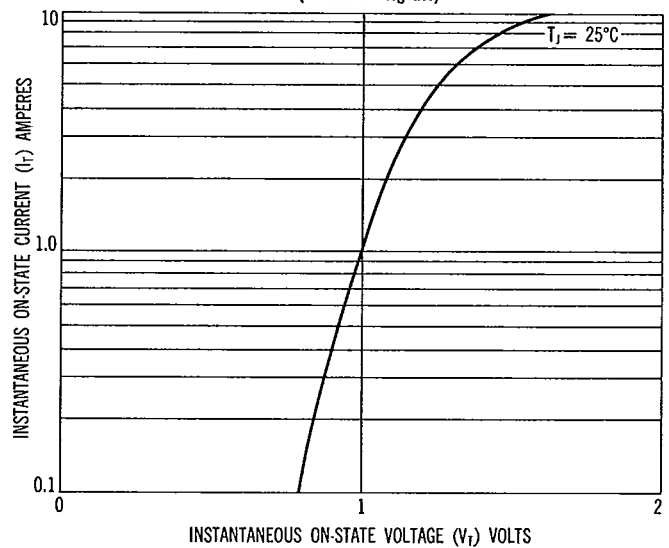
Peak Surge Current vs Surge Current Duration



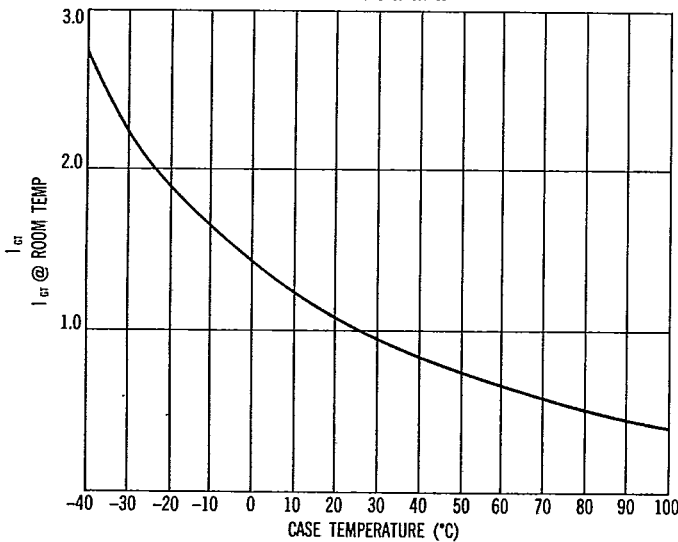
Gate-Trigger Characteristics and Limiting Conditions of Gate-Trigger pulses.



Typical On-State Current vs On-State Voltage (Positive or Negative)



Typical Gate Current vs Case Temperature Sensitive Gate Triacs



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