

BLOCK DIAGRAM	<table border="1"> <tr><td>2K ROM</td></tr> <tr><td>Z8[®] CPU</td></tr> <tr><td>RAM</td></tr> <tr><td>Counter/Timer</td></tr> <tr><td>WDT</td></tr> <tr><td>Comparators</td></tr> <tr><td>P0</td></tr> <tr><td>P2</td></tr> <tr><td>P3</td></tr> </table>	2K ROM	Z8 [®] CPU	RAM	Counter/Timer	WDT	Comparators	P0	P2	P3	<table border="1"> <tr><td>1K ROM</td></tr> <tr><td>Z8[®] CPU</td></tr> <tr><td>RAM</td></tr> <tr><td>Counter/Timer</td></tr> <tr><td>WDT</td></tr> <tr><td>Comparators</td></tr> <tr><td>P0</td></tr> <tr><td>P2</td></tr> <tr><td>P3</td></tr> </table>	1K ROM	Z8 [®] CPU	RAM	Counter/Timer	WDT	Comparators	P0	P2	P3	<table border="1"> <tr><td>4K ROM</td></tr> <tr><td>DSP</td></tr> <tr><td>RAM</td></tr> <tr><td>Counter/Timer</td></tr> <tr><td>Coddec Interface</td></tr> <tr><td>16-Bit DATA</td></tr> <tr><td>MAC</td></tr> <tr><td>I/O</td></tr> </table>	4K ROM	DSP	RAM	Counter/Timer	Coddec Interface	16-Bit DATA	MAC	I/O	<table border="1"> <tr><td>4K ROM</td></tr> <tr><td>Z8[®] MCU</td></tr> <tr><td>RAM</td></tr> <tr><td>Counter/Timer</td></tr> <tr><td>WDT</td></tr> <tr><td>Comparators</td></tr> <tr><td>P0</td></tr> <tr><td>P2</td></tr> <tr><td>P3</td></tr> </table>	4K ROM	Z8 [®] MCU	RAM	Counter/Timer	WDT	Comparators	P0	P2	P3
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PART NUMBER	Z86C08/Z86C07/Z86E08	Z86C04/Z86E04	Z89321/Z89371	Z86C30/Z86E30																																			
DESCRIPTION	Pointing Device Z8 [®] MCU Z86E08 = OTP Version	Discrete MCU Z86E04 = OTP Version	16-Bit Digital Signal Processor Z89371 = OTP Version	Z8 [®] MCU Z86E30 = OTP Version																																			
PROCESS/SPEED	CMOS: 4, 8, 12 MHz	CMOS: 4 MHz	CMOS: 15, 20 MHz	CMOS: 8, 12 MHz																																			
FEATURES	<ul style="list-style-type: none"> ■ 2K ROM ■ 124 Byte RAM ■ 14 I/O Lines ■ Two Counter/Timers ■ Power-Down Modes ■ Two Comparators ■ Power-On Reset (POR) ■ Watch-Dog Timer (WDT) ■ Auto Latch (Z86C07 Only) 	<ul style="list-style-type: none"> ■ 1K ROM ■ 124 Byte RAM ■ 14 I/O Lines ■ Two Counter/Timers ■ Power-Down Modes ■ Two Comparators ■ Power-On Reset (POR) ■ Watch-Dog Timer (WDT) 	<ul style="list-style-type: none"> ■ 4K Word ROM ■ 512 Word RAM ■ 16 Bit I/O Bus ■ Two Counter/Timers ■ CODEC Interface ■ 50/75 ns Cycle Timer ■ 4K OTP ROM (Z89371 Only) 	<ul style="list-style-type: none"> ■ 4K Word ROM ■ 256 Byte RAM ■ 24 I/O Lines ■ 2 Counter/Timers ■ Power-Down Mode ■ Two Comparators ■ Power-On Reset (POR) ■ Watch-Dog Timer (WDT) 																																			
PACKAGE	18-Pin DIP 18-Pin SOIC	18-Pin DIP 18-Pin SOIC	40-Pin DIP 44-Pin PLCC	28-Pin DIP 28-Pin SOIC																																			
SUPPORT PRODUCTS	Z86C1200ZEM - Emulator	Z86C1200ZEM - Emulator Z86C100ZEM - Emulator	Z8937100ZEM - Emulator Z8937100TSC - Emulator	Z86C5000ZEM - Emulator																																			