PLL Synthesizer

PLA Series



Product Features

- PLL + VCO+ Rom + TCXO Type
- Excellent Phase Noise
- Low Spurious
- Surface Mount Package
- RF Frequencies from 10MHz to 5.8GHz
- Internal Crystal Reference Clock (-30°C~+80°C:2.5ppm Tolerance)
- Pb Free / RoHS Compliant

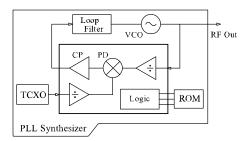
Applications

- Wireless Communication System
- Base Station
- Cable Telephone, TV
- Other Wireless Communication Systems

Series View

 $\bullet A: \mathsf{TCXO}, \mathsf{ROM} \text{ included}$

Test Configuration



Specifications

PARAMETER	SPECIFICATION	UNIT	CONDITION		
Storage Temperature	-40 ~ +85	°C			
Operating Temperature	-30 ~ +80	°C			
Frequency	10 ~ 5800	MHz			
Supply Voltage VCC (PLL) / VCC (VCO)	3~5	V			
Reference Frequency	5~40	MHz			
Channel Step	10 ~ 10000	KHz			
Output Power	-3 ~ +7	dBm			
Current Consumption	40	mA / Max			
Spurious	-70	dBc / Max			
Lock Time	0.1 ~ 30	ms			
Phase Noise	-105	dBc/Hz	@ 10 KHz Offset		
Output Impedance	50	Ω			

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PS-32 / PS-42

(Operating Temperature:-30°C~+80°C)



• All specifications may change without notice.

• Version 5.2

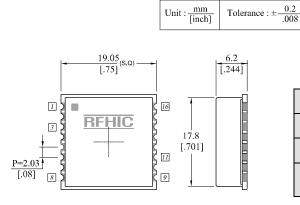
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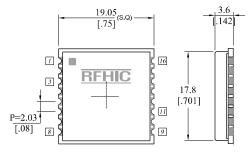
Package Dimensions

PS-16 type

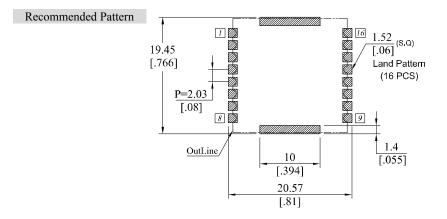


PLA Series Pin Assignment					
1	GND	2	GND	3	GND
4	N.C	9	VCC(VCO)	13	RF Out
15	VCC (PLL)	16	Lock Detect	All other pins are GND	

PS-26 Type



PLA Series Pin Assignment					
1	GND	2	GND	3	GND
4	N.C	9	VCC(VCO)	13	RF Out
15	VCC (PLL)	16	Lock Detect	All other pins are GND	



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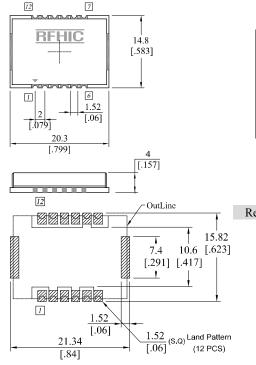
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PLL Synthesizer

PLA Series

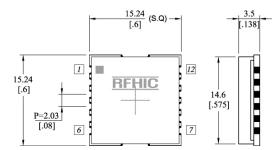


PS-32 type



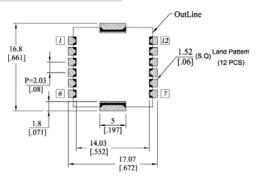
PLA Series Pin Assignment					
1	VCC(PLL)	3	N.C	5	VCC(VCO)
7	RF Out	9	LD	10	GND
11	GND	12	GND	All other pins are GND	

Recommended Pattern



PLA Series Pin Assignment					
1	GND	2	GND	3	GND
4	N.C	7	VCC(VCO)	10	RF Out
11	VCC (PLL)	12	Lock Detect	All other pins are GND	

Recommended Pattern



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