# 3.3V LVCMOS Surface Mount Crystal Clock Oscillator CWX823



XO

2111 Comprehensive Drive
Aurora, Illinois 60505
Phone: 630-851-4722
Fax: 630-851-5040

www.conwin.com

US Headquarters: 630-851-4722 European Headquarters: +353-61-472221 The Connor-Winfield CWX823 is a RoHS compliant 3.3V, LVCMOS, 7.5x5mm, surface mount, oscillator (XO). This fixed frequency crystal oscillator is designed for use in applications requiring high stability and low jitter. The surface mount package is designed for high-density mounting and is optimum for mass production.

1 MHz to 156.25 MHz 3.3V Operation RoHS Compliant

Features:

Frequency Tolerance: ±50ppm Temperature Range: -20 to 70°C

Low Jitter: <1 pS RMS

Tri-State Enable / Disable

Ceramic Surface Mount Package

Tape and Reel Packaging

# **Absolute Maximum Ratings**

Parameter	Minimum	Nominal	Maximum	Units	Notes
Storage Temperature	-55	-	125	°C	
Supply Voltage (Vcc)	-0.5	-	7.0	Vdc	

#### **Operating Specifications**

oporanii g opoonioanoii o					
Parameter	Minimum	Nominal	Maximum	Units	Notes
Frequency Range (Fo)	1.00	-	100.00	MHz	
Additional Frequencies Available	106.25, 12	25.00, 155.52	2 and 156.25	MHz	
Frequency Tolerance	-50	-	50	ppm	1
Operating Temperature Range	-20	-	70	°C	
Supply Voltage (Vdd)	3.63	3.30	2.97	Vdc	
Supply Current (Icc)	-	-	30	mA	

#### **Input Characteristics**

Parameter	Minimum	Nominal	Maximum	Units	Notes	
Enable Voltage - (Vih)	≥ 2.2	-	-	Vdc	2	
Disable Voltage - (Vil)	-	-	≤ 0.8	Vdc		

#### **HCMOS Output Characteristics**

Parameter	Minimum	Nominal	Maximum	Units	Notes
Load	-	-	15	рF	
Voltage High (Voh) Low (Vol)	2.97 -	-	0.33	Vdc	
Current High (loh) Low (lol)	-8 -	-	- 8	mA	
Duty Cycle at 50% of Vcc	40	50	60	%	
Rise / Fall Time 10% to 80%	-	2	6	nS	
Start-Up Time	-	-	10	mS	
Jitter (BW=10Hz to 20MH (BW=12kHz to 20MH	z) - Hz) -	-	5 1	pS RMS	

## **Package Characteristics**

Package	Hermetically	sealed	ceramic	package

#### Notes:

Inclusive of calibration @ 25°C, frequency vs temperature stability, supply voltage change, load change, shock and vibration, 10 years aging.
 Oscillator output is enabled with no connection on pad 1

#### Standard Frequencies Available (MHz)

1.544	1.8432	2.048	3.6864	4	5
6.48	10	11.0592	12	12.288	14.31818
15.36	16	16.896	19.44	20	24
24.576	25	27	29.498928	29.4912	30
32.768	33	33.33	36	40	44.736
48	49.152	50	60	66	75
80	100	106.25	125	155.52	156.25



Bulletin	Sm112
Page	1 of 2
Revision	02
Date	07 Oct 2005

Aurora, Illinois 60505

Phone: 630-851-4722 Fax: 630-851-5040

www.conwin.com

#### **Environmental Characteristics**

The specimen shall meet electrical characteristics after tested 5 cycles of -55°C / 30 minutes and +125°C / 30 minutes Temperature Cycle No bubbles appear in Flourinert (FC-43) at 125°C ±5°C for 5 minutes Hermetical Marking will withstand immersion in Isopropyl Alcohol or Trichloroethylene Solvent Resistance

Soldering

260°C max x 10 sec max x 2 times max or 230°C max x 180 sec max x 1 time General Conditions 20 to 100 sec up to 215°C, 50 sec at 215°C, then down to room temperature per 1 to 5°C / sec Typical Operation Data (Vapor phase reflow)

#### **Mechanical Characteristics**

Free Drop The specimen shall meet electrical characteristics after tested 3 times, Free Drop testing on the hard wooden board from a height of 75 cm.

The specimen shall meet electrical characteristics after tested by the following conditions: 10-55Hz 1.5mm Amplitude, 55-2000 Hz 20 G's, 2 hours for each plane Vibration

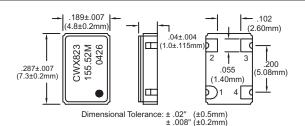
After applied Thermal Shock of 260°C max x 10 sec max x 2 times, or 230°C max x 180 sec max, the specimen shall meet electrical characteristics Thermal Shock

Solderability

(EIAJ-RCX-0102.101 Condition 1a)
Flux: MIL-F-14256 (WW Rosin=25%, Isopropyl Alcohol = 75%)
Solder: QQ-S-571 (Sn = 63%, Pb = 37%)
Solder bath temperature: 235°C ±5°C

Depth of immersion: Up to electrical terminal Immersing time: Within 2 sec ±0.5 sec into solder bath

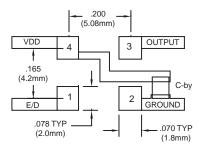
After performing the above procedures, a newly soldered coverage shall be greater than 90%



#### **Pad Connection**

- Enable / Disable
- 2. Ground
- 3: Output
- 4: Vcc

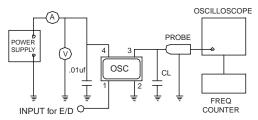
# **Suggested Pad Layout**



Bypass capacitor. C-by, should be ceramic capacitor ≥ .01uf.

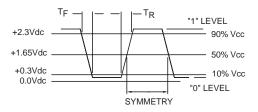
Dimensional Tolerance: ±.02" (.508mm) ±.008"(0.2mm)

#### **Test Circuit**



Enable/Disable Funct	tion Output
Pin 1 Open	Pin 3 Active
Pin 1 ≥ 2.2V	Pin 3 Active
Pin 1 ≤ 0.8V	Pin 3 High Impedence

## **Output Waveform**



### Ordering Information



Bulletin	Sm112
Page	2 of 2
Revision	02
Date	07 Oct 2005

Tape and Reel Dimensions	
8.46 DIA (216mm DIA) (2.0mm) (	2.0mm) 3.15 (5.4mm) 3.15 (80mm) 0