

Product Features

- PLL + VCO Type
- Excellent Phase Noise
- Low Spurious
- Surface Mount Package
- RF Frequency up to 5.8GHz
- 5.0V DC Power Supply
- Pb Free / RoHS Compliant

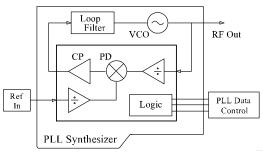
Application

- Wireless Communication System
- Base Station
- · Cable Telephone, TV
- Others Wireless Communication System

Series View

V : VCO Frequency Variable

Test Configuration



Specifications

(Operating Temperature:-30°C~+80°C)

Package Type: PS-16 / PS-26

PS-32 / PS-42

PARAMETER	SPECIFICATION	UNIT	CONDITION
Storage Temperature	-40 ~ +85	°C	
Operating Temperature	- 30 ∼ +80	°C	
Frequency	10 ~ 5800 MHz		
Supply Voltage VCC (PLL) / VCC (VCO)	3 ~ 5	V	
Reference Frequency	5 ~ 100	MHz	
Channel Step	10 ~ 10000	KHz	
Output Power	- 3 ∼ +7	dBm	
Current Consumption	40	mA / Max	
Spurious	-70	dBc / Max	
Lock Time	0.1 ~ 30	ms	
Phase Noise	-105	dBc/Hz	@ 10 KHz Offset
Output Impedance	50	Ω	

Package Dimensions

- Tel: 82-31-250-5011
- rfsales@rfhic.com

- All specifications may change without notice.
- Version 5.2

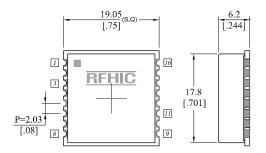
PLL Synthesizer

PLV Series



PS-16 type





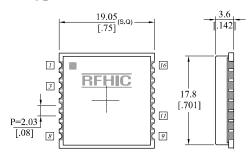
	PLV Series Pin Assignment					
1	Clock	2	Data	3	Enable	
4	Ref. IN	9	VCC(VCO)	13	RF Out	
15	VCC (PLL)	16	Lock Detect	A	All other pins are GND	

PLT Series: 4pin N.C

PLF Series: 1,2,3pin GND

PLA series: 1,2,3pin GND & 4pin N.C

PS-26 Type

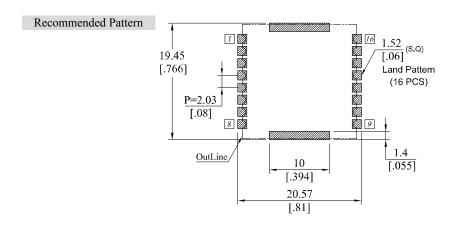


PLV Series Pin Assignment						
1	Clock	2	Data	3	Enable	
4	Ref. IN	9	VCC(VCO)	13	RF Out	
15	VCC (PLL)	16	Lock Detect	All other pins are GND		

PLT Series: 4pin N.C

PLF Series: 1,2,3pin GND

PLA series: 1,2,3pin GND & 4pin N.C

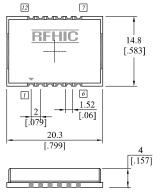


PLL Synthesizer

PLV Series



PS-32 type

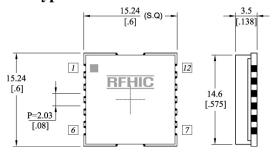


PLV Series Pin Assignment					
1	VCC(PLL)	3	Ref. IN	5	VCC(VCO)
7	RF Out	9	LD	10	Clock
11	Data	12	Enable	All other pins are GND	

OutLine 15.82 7.4 10.6 [.623] [.291] [.417] 1.52 [.06] 1.52 [.06] (s.o.) Land Pattern (12 PCS)

Recommended Pattern

PS-42 type



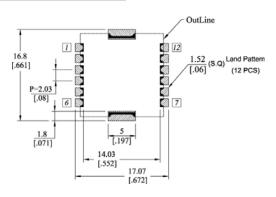
PLV-K Series Pin Assignment 1 Clock 2 Data 3 Enable 7 4 Ref. IN VCC(VCO) 10 RF Out VCC Lock All other pins 11 12 (PLL) Detect are GND

PLT Series: 4pin N.C

PLF Series: 1,2,3pin GND

PLA series: 1,2,3pin GND & 4pin N.C

Recommended Pattern



PLV-S Series Pin Assignment					
1	Clock	2	Data	3	Enable
4	Ref. IN	7	VCC(VCO)	9	RF Out
11	Lock Detect	12	VCC(PLL)	All other pins are GND	

PLT Series: 4pin N.C

PLF Series: 1,2,3pin GND

PLA series: 1,2,3pin GND & 4pin N.C

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