



1.8V HCMOS CERAMIC SMD OSCILLATOR WITH STANDBY MODEL: F510L SERIES



FEATURES

- 1.8V Operation
- HCMOS Output
- Low Power Consumption
- Standby Function
- Tape and Reel (2,000 pcs. STD)

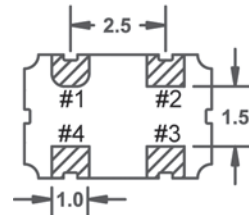
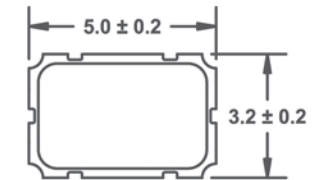


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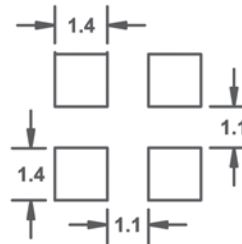
PART NUMBER SELECTION Learn More - Internet Required				
Part Number	Model Number	Frequency Stability ¹	Operating Temperature (°C)	Frequency Range (MHz)
551-Frequency-xxxxx	F510L	±100PPM	-10 ~ +70	1.800 ~ 50.000
552-Frequency-xxxxx	F510LR	±100PPM	-40 ~ +85	1.800 ~ 50.000
553-Frequency-xxxxx	F515L	±50PPM	-10 ~ +70	1.800 ~ 50.000
554-Frequency-xxxxx	F515LR	±50PPM	-40 ~ +85	1.800 ~ 50.000
555-Frequency-xxxxx	F516L	±25PPM	-10 ~ +70	1.800 ~ 50.000
556-Frequency-xxxxx	F516LR	±25PPM*	-40 ~ +85	1.800 ~ 50.000
557-Frequency-xxxxx	F518L	±20PPM*	-10 ~ +70	1.800 ~ 50.000

Learn more about:
[Part Marking Identification](#)
[Tape and Reel Specification](#)
[Mechanical Specification](#)
 Internet required

ELECTRICAL CHARACTERISTICS	
PARAMETERS	MAX (unless otherwise noted)
Frequency Range (Fo)	1.800 ~ 50.000 MHz
Storage Temperature Range (TSTG)	-55°C ~ +125°C
Supply Voltage (VDD)	1.8V ± 5%
Input Current (IDD)	
1.800 ~ 32.100 MHz	7mA
32.100+ ~ 50.000 MHz	15mA
Output Symmetry (50% VDD)	40% ~ 60%
Rise Time (20% ~ 80% VDD) (TR)	
1.800 ~ 32.100 MHz	5nS
32.100+ ~ 50.000 MHz	3.5nS
Fall Time (80% ~ 20% VDD) (TF)	
1.800 ~ 32.100 MHz	5nS
32.100+ ~ 50.000 MHz	3.5nS
Output Voltage (VOL)	20% VDD
(VOH)	80% VDD Min
Output Current (IOL)	2mA Min
(IOH)	2mA Min
Output Load (HCMOS)	15pF
Standby Current	10µA
Start-up Time (Ts)	10mS
Output Disable Time ²	300nS
Output Enable Time ²	10mS



Recommended Solder Pad Layout



Pin Connections

- #1 E/D
- #2 GND
- #3 Output
- #4 VDD

All dimensions are in millimeters.

¹ Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration. *Excludes Shock/Vibration

² An internal pullup resistor from pin 1 to pin 4 allows active output if pin 1 is left open.

All specifications subject to change without notice. Rev. 6/1/04

ENABLE / DISABLE FUNCTION	
INH (Pin 1)	OUTPUT (Pin 3)
OPEN ²	ACTIVE
'1' Level VIH ≥ 70% VDD	ACTIVE
'0' Level VIL ≤ 30% VDD	High Z