

Product Features

- PLL + VCO+TCXO Type
- Excellent Phase Noise
- Low Spurious
- Surface Mount Package
- RF Frequency up to 5.8GHz
- 5.0V DC Power Supply
- Internal Crystal Reference Clock (-30°C~+80°C:2.5PPm Tolerance)
- Pb Free / RoHS Compliant

Application

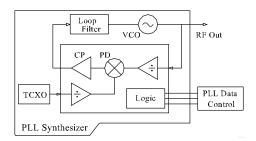
- Wireless Communication System
- Base Station
- Cable Telephone, TV
- Others Wireless Communication System



 \bullet T : TCXO included

Test Configuration





Specifications

(Operating Temperature:-30°C~+80°C)

| specifications | (Operating Temperature: -50 C-1 | | | | |
|--------------------------------------|---------------------------------|-----------|-----------------|--|--|
| PARAMETER | SPECIFICATION | UNIT | CONDITION | | |
| Storage Temperature | -40 ~ +85 | °C | | | |
| Operating Temperature | -30 ∼ +80 | °C | | | |
| Frequency | 10 ~ 5800 | MHz | | | |
| Supply Voltage VCC (PLL) / VCC (VCO) | 3 ~ 5 | V | | | |
| Reference Frequency | 5 ~ 40 | MHz | | | |
| Channel Step | 10 ~ 10000 | KHz | | | |
| Output Power | - 3 ∼ +7 | dBm | | | |
| Current Consumption | 40 | mA / Max | | | |
| Spurious | -70 | dBc / Max | | | |
| Lock Time | 0.1 ~ 30 | ms | | | |
| Phase Noise | -105 | dBc/Hz | @ 10 KHz Offset | | |
| Output Impedance | 50 | Ω | | | |

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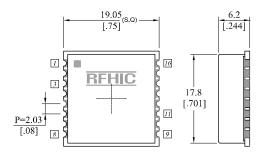
- All specifications may change without notice.
- Version 5.2



Package Dimensions

PS-16 type





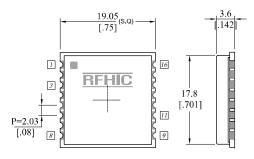
| PLV Series Pin Assignment | | | | | | |
|---------------------------|--------------|----|----------------|------------------------|--------|--|
| 1 | Clock | 2 | Data | 3 | Enable | |
| 4 | Ref. IN | 9 | VCC(VCO) | 13 | RF Out | |
| 15 | VCC (PLL) | 16 | Lock Detect | All other pins are GND | | |

PLT Series: 4pin N.C

PLF Series: 1,2,3pin GND

PLA series: 1,2,3pin GND & 4pin N.C

PS-26 Type

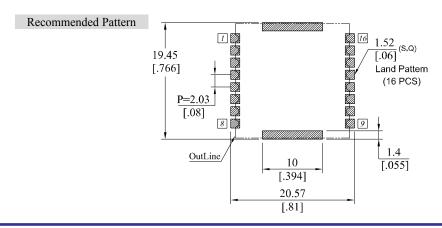


| PLV Series Pin Assignment | | | | | |
|---------------------------|--------------|----|----------------|------------------------|--------|
| 1 | Clock | 2 | Data | 3 | Enable |
| 4 | Ref. IN | 9 | VCC(VCO) | 13 | RF Out |
| 15 | VCC (PLL) | 16 | Lock Detect | All other pins are GND | |

PLT Series: 4pin N.C

PLF Series: 1,2,3pin GND

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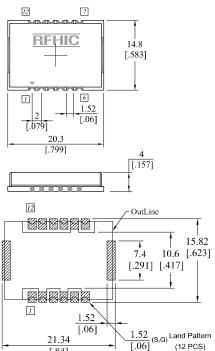
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PLL Synthesizer

PLT Series



PS-32 type

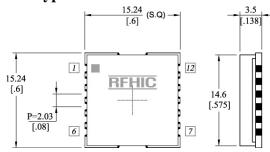


| PLV Series Pin Assignment | | | | | |
|---------------------------|----------|----|---------|------------------------|----------|
| 1 | VCC(PLL) | 3 | Ref. IN | 5 | VCC(VCO) |
| 7 | RF Out | 9 | LD | 10 | Clock |
| 11 | Data | 12 | Enable | All other pins are GND | |

Recommended Pattern

PS-42 type

Recommended Pattern



| PLV-K Series Pin Assignment | | | | | |
|-----------------------------|--------------|----|----------------|------------------------|--------|
| 1 | Clock | 2 | Data | 3 | Enable |
| 4 | Ref. IN | 7 | VCC(VCO) | 10 | RF Out |
| 11 | VCC (PLL) | 12 | Lock Detect | All other pins are GND | |

PLT Series : 4pin N.C

PLF Series: 1,2,3pin GND

PLA series: 1,2,3pin GND & 4pin N.C

16.8 [.661] 1.52 (S.Q) Land Pattern (12 PCS) [.08] 6 7

[.672]

OutLine

| PLV-S Series Pin Assignment | | | | | |
|-----------------------------|----------------|----|----------|------------------------|--------|
| 1 | Clock | 2 | Data | 3 | Enable |
| 4 | Ref. IN | 7 | VCC(VCO) | 9 | RF Out |
| 11 | Lock Detect | 12 | VCC(PLL) | All other pins are GND | |

PLT Series: 4pin N.C

PLF Series: 1,2,3pin GND

PLA series: 1,2,3pin GND & 4pin N.C

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