PLL Synthesizer

PLT Series



Product Features

- PLL + VCO+TCXO Type
- Excellent Phase Noise
- Low Spurious
- Surface Mount Package
- RF Frequencies from 10MHz to 5.8GHz
- 5.0V DC Power Supply
- Internal Crystal Reference Clock (-30°C~+80°C:2.5PPm Tolerance)
- Pb Free / RoHS Compliant

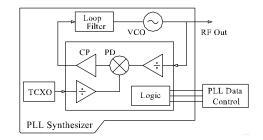
Applications

- Wireless Communication System
- Base Station
- Cable Telephone, TV
- Other Wireless Communication Systems

Series View

• T : TCXO included

Test Configuration



Specifications

PARAMETER **SPECIFICATION** UNIT CONDITION Storage Temperature $-40 \sim +85$ °C °C **Operating Temperature** $-30 \sim +80$ Frequency $10 \sim 5800$ MHz Supply Voltage VCC (PLL) / VCC (VCO) $3 \sim 5$ V Reference Frequency $5 \sim 40$ MHz Channel Step $10 \sim 10000$ KHz Output Power $-3 \sim +7$ dBm Current Consumption 40 mA / Max Spurious -70 dBc / Max Lock Time $0.1\sim 30$ ms Phase Noise -105 dBc/Hz @ 10 KHz Offset Ω Output Impedance 50

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(Operating Temperature:-30°C~+80°C)

All specifications may change without notice.

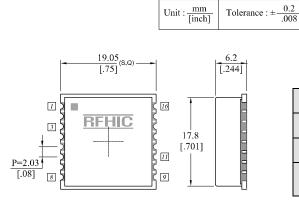
• Version 5.2

PLT Series

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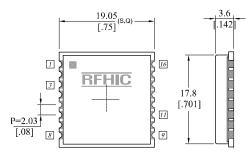
Package Dimensions

PS-16 type

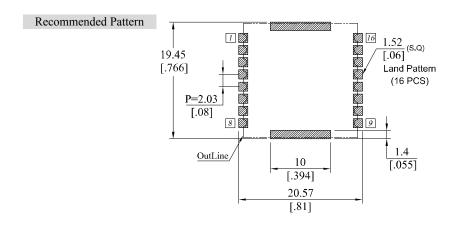


PLT Series Pin Assignment						
1	Clock	2	Data	3	Enable	
4	N.C	9	VCC(VCO)	13	RF Out	
15	VCC (PLL)	16	Lock Detect	All other pins are GND		

PS-26 Type



PLT Series Pin Assignment						
1	Clock	2	Data	3	Enable	
4	N.C	9	VCC(VCO)	13	RF Out	
15	VCC (PLL)	16	Lock Detect	All other pins are GND		



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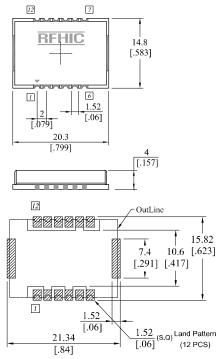
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PLL Synthesizer

PLT Series



PS-32 type



15.24 (S.Q) -

12

7

14.6 [.575]

[.6]

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3.5 [.138]

PLT Series Pin Assignment					
1	VCC(PLL)	3	N.C	5	VCC(VCO)
7	RF Out	9	LD	10	Clock
11	Data	12	Enable	All other pins are GND	

Recommended Pattern

PLT Series Pin Assignment						
1	Clock	2	Data	3	Enable	
4	N.C	7	VCC(VCO)	10	RF Out	
11	VCC (PLL)	12	Lock Detect	All other pins are GND		

P=2.03 [.08]

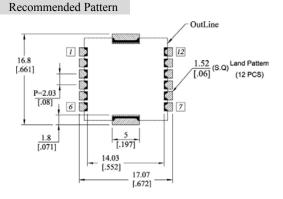
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6

PS-42 type

15.24

[.6]



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