PLL Synthesizer

PLL-D Series



Product Features

- PLL + VCO + Rom + **Divider** Type
- Excellent Phase Noise
- Low Spurious
- Surface Mount Package
- RF Frequencies from 10MHz to 5.8GHz
- 5.0V DC Power Supply
- Pb Free / RoHS Compliant
- PLV, PLT, PLF, PLA Series +D

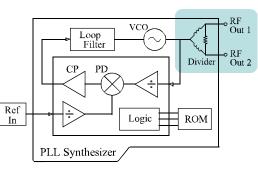
Applications

- Wireless Communication System
- Base Station
- · Cable Telephone, TV
- Other Wireless Communication Systems

Series View

- F : Fixed (Rom included)
- $\bullet D$: Divider included

Test Configuration



Specifications

•						
PARAMETER	SPECIFICATION	UNIT	CONDITION			
Storage Temperature	-40 ~ +85	°C				
Operating Temperature	-30 ~ +80	°C				
Frequency	10 ~ 5800	MHz				
Supply Voltage VCC (PLL) / VCC (VCO)	3~5	V				
Reference Frequency	5~100	MHz				
Channel Step	10 ~ 10000	KHz				
Output Power (Port1&Port2)	-3 ~ +7	dBm	1:1			
Current Consumption	40	mA / Max				
Spurious	-70	dBc / Max				
Lock Time	0.1 ~ 30	ms				
Phase Noise	-105 Тур	dBc/Hz	@ 10 KHz Offset			
Output Impedance	50	Ω				

• Tel : 82-31-250-5011

rfsales@rfhic.com

• All specifications may change without notice.

• Version 1.2



PS-32 / PS-42



(Operating Temperature:-30°C~+80°C)

PLL Synthesizer

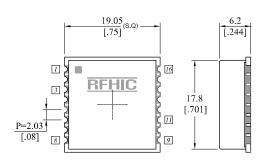
PLL-D Series



Package Dimensions

PS-16 type

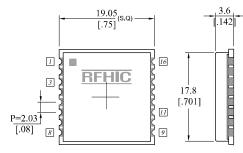




PLF-D Series Pin Assignment							
1	GND	2	GND	3	GND		
4	Ref. IN	9	VCC(VCO)	11	RF Out 1		
13	RF Out 2	15	VCC (PLL)	16	Lock Detect		

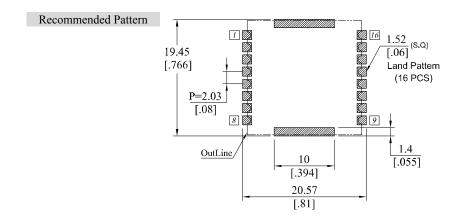
- All other pins are GND

PS-26 Type



PLF-D Series Pin Assignment							
1	GND	2	GND	3	GND		
4	Ref. IN	9	VCC(VCO)	11	RF Out 1		
13	RF Out 2	15	VCC (PLL)	16	Lock Detect		

⁻ All other pins are GND



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