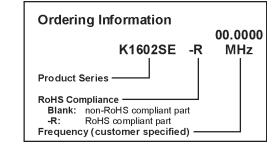
K1602SE Series 14 DIP, 5.0 Volt, Sinewave, TCVCXO

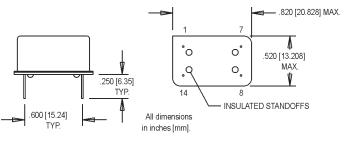






- Former Champion Product
- Phase-Locked Loops, SONET, Reference Signal, Signal Tracking, ATM





Pin Connections

PIN	FUNCTION				
1	EFC, Control Voltage				
7	Ground/Case Gnd				
8	Output				
14	+Vdd				

	<u>\</u>
	.400 [10.16] MAX.
-	.018 [0.46] DIA. TYP.
	.300 [7.62] TYP.

	PARAMETER	Symbol	Min.	Тур.	Max.	Units	Condition/Notes		
	Frequency Range	F	90		115	MHz			
	Operating Temperature	TA	-40		+85	°C			
	Storage Temperature	Ts	-40		+85	°C			
١,,	Frequency Stability	ÄF/F			±7.0	ppm	See Note 1		
ations	Aging (10 year)		-2		+2	ppm			
ati	Control Voltage	Vc	0	2.0	4	V	Positive monotonic slope		
ij.	Tuning Range		±4		±15	ppm	Vc = 0.5V to 3.5V		
Specifica	Modulation Bandwidth	fm				kHz	±3dB		
	Input Impedance	Zin	50k			Ω	@ 10kHz		
ctrical	Input Voltage	Vdd	4.75	5.0	5.25	٧			
ΙĖ	Input Current	ldd			20	mA			
<u>ĕ</u>	Output Type						Sinewave		
edElec	Load			50 Ω			See Note 2		
١"	Output Level		1.0			V p-p	Into 50 Ω		
	Output Power	Po		+2	+4	dBm	50 Ω		
	Start up Time				10	ms			
	Phase Noise (Typical)	10 Hz	100 Hz	1 kHz	10 kHz	100 kHz	Offset from carrier		
		-65	-95	-120	-140	-150	dBc/Hz		
nvironmental	Mechanical Shock	Per MIL-STD-202, Method 213, Condition C (100 g's, 6 mS duration, ½ sinewave)							
	Vibration	Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz)							
	Hermeticity	Per MIL-ST	Per MIL-STD-202, Method 112, (1x10-8 atm. cc/s of Helium)						
	Thermal Cycle	Per MIL-STD-883, Method 1010, Condition B (-55°C to +125°C, 15 min. dwell, 10 cycles)							
	Solderability	Per EIAJ-STD-002							
Ш	Soldering Conditions	+240°C max. for 10 secs.							

- 1. Inclusive of calibration, temperature, voltage, load and aging.
- 2. See load circuit diagram #8

MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.