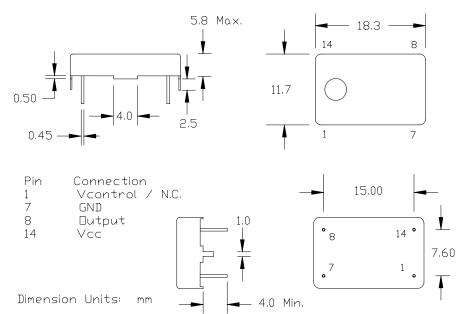




Metal Package, Full Size DIP

	I122 / I322 I121 / I321			
Frequency	1.000 MHz to 40.000 MHz	9.600 MHz to 40.000 MHz		
Output Level	HC-MOS / TTL	Clipped Sine		
Level	'0' = 0.1 Vcc Max., '1'= 0.9 Vcc Min.	1.0 V p-p Min.		
Duty Cycle	$50\% \pm 10\%$			
Rise / Fall Time	10 nS Max.			
Output Load	15 pF, Fo < 50 MHz = 10 TTL, Fo > 50 MHz = 5 LSTTL	20K Ohms / 10 pF		
Frequency Stability	See Frequency Stability Table			
Frequency Stability at +25° C	± 1 ppm (I121 and I122)			
Aging	\pm 1 ppm / Year Max.			
Supply Voltage	See Supply Voltage Table, tolerance ±5 %			
Current	20 mA Max. ***	3 mA Max.		
Control Voltage (I321 / I322)	1.65 VDC \pm 1.5 VDC, \pm 5 ppm Min. for Vcc = 3.3 VDC	$2.5 \text{ VDC} \pm 2.0 \text{ VDC}, \pm 5 \text{ ppm Min. for Vcc} = 5.0 \text{ VDC}$		
Slope	Positive			
Temperature				
Operating	See Operating Temperature Table			
Storage	-40° C to +85° C			
Environmental	See Appendix B for information			
Package Information	MSL = 2a., Termination = e1			

* I122 = HC-MOS TCXO, I121 = Clipped Sine TCXO, I322 = HC-MOS TCVCXO, I321 = Clipped Sine TCVCXO



Part Number GuideSample Part Number:I121 - 1Q3 - 20.000				
Package and Output	Operating Temperature	Frequency Stability vs. Temperature	Supply Voltage	Frequency
1121 - 1122 - 1321 - 1322 -	$7 = 0^{\circ} \text{ C}$ to $+50^{\circ} \text{ C}$	**O = ±1.5 ppm	3 = 3.3 VDC	- 20.000 MHz
	$1 = 0^{\circ} C$ to $+70^{\circ} C$	**P = ±2.0 ppm	5 = 5.0 VDC	
	$3 = -20^{\circ} \text{ C}$ to $+70 \text{ C}$	$Q = \pm 2.5 \text{ ppm}$		
	$2 = -40^{\circ} \text{ C to } +85^{\circ} \text{ C}$	$R = \pm 3.0 \text{ ppm}$		
		$J = \pm 5.0 \text{ ppm}$		

NOTE: A 0.01 µF bypass capacitor is recommended between Vcc (pin 4) and Gnd (pin 2) to minimize power supply noise.

** Not available for all temperature ranges. *** Frequency, supply, and load related parameters.

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