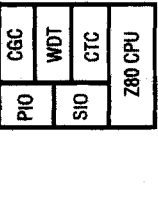


<p>BLOCK DIAGRAM</p> 	<p>Z84C00</p> <p>84C00 CPU Power Down</p> <p>OSC</p> <p>SIO</p> <p>PIO</p> <p>PIA</p> <p>CTC</p>	<p>Z84C13/Z84C15</p> <p>CTC</p> <p>SIO</p> <p>CGC</p> <p>WDT</p> <p>Z80 CPU</p>	<p>Z84C01</p> <p>84C00 CPU Power Down</p> <p>OSC</p> <p>SIO</p> <p>PIO</p> <p>PIA</p> <p>CTC</p>	<p>Z84C01</p> <p>Z80[®] CPU with Clock Generator/Clock</p> <p>CMOS: 10 MHz</p> <p>■ Clock Generator/Controller</p> <p>■ Four Power Down Modes</p>	<p>Z84C13/Z84C15</p> <p>Enhanced Intelligent Peripheral</p> <p>Z84C15 = CMOS: 6, 10 MHz Z84C15 = CMOS: 16 MHz</p>	<p>Z84C13/Z84C15</p> <p>Intelligent Peripheral Controller</p> <p>Z84C13 = CMOS: 6, 10 MHz Z84C15 = CMOS: 6, 10 MHz</p> <p>■ Serial Input/Output (SIO)</p> <p>■ Counter/Timer Circuit (CTC)</p> <p>■ Watch-Dog Timer (WDT)</p> <p>■ Clock Generator Circuit (CGC)</p> <p>■ Wait State Generator (WSG)</p> <p>■ Power-On Reset (POR)</p> <p>■ Two Chip Selects</p> <p>■ Evaluation Mode</p>	<p>Z84C30</p> <p>Killer I/O (Three Z80[®] Peripherals)</p> <p>CMOS: 8, 10, 12 MHz</p> <p>■ Serial Input/Output (SIO)</p> <p>■ Counter/Timer Circuit (CTC)</p> <p>■ Plus Eight I/O Lines</p> <p>■ Three 8-Bit Ports</p>	<p>Z84C01</p> <p>Z80[®] CPU with Clock Generator/Clock</p> <p>CMOS: 10 MHz</p> <p>■ Clock Generator/Controller</p> <p>■ Four Power Down Modes</p>	<p>Z84C01</p> <p>Z80[®] CPU with Clock Generator/Clock</p> <p>CMOS: 10 MHz</p> <p>■ Clock Generator/Controller</p> <p>■ Four Power Down Modes</p>	<p>Z84C01</p> <p>Enhanced Intelligent Peripheral</p> <p>Z84C15 = CMOS: 6, 10 MHz Z84C15 = CMOS: 16 MHz</p> <p>■ Serial Input/Output (SIO)</p> <p>■ Counter/Timer Circuit (CTC)</p> <p>■ Watch-Dog Timer (WDT)</p> <p>■ Clock Generator Circuit (CGC)</p> <p>■ Four Power-Down Modes</p> <p>■ Power-On Reset</p> <p>■ Two Chip Selects</p> <p>■ 32-Bit CRC</p> <p>■ Wait State Generator (WSG)</p> <p>■ Evaluation Mode</p>	<p>Z84C13/Z84C15</p> <p>Intelligent Peripheral Controller</p> <p>Z84C13 = CMOS: 6, 10 MHz Z84C15 = CMOS: 6, 10 MHz</p> <p>■ Serial Input/Output (SIO)</p> <p>■ Counter/Timer Circuit (CTC)</p> <p>■ Watch-Dog Timer (WDT)</p> <p>■ Clock Generator Circuit (CGC)</p> <p>■ Wait State Generator (WSG)</p> <p>■ Power-On Reset (POR)</p> <p>■ Two Chip Selects</p> <p>■ Evaluation Mode</p>	<p>Z84C30</p> <p>Killer I/O (Three Z80[®] Peripherals)</p> <p>CMOS: 8, 10, 12 MHz</p> <p>■ Serial Input/Output (SIO)</p> <p>■ Counter/Timer Circuit (CTC)</p> <p>■ Plus Eight I/O Lines</p> <p>■ Three 8-Bit Ports</p>	<p>Z84C01</p> <p>Z80[®] CPU with Clock Generator/Clock</p> <p>CMOS: 10 MHz</p> <p>■ Clock Generator/Controller</p> <p>■ Four Power Down Modes</p>	<p>Z84C01</p> <p>Z80[®] CPU with Clock Generator/Clock</p> <p>CMOS: 10 MHz</p> <p>■ Clock Generator/Controller</p> <p>■ Four Power Down Modes</p>	<p>Z84C01</p> <p>Enhanced Intelligent Peripheral</p> <p>Z84C15 = CMOS: 6, 10 MHz Z84C15 = CMOS: 16 MHz</p> <p>■ Serial Input/Output (SIO)</p> <p>■ Counter/Timer Circuit (CTC)</p> <p>■ Watch-Dog Timer (WDT)</p> <p>■ Clock Generator Circuit (CGC)</p> <p>■ Four Power-Down Modes</p> <p>■ Power-On Reset</p> <p>■ Two Chip Selects</p> <p>■ 32-Bit CRC</p> <p>■ Wait State Generator (WSG)</p> <p>■ Evaluation Mode</p>	<p>Z84C13/Z84C15</p> <p>Intelligent Peripheral Controller</p> <p>Z84C13 = CMOS: 6, 10 MHz Z84C15 = CMOS: 6, 10 MHz</p> <p>■ Serial Input/Output (SIO)</p> <p>■ Counter/Timer Circuit (CTC)</p> <p>■ Watch-Dog Timer (WDT)</p> <p>■ Clock Generator Circuit (CGC)</p> <p>■ Wait State Generator (WSG)</p> <p>■ Power-On Reset (POR)</p> <p>■ Two Chip Selects</p> <p>■ Evaluation Mode</p>	<p>Z84C30</p> <p>Killer I/O (Three Z80[®] Peripherals)</p> <p>CMOS: 8, 10, 12 MHz</p> <p>■ Serial Input/Output (SIO)</p> <p>■ Counter/Timer Circuit (CTC)</p> <p>■ Plus Eight I/O Lines</p> <p>■ Three 8-Bit Ports</p>	<p>Z84C01</p> <p>Z80[®] CPU with Clock Generator/Clock</p> <p>CMOS: 10 MHz</p> <p>■ Clock Generator/Controller</p> <p>■ Four Power Down Modes</p>	<p>Z84C01</p> <p>Z80[®] CPU with Clock Generator/Clock</p> <p>CMOS: 10 MHz</p> <p>■ Clock Generator/Controller</p> <p>■ Four Power Down Modes</p>	<p>PACKAGE</p> <p>100-Pin QFP 100-Pin VQFP</p>	<p>PACKAGE</p> <p>84-Pin PLCC</p>	<p>PACKAGE</p> <p>84-Pin PLCC 80-Pin QFP</p>	<p>PACKAGE</p> <p>44-Pin QFP 44-Pin PLCC</p>	<p>PACKAGE</p> <p>84-Pin PLCC 80-Pin QFP</p>	<p>SUPPORT PRODUCTS</p> <p>Z84C1500ZC0 - Evaluation Board</p>	<p>SUPPORT PRODUCTS</p> <p>Z84C1500ZC0 - Evaluation Board</p>	<p>SUPPORT PRODUCTS</p> <p>Z84C9000ZC0 - Evaluation Board</p>	<p>SUPPORT PRODUCTS</p> <p>Z84C9000ZC0 - Evaluation Board</p>	<p>SUPPORT PRODUCTS</p> <p>Z84C9000ZC0 - Evaluation Board</p>
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