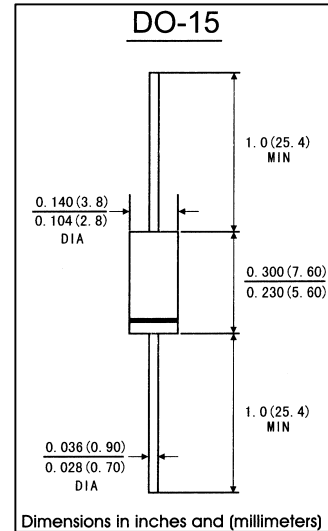


FEATURES

- . Low forward voltage drop
- . High current capability
- . High reliability
- . High surge current capability
- . Super fast recovery time
- . Good for use in switching mode circuits
- . Plastic package has Underwriters Laboratory
Flammability Classification 94V-0

MECHANICAL DATA

- . **Case:** JEDEC DO-15 molded plastic body
- . **Terminals:** plated axial leads, solderable per MIL-STD-750, method 2026
- . **Polarity:** Color band denotes cathode end
- . **Mounting Position:** Any
- . **Weight:** 0.014 ounce, 0.40 gram



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(Ratings at 25°C ambient temperature unless otherwise specified, Single phase, half wave 60Hz, resistive or inductive load. For capacitive load, derate current by 20%)

	Symbols	SF 201	SF 202	SF 203	SF 204	SF 205	SF 206	Units
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	150	200	300	400	Volts
Maximum RMS voltage	V _{RMS}	35	70	105	140	210	280	Volts
Maximum DC blocking voltage	V _{DC}	50	100	150	200	300	400	Volts
Maximum average forward rectified current 0.375"(9.5mm)lead length at T _A =55°C	I _(AV)	2.0						Amp
Peak forward surge current 8.3ms sing-wave superimposed on rated load (JEDEC method)	I _{FSM}	50.0						Amps
Maximum instantaneous forward voltage at 2.0 A	V _F	0.95			1.25			Volts
Maximum DC Reverse Current At Rated DC Blocking Voltage	I _R	5.0						μA
		50						
Maximum reverse recovery time(Note 1)	T _{rr}	35						ns
Typical junction Capacitance(Note 2)	C _J	60			30			pF
Operating junction and storage temperature range	T _J	-65 to +125						°C
	T _{STG}	-65 to +150						

Notes: 1. Test conditions: I_F=0.5A, I_R=1.0A, I_{rr}=0.25A.

2. Measured at 1MHz and applied reverse voltage of 4.0V Volts

RATINGS AND CHARACTERISTIC CURVES SF201 THRU SF206

FLG.1-TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC

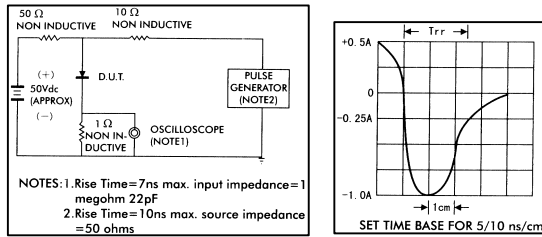


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

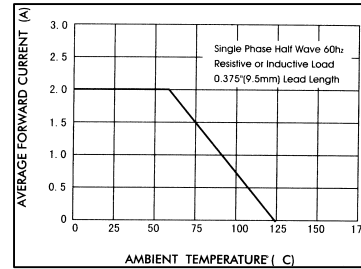


FIG.3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

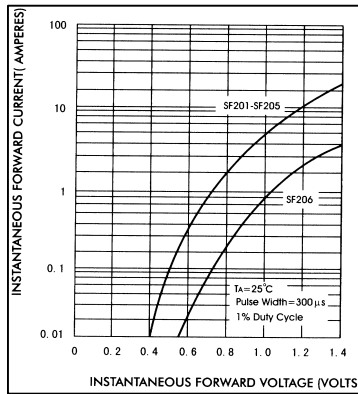


FIG.4-TYPICAL REVERSE CHARACTERISTICS

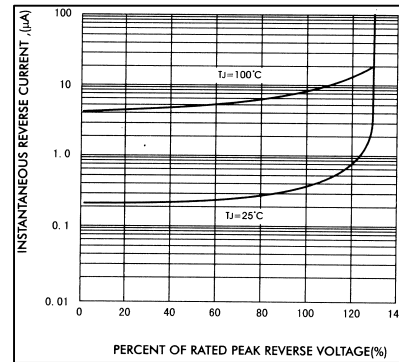


FIG.5-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

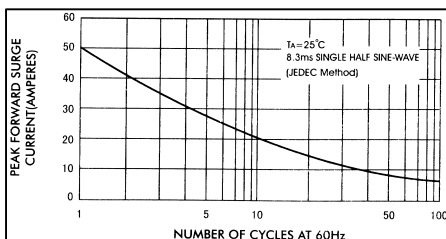


FIG.6-TYPICAL JUNCTION CAPACITANCE

