

# F100371

## Low Power Triple 4-Input Multiplexer with Enable

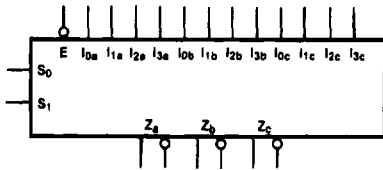
### General Description

The F100371 contains three 4-input multiplexers which share a common decoder (inputs  $S_0$  and  $S_1$ ). Output buffer gates provide true and complement outputs. A HIGH on the Enable input ( $\bar{E}$ ) forces all true outputs LOW (see Truth Table). All inputs have 50 k $\Omega$  pull-down resistors.

### Features

- 35% power reduction of the F100171
- 2000V ESD protection
- Pin/function compatible with F100171
- Voltage compensated operating range = -4.2V to -5.7V

### Logic Symbol



TL/F/10048-1

Pin Names	Description
$I_{0x}-I_{3x}$	Data Inputs
$S_0, S_1$	Select Inputs
$E$	Enable Input (Active LOW)
$Z_a-Z_c$	Data Outputs
$\bar{Z}_a-\bar{Z}_c$	Complementary Data Outputs

### Connection Diagrams

